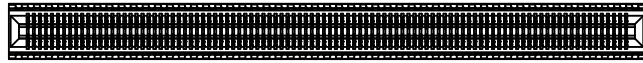
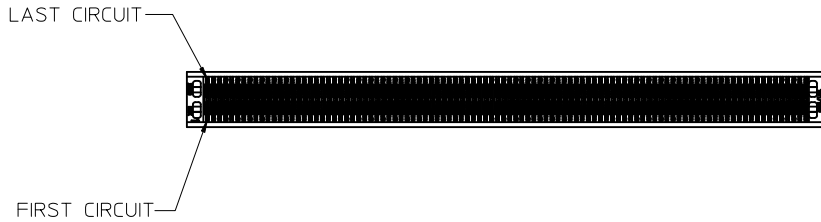
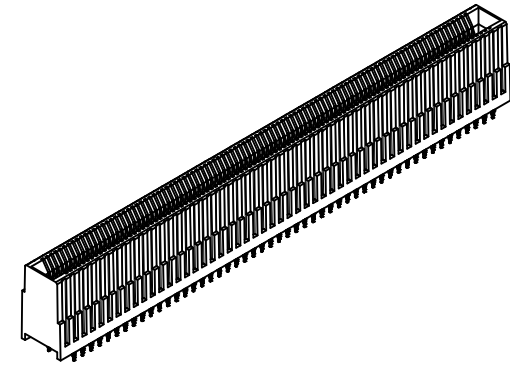
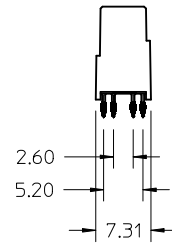
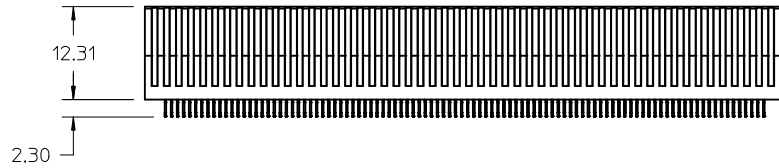


2 BAY EDGELINE



1 BAY EDGELINE

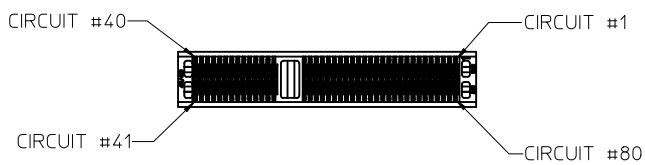
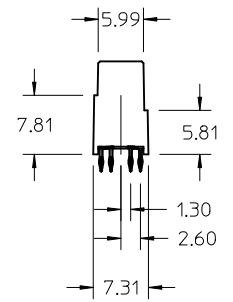
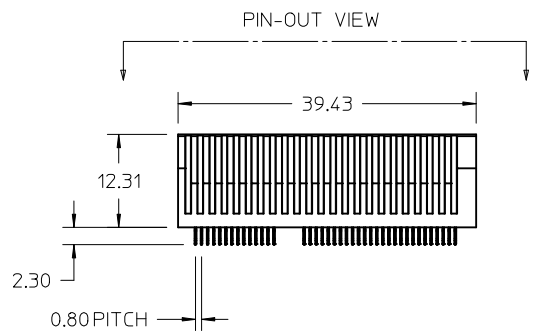
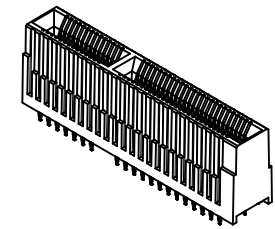
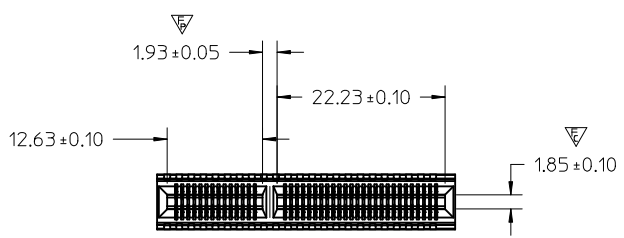


PART NUMBER	VERSION	BAY 1/BAY 2	TOTAL CIRCUITS	DIM A	SALES DRAWING (SEE NOTE 9)
76691-2278	2 BAY	143/132	278	118.63	SD-76691-2278
76691-1200	1 BAY	200	200	84.20	SD-76691-1200
76691-2108	2 BAY	86/22	108	50.63	SD-76691-2108
76691-3108	2 BAY	86/22	108	50.63	SD-76691-3108
76691-2080	2 BAY	52/28	80	39.43	SD-76691-111
76691-9046*	1 BAY	46	46	22.60	SD-76691-109
76691-1046	1 BAY	46	46	22.60	SD-76691-107

NOTES:

- MATERIAL:  
HOUSING - GLASS FILLED THERMOPLASTIC, 94-V0, BLACK  
TERMINALS - COPPER ALLOY
- FINISH:  
CONTACT AREA: HARD GOLD -0.76µm MIN OVER 3.80µm NICKEL  
COMPLIANT AREA: TIN - 0.76/1.52µm OVER NICKEL  
\* SPECIAL FINISH:  
CONTACT AREA: HARD GOLD -0.76µm MIN OVER 3.80µm NICKEL  
COMPLIANT AREA: TIN-LEAD - 0.76/1.52µm OVER NICKEL
- REFER TO PS-75594-999 PRODUCT SPECIFICATION FOR ALL ELECTRICAL, MECHANICAL AND ENVIRONMENTAL SPECIFICATIONS.
- REFER TO PK-76693-900 FOR ALL PACKAGING SPECIFICATIONS.
- PROCESSING: PRESSFIT TO PC BOARD.
- MATING PC BOARD THICKNESS = 1.60±0.16MM OVER CONTACT PADS.
- PRODUCT IS ELV AND RoHS COMPLIANT. LEVEL OF COMPLIANCE: 6/6  
ALL BANNED SUBSTANCES ARE REMOVED:  
Pb (LEAD)  
HEXAVALENT CHROMIUM (CrVI)  
CADMIUM  
MERCURY  
POLYBROMINATED BIPHENYL (PBB)  
POLYBROMINATED DIPHENYL ETHER (PBDE)
- THIS PART CONFORMS TO CLASS B REQUIREMENTS OF COSMETIC SPEC PS-45499-002
- FOR EDGE CARD AND MOUNTING PCB LAYOUT DETAIL SEE CORRESPONDING SALES DRAWING.

<b>REDRAWN DRAWING</b> IEC NO: UCP2011-2702 DRWING: DROSCA 2011/03/07 CHKD: JCOMERCI 2011/03/28 APPR: JCOMERCI 2011/03/28	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE <b>MM ONLY</b>		SCALE <b>2:1</b>	DESIGN UNITS <b>METRIC</b>	THIRD ANGLE PROJECTION	
	▽ = 0	4 PLACES ± ---	INCH ± ---	DRAWN BY DROSCA	DATE 07/21/08	<b>EDGELINE 12.5GB</b> <b>0.062"PCB 0.8MM PITCH</b>			
	▽ = 0	3 PLACES ± ---	± ---	CHECKED BY JCOMERCI	DATE 07/21/08				
	▽ = 0	2 PLACES ± 0.13	± ---	APPROVED BY JCOMERCI	DATE 2010/10/25	<b>MOLEX INCORPORATED</b> MATERIAL NO. SEE TABLE DOCUMENT NO. SD-76691-100 SHEET NO. 1 OF 1			
▽ = 0	1 PLACE ± 0.25	± ---	SIZE C THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION						
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS									

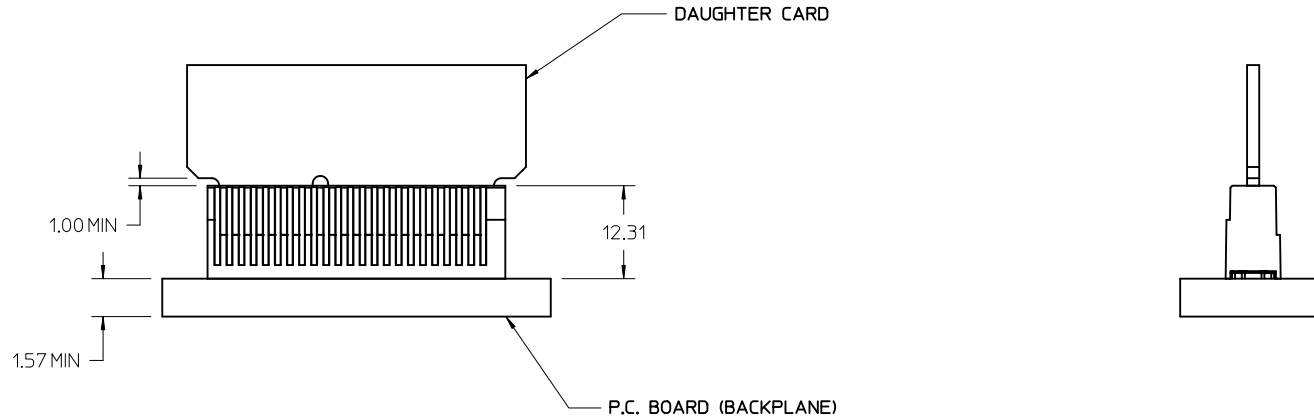
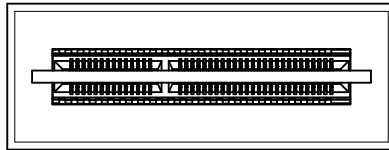


NOTES:

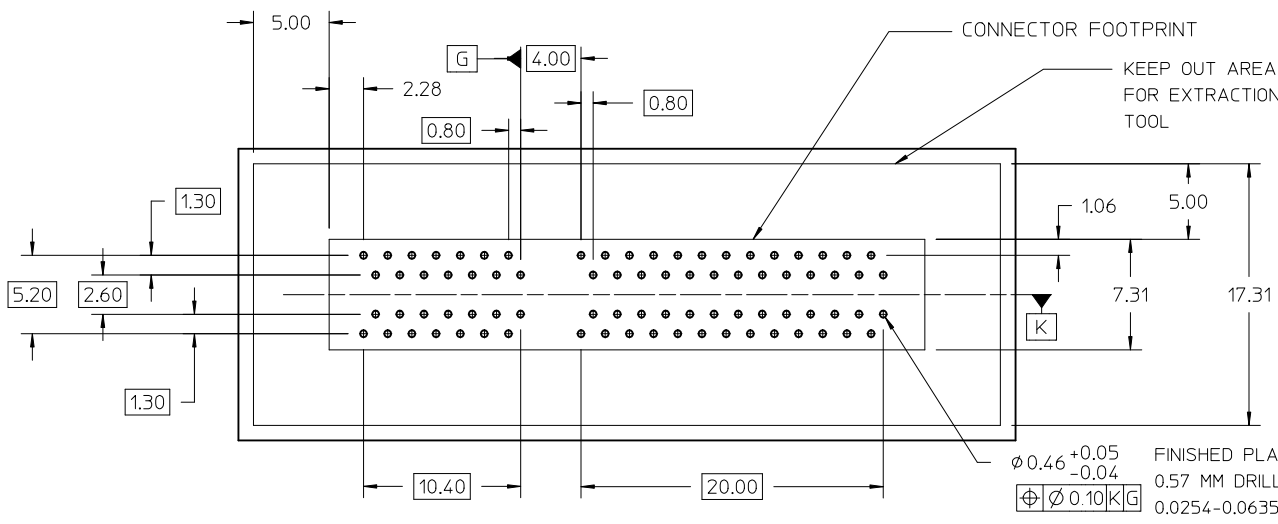
- MATERIAL:  
HOUSING - GLASS FILLED THERMOPLASTIC, 94-V0, BLACK  
TERMINALS - COPPER ALLOY
- FINISH:  
CONTACT AREA: HARD GOLD -0.76μm MIN OVER 3.80μ NICKEL  
COMPLIANT AREA: TIN - 0.76/1.52μm OVER NICKEL
- REFER TO PS-76421-001 PRODUCT SPECIFICATION FOR ALL ELECTRICAL, MECHANICAL AND ENVIRONMENTAL SPECIFICATIONS.
- REFER TO PK-75594-999 FOR ALL PACKAGING SPECIFICATIONS.
- PROCESSING: PRESSFIT TO PC BOARD.
- IN ORDER TO PROVIDE OPTIMAL ELECTRICAL PERFORMANCE, THE MATING BOARD MUST MEET REQUIREMENTS OF AMC.0 R2 SPECIFICATION.
- MATING PC BOARD THICKNESS = 1.60±0.16MM OVER CONTACT PADS.
- PRODUCT IS ELV AND RoHS COMPLIANT. LEVEL OF COMPLIANCE: 6/6 ALL BANNED SUBSTANCES ARE REMOVED:  
Pb (LEAD)  
HEXAVALENT CHROMIUM (CrVI)  
CADMIUM  
MERCURY  
POLYBROMINATED BIPHENYL (PBB)  
POLYBROMINATED DIPHENYL ETHER (PBDE)
- THIS PART CONFORMS TO CLASS B REQUIREMENTS OF COSMETIC SPEC PS-45499-002

REV A RELEASE EC NO: UCP2011-2702 DRWN: DROSCA 2011/03/23 CHKD: JCOMERCI 2011/03/28 APPR: JCOMERCI 2011/03/28	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE <b>MM ONLY</b>		SCALE <b>2:1</b>	DESIGN UNITS <b>METRIC</b>	THIRD ANGLE PROJECTION	
	▽ = 0	mm	INCH	DRAWN BY DROSCA	DATE 06/15/2009	TITLE <b>EDGE LINE 12.5GB 80CKTS 0.062"PCB 0.8MM PITCH</b>			
	▽ = 1	4 PLACES ± ---	± ---	CHECKED BY JCOMERCI	DATE 06/15/2009				
	▽ = 1	3 PLACES ± ---	± ---	APPROVED BY JCOMERCI	DATE 06/15/2009	MATERIAL NO. <b>766912080</b>		DOCUMENT NO. <b>SD-76691-111</b>	
▽ = 1	2 PLACES ± 0.15	± ---	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SIZE C	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			
▽ = 1	1 PLACE ± 0.25	± ---				SHEET NO. 1 OF 5			

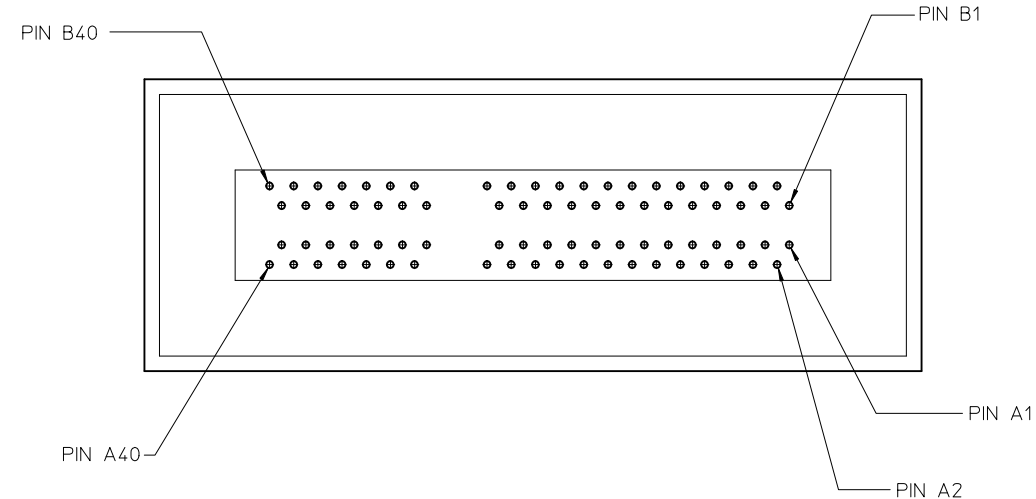
# P.C. BOARD MOUNTING



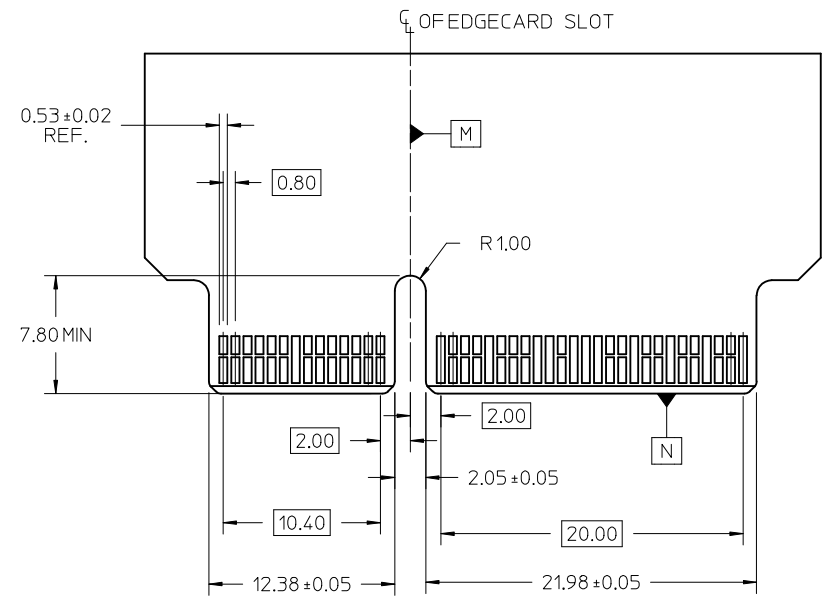
SEE SHEET 1 EC NO: UCP2011-2702 DRWING: DROSCA 2011/03/23 CHKD: JCOMERCI 2011/03/28 APPR: JCOMERCI 2011/03/28	QUALITY SYMBOLS	▽=0 ▽=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE	SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION		
			mm	INCH	DRAWN BY	DATE	2:1	METRIC	EDGELINE 12.5GB 80CKTS 0.062"PCB 0.8MM PITCH	
			± ---	± ---	DROSCA	06/15/2009	MOLEX INCORPORATED			
	4 PLACES		± ---	± ---	CHECKED BY	DATE			DOCUMENT NO.	SHEET NO.
3 PLACES	± ---	± ---	JCOMERCI	06/15/2009	SD-76691-111		2 OF 5			
2 PLACES	± 0.15	± ---	APPROVED BY	DATE	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					
1 PLACE	± 0.25	± ---	JCOMERCI	06/15/2009						
			ANGULAR ±1/2°		SEE SHEET 1					
			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS							



$\varnothing 0.46^{+0.05}_{-0.04}$  FINISHED PLATED THRU HOLE SIZE  
 $\varnothing 0.57$  MM DRILLED HOLE SIZE  
 $\varnothing 0.10$  K G 0.0254-0.0635 ELECTRO DEPOSITED COPPER



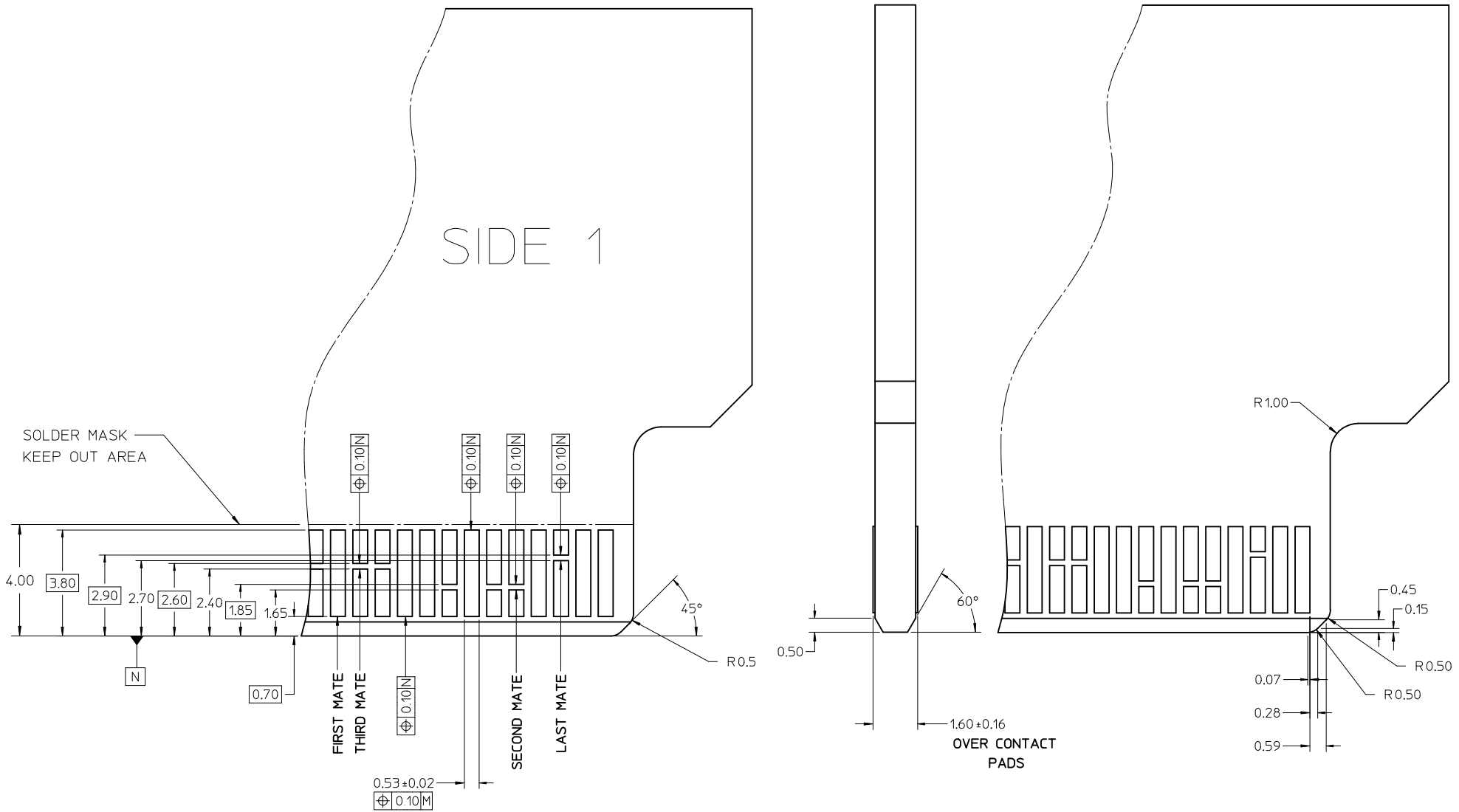
MOTHER BOARD HOLE LAYOUT  
(COMPONENT SIDE)



DAUGHTER CARD

SEE SHEET 1 IEC NO: UCP2011-2702 DRAWN BY: DROSCA 2011/03/23 CHECKED BY: JCOMERCI 2011/03/28 APPR: JCOMERCI 2011/03/28	QUALITY SYMBOLS ▽=0 ▽=0	GENERAL TOLERANCES (UNLESS SPECIFIED) <table border="1"> <thead> <tr> <th></th> <th>mm</th> <th>INCH</th> </tr> </thead> <tbody> <tr> <td>4 PLACES</td> <td>± 0.15</td> <td>± 0.006</td> </tr> <tr> <td>3 PLACES</td> <td>± 0.25</td> <td>± 0.010</td> </tr> <tr> <td>2 PLACES</td> <td>± 0.38</td> <td>± 0.015</td> </tr> <tr> <td>1 PLACE</td> <td>± 0.51</td> <td>± 0.020</td> </tr> </tbody> </table>		mm	INCH	4 PLACES	± 0.15	± 0.006	3 PLACES	± 0.25	± 0.010	2 PLACES	± 0.38	± 0.015	1 PLACE	± 0.51	± 0.020	DIMENSION STYLE <b>MM ONLY</b> DRAWN BY DATE DROSCA 06/15/2009 CHECKED BY DATE JCOMERCI 06/15/2009 APPROVED BY DATE JCOMERCI 06/15/2009	SCALE <b>4:1</b> DESIGN UNITS <b>METRIC</b> THIRD ANGLE PROJECTION	TITLE <b>EDGE LINE 12.5GB 80CKTS          0.062"PCB 0.8MM PITCH</b>
		mm	INCH																	
	4 PLACES	± 0.15	± 0.006																	
	3 PLACES	± 0.25	± 0.010																	
	2 PLACES	± 0.38	± 0.015																	
1 PLACE	± 0.51	± 0.020																		
MATERIAL NO. <b>SEE SHEET 1</b>	DOCUMENT NO. <b>SD-76691-111</b>	SHEET NO. <b>3 OF 5</b>																		
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS			THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION																	
			MOLEX INCORPORATED																	

# MODULE EDGE CARD CONTACT DETAIL



Note:  
 These are generic details that describe the configuration of various PCB design elements. The customer must determine where and when to use each element to accommodate their specific application.'

SEE SHEET 1 EC NO: UCP2011-2702 DRAWN: DROSCA 2011/03/23 CHKD: JCOMERC I 2011/03/28 APPR: JCOMERC I 2011/03/28	QUALITY SYMBOLS ▽=0 ▽=0	GENERAL TOLERANCES (UNLESS SPECIFIED) <table border="1"> <tr> <th></th> <th>mm</th> <th>INCH</th> </tr> <tr> <td>4 PLACES</td> <td>± ---</td> <td>± ---</td> </tr> <tr> <td>3 PLACES</td> <td>± ---</td> <td>± ---</td> </tr> <tr> <td>2 PLACES</td> <td>± 0.15</td> <td>± ---</td> </tr> <tr> <td>1 PLACE</td> <td>± 0.25</td> <td>± ---</td> </tr> </table> ANGULAR ±1/2°		mm	INCH	4 PLACES	± ---	± ---	3 PLACES	± ---	± ---	2 PLACES	± 0.15	± ---	1 PLACE	± 0.25	± ---	DIMENSION STYLE <b>MM ONLY</b> DRAWN BY DATE DROSCA 06/15/2009 CHECKED BY DATE JCOMERC I 06/15/2009 APPROVED BY DATE JCOMERC I 06/15/2009 MATERIAL NO.	SCALE <b>10:1</b> DESIGN UNITS METRIC THIRD ANGLE PROJECTION	TITLE <b>EDGELINE 12.5GB 80CKTS                  0.062"PCB 0.8MM PITCH</b>
		mm	INCH																	
4 PLACES	± ---	± ---																		
3 PLACES	± ---	± ---																		
2 PLACES	± 0.15	± ---																		
1 PLACE	± 0.25	± ---																		
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	SEE SHEET 1	MOLEX INCORPORATED SD-76691-111	SHEET NO. 4 OF 5																	

76691-2080 LEGEND 80 CIRCUITS / 0.062"PCB

COMPONENT SIDE 1

COMPONENT SIDE 2

PIN NO.	SIGNAL	ALLEGRO PIN NO.				SIGNAL	PIN NO.
B1	GND		○		○	GND	A1
B2	B_TP+	○			○	A_TP+	A2
B3	B_TP-		○		○	A_TP-	A3
B4	GND	○			○	GND	A4
B5	GND		○		○	GND	A5
B6	B_RP+	○			○	A_RP+	A6
B7	B_RP-		○		○	A_RP-	A7
B8	GND	○			○	GND	A8
B9	GND		○		○	GND	A9
B10	B_TS+	○			○	A_TS+	A10
B11	B_TS-		○		○	A_TS-	A11
B12	GND	○			○	GND	A12
B13	GND		○		○	GND	A13
B14	B_RS+	○			○	A_RS+	A14
B15	B_RS-		○		○	A_RS-	A15
B16	GND	○			○	GND	A16
B17	GND		○		○	GND	A17
B18	B_PRSNT	○			○	A_PRSNT	A18
B19	GND		○		○	GND	A19
B20	B_RDY	○			○	A_RDY	A20
B21	GND		○		○	GND	A21
B22	B_5V_PRE	○			○	A_5V_PRE	A22
B23	B_5V		○		○	A_5V	A23
B24	B_5V	○			○	A_5V	A24
B25	B_5V		○		○	A_5V	A25
B26	B_5V	○			○	A_5V	A26

POLARIZATION KEY

B27	GND		○		○	GND	A27
B28	GND	○			○	GND	A28
B29	GND		○		○	GND	A29
B30	GND	○			○	GND	A30
B31	B_12V_PRE		○		○	A_12V_PRE	A31
B32	B_12V	○			○	A_12V	A32
B33	B_12V		○		○	A_12V	A33
B34	B_12V	○			○	A_12V	A34
B35	B_12V		○		○	A_12V	A35
B36	GND	○			○	GND	A36
B37	GND		○		○	GND	A37
B38	GND	○			○	GND	A38
B39	GND		○		○	GND	A39
B40	GND	○			○	GND	A40

SEE SHEET 1  
EC NO: UCP2011-2702  
DRAWN: DROSCA 2011/03/23  
CHKD: JCOMERC I 2011/03/28  
APPR: JCOMERC I 2011/03/28

REV	DESCRIPTION
A	

QUALITY SYMBOLS  
▽=0  
▽=0

GENERAL TOLERANCES (UNLESS SPECIFIED)

	mm	INCH
4 PLACES	± .005	± .0002
3 PLACES	± .005	± .0002
2 PLACES	± 0.15	± .005
1 PLACE	± 0.25	± .010

ANGULAR ±1/2°

DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS

DIMENSION STYLE		SCALE		DESIGN UNITS		THIRD ANGLE PROJECTION	
MM ONLY		2:1		METRIC			
DRAWN BY	DATE	TITLE	EDGE LINE 12.5GB 80CKTS 0.062"PCB 0.8MM PITCH				
DROSCA	06/15/2009						
CHECKED BY	DATE						
JCOMERC I	06/15/2009						
APPROVED BY	DATE						
JCOMERC I	06/15/2009						
MATERIAL NO.	SEE SHEET 1						

MOLEX INCORPORATED		SHEET NO.
DOCUMENT NO. SD-76691-111		5 OF 5

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