

32-bit Microcontrollers

Kinetis K20 Family

Low-power MCUs with USB On-The-Go

Target Applications

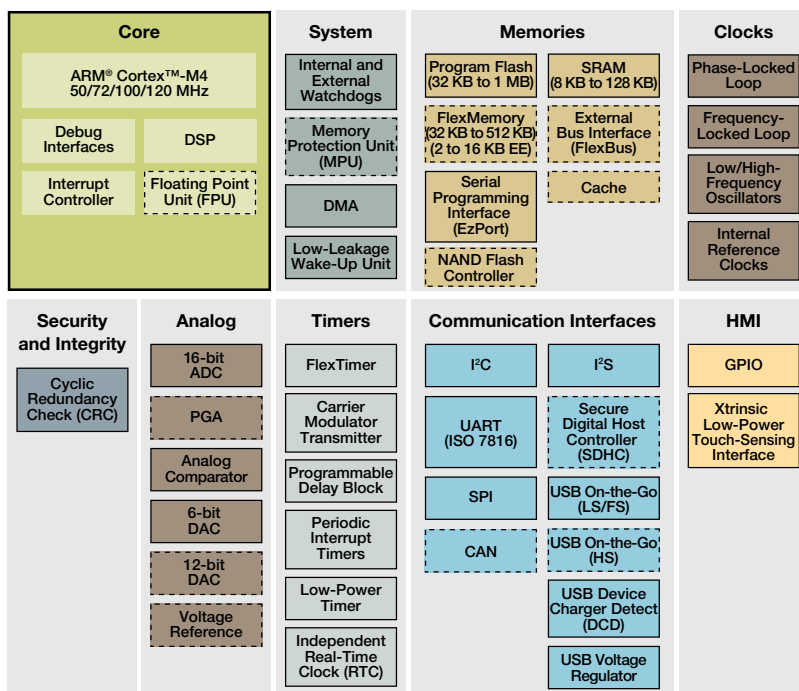
- Barcode scanners
- Portable media players
- Printers
- Programmable logic controllers

Overview

The Kinetis MCU portfolio consists of multiple pin-, peripheral- and software-compatible MCU families based on the ARM® Cortex™-M4 core. Families are built from innovative 90 nm thin-film storage (TFS) flash technology with unique FlexMemory (EEPROM) capability, and offer industry-leading low-power and mixed-signal analog integration.

The K20 MCU family is pin-, peripheral- and software-compatible with the K10 MCU family and adds Full- and High-speed USB 2.0 On-The-Go with device charger detect capability. Devices start from 32 KB of flash in 5x5 mm 32 QFN packages extending up to 1 MB in a 144 MAPBGA package with a rich suite of analog, communication, timing and control peripherals. High memory density K20 family devices include a single precision floating point unit and NAND flash controller.

Kinetis K20 Family



□ Standard Feature □ Optional Feature



One-Stop Enablement Offering—MCU + IDE + RTOS

Freescale Tower System hardware development environment:

- Integrated development environments
 - Eclipse-based CodeWarrior V10.x IDE and Processor Expert
 - IAR Embedded Workbench
 - Keil MDK
 - CodeSourcery Sourcery G++ (GNU)
- Runtime software and RTOS
 - Math, DSP and encryption libraries
 - Motor control libraries
 - Complimentary bootloaders (USB, Ethernet, RF, serial)
 - Complimentary Freescale embedded GUI
 - Complimentary Freescale MQX™
 - Cost-effective Nano™ SSL/Nano™ SSH for Freescale MQX RTOS
 - Micrium uC/OS-III
 - Express Logic ThreadX
 - SEGGER embOS
 - freeRTOS
 - Mocana (security)
- Full ARM ecosystem

Features	Benefits
<ul style="list-style-type: none"> • ARM® Cortex™-M4 core with DSP instruction support and optional single precision floating point unit • Up to 32-channel DMA. Up to 16 KB of cache. Cross bar switch 	<ul style="list-style-type: none"> • Up to 120 MHz core supporting a broad range of processing bandwidth needs • Peripheral and memory servicing with reduced CPU loading. Optimized bus bandwidth and flash execution performance. Concurrent multi-master bus accesses for increased bus bandwidth
<ul style="list-style-type: none"> • USB On-The-Go (Full- and High-Speed) with device charger detect 	<ul style="list-style-type: none"> • Optimized charging current/time for portable USB devices, enabling longer battery life. USB low-voltage regulator supplies up to 120 mA off chip at 3.3V to power external components from 5V input
<ul style="list-style-type: none"> • Memory protection unit • Hardware cyclic redundancy check engine • Independent-clocked COP. External watchdog monitor 	<ul style="list-style-type: none"> • Provides memory protection for all cross bar switch masters, increasing software reliability • Validates memory contents and communication data, increasing system reliability • Prevents code runaway in fail-safe applications. Drives output pin to safe state external components if watchdog event occurs
<ul style="list-style-type: none"> • Up to four FlexTimers with up to 20 channels • Carrier modulator transmitter • 4-channel, 32-bit periodic interrupt 	<ul style="list-style-type: none"> • General purpose timers with hardware dead-time insertion and quadrature decoding for motor control • Infrared waveform generation for remote control applications • Time base generation for RTOS task scheduler or trigger source for ADC conversion and programmable delay block
<ul style="list-style-type: none"> • FlexBus external bus interface • Secure digital host controller • NAND flash controller 	<ul style="list-style-type: none"> • Enables the connection of external memories and peripherals (e.g., graphics displays) • Connection to SD, SDIO, MMC or CE-ATA cards for in-application software upgrades, file systems or adding Wi-Fi® or Bluetooth® support • Supports up to 32-bit ECC current and future NAND types with minimal software overhead
<ul style="list-style-type: none"> • 32 KB–1 MB flash. Up to 128 KB of SRAM • 32 KB–512 KB FlexMemory 	<ul style="list-style-type: none"> • High reliability, fast access program memory with 4-level security protection. Independent flash banks allow concurrent code execution and firmware updating • FlexMemory provides 32B–16 KB of user-segmentable byte write/erase EEPROM. In addition, FlexNVM from 32 KB–512 KB for extra program code, data or EEPROM backup

K20 Family Options

Part Number	Memory					Feature Options								Other	Packages													
	CPU (MHz)	Flash (KB)	Flex NVM (KB)	SRAM (KB)	Cache (KB)	Single Precision Floating Point Unit	Memory Protection Unit	CAN	Secure Digital Host Controller	NAND Flash Controller	External Bus Interface	12-bit DAC	Prog. Gain Amplifier		5V Tolerant I/O	FM	FT	LF	EX	LH	LK	MB	LL	ML	MC	LQ	MD	
																32QFN (5x5)	48QFN (7x7)	48LQFP (7x7)	64QFN (9x9)	64LQFP (10x10)	80LQFP (12x12)	81BGA (8x8)	100LQFP (14x14)	104BGA (8x8)	121BGA (8x8)	144LQFP (20x20)	144BGA (13x13)	
MK20DN32Vyy5	50	32		8											USB OTG (FS)	√	√	√	√	√								
MK20DN64Vyy5	50	64		16											USB OTG (FS)	√	√	√	√	√								
MK20DN128Vyy5	50	128		16											USB OTG (FS)	√	√	√	√	√								
MK20DN512Vyy10	100	512		128		√	√	√	√	√	√	√	√	√	USB OTG (FS)						√	√	√		√	√	√	
MK20FN1M0Vyy12	120	1 MB		128	16	√	√	√	√	√	√	√	√	√	USB OTG (FS/HS)											√	√	
MK20DX32Vyy5	50	32	32	8											USB OTG (FS)	√	√	√	√	√								
MK20DX64Vyy5	50	64	32	16											USB OTG (FS)	√	√	√	√	√								
MK20DX128Vyy5	50	128	32	16											USB OTG (FS)	√	√	√	√	√								
MK20DX64Vyy7	72	64	32	16				√			√	√	√	√	USB OTG (FS)				√	√	√	√						
MK20DX128Vyy7	72	128	32	32				√			√	√	√	√	USB OTG (FS)				√	√	√	√	√					
MK20DX256Vyy7	72	256	32	64				√			√	√	√	√	USB OTG (FS)				√	√	√	√	√	√				
MK20DX128Vyy10	100	128	128	32		√	√	√			√	√	√	√	USB OTG (FS)											√	√	
MK20DX256Vyy10	100	256	256	64		√	√	√			√	√	√	√	USB OTG (FS)						√	√	√		√	√	√	
MK20FX512Vyy12	120	512	512	128	16	√	√	√	√	√	√	√	√	√	USB OTG (FS/HS)											√	√	

yy = package designator

For current information about Kinetis products and documentation, please visit freescale.com/Kinetis

Freescale, the Freescale logo and CodeWarrior are trademarks of Freescale Semiconductor, Inc., Reg. U.S. Pat. & Tm. Off. Kinetis, Processor Expert and Xtrinsic are trademarks of Freescale Semiconductor, Inc. ARM is the registered trademark of ARM Limited. ARM Cortex-M4 is the trademark of ARM Limited. All other product or service names are the property of their respective owners. © 2011 Freescale Semiconductor, Inc.

Doc Number: KNTSK20FMYLFS REV 5

