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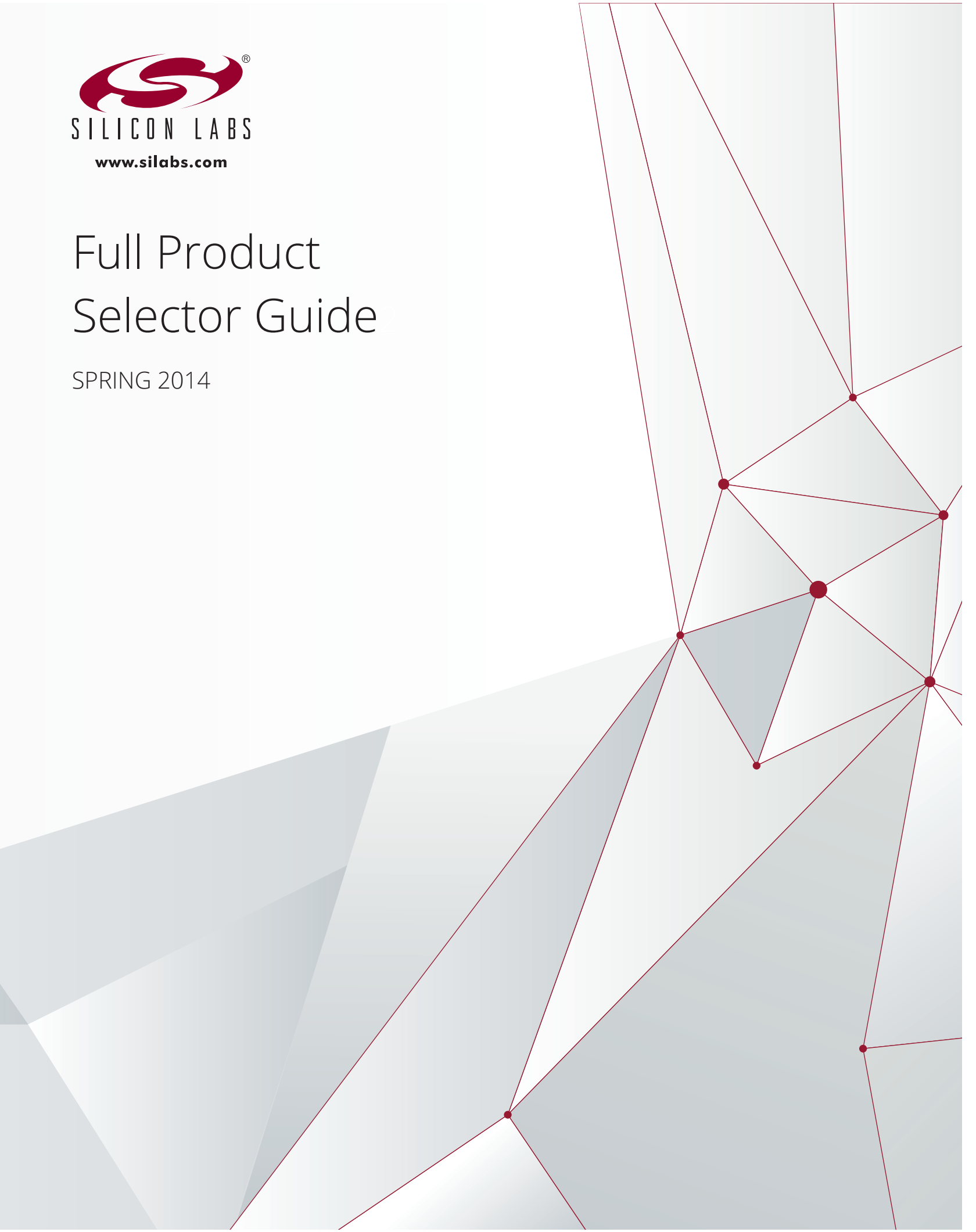
Full Product Selector Guide

SPRING 2014



Hardware and Software Support

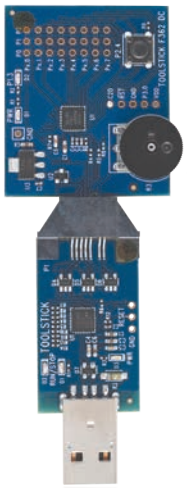
FIND THE TOOLS YOU NEED TO HELP WITH YOUR ENTIRE PROJECT www.silabs.com

Development Support

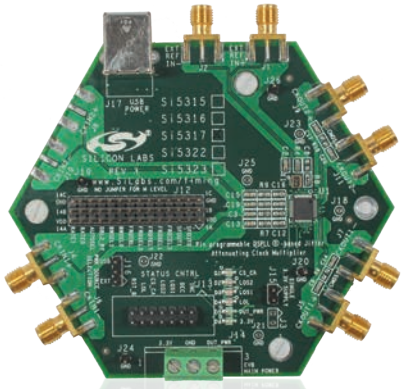
Silicon Labs offers complete tools to help designers throughout the entire project. 8-bit, 32-bit microcontroller and EZRadioPRO® wireless solutions offer hardware and software platforms to easily set up and configure, compile and debug a project. Full documentation and a broad range of third-party compilers and development tools are available. Software stacks provide networking support for multi-node metering networks. Software simulation tools can estimate power consumption and determine expected battery life.
www.silabs.com/devkits

Complete development system includes the following:

- Prototyping/demonstration board
- **Simplicity Studio:**
  - USB adapter for in-system programming and debugging
  - Silicon Laboratories IDE
  - MCU configuration wizard

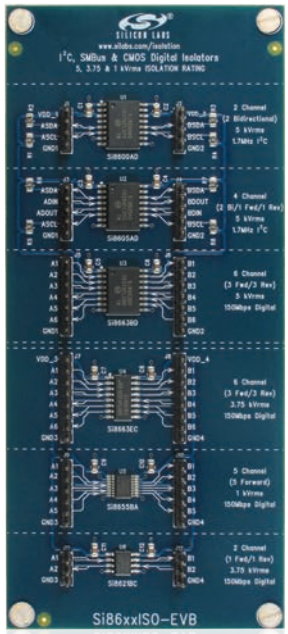


MCU USB TOOLSTICK

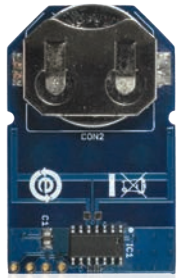


Si5317 JITTER ATTENUATING CLOCK EVALUATION BOARD

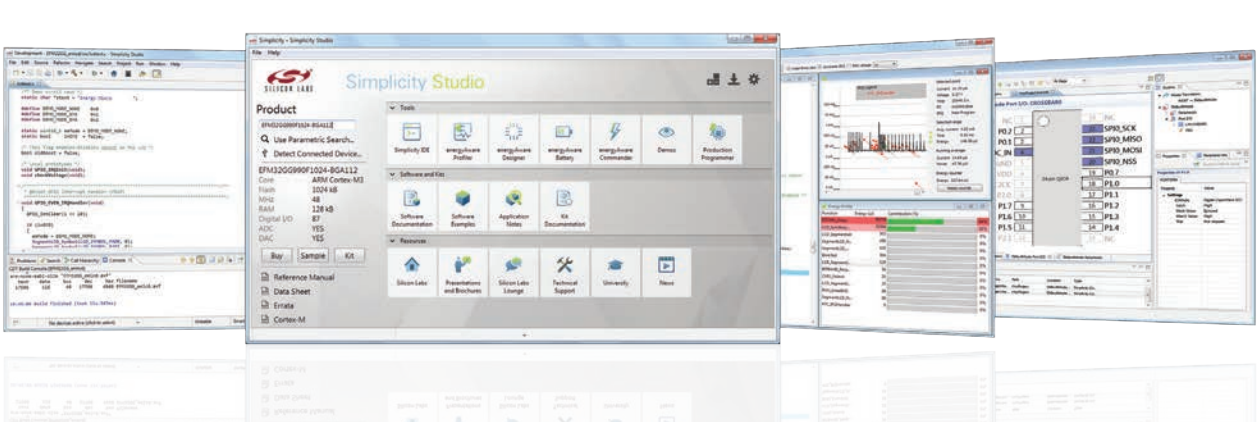
Si2185 TV RECEIVER EVALUATION BOARD



Si86xxISO-KIT ISOLATION EVALUATION BOARD



Si4010 EZRadio® KEY FOB TRANSMITTER



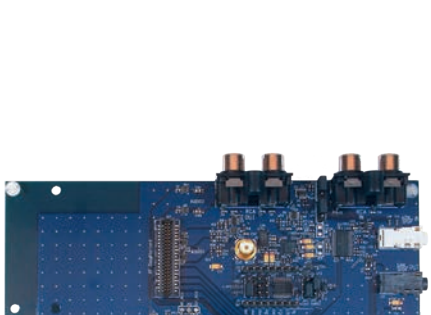
Simplicity Studio

Silicon Labs' EFM32™ 32-bit and 8051 8-bit MCUs are supported by Simplicity Studio — a complimentary software suite that provides instant, one-click access to all your IDE, EFM32 tools, software, code examples, news, documents and resources. www.silabs.com/simplicity-studio

- **Always updated:** Automatically keeps you up-to-date with changes affecting your development environment.
- **One-click accessibility:** Instant access to tools, relevant documentation, software and source code libraries.
- **World-class simplicity:** Includes a product selector to help you speed up the MCU selection process.

Si7013 RH&T
SENSOR USB DONGLE

EFM32™ ZERO GECKO
STARTER KIT



Si47xx MULTI-BAND
RADIO RECEIVER
EVALUATION BOARD

CP2108 USB
TO QUAD UART
EVALUATION BOARD

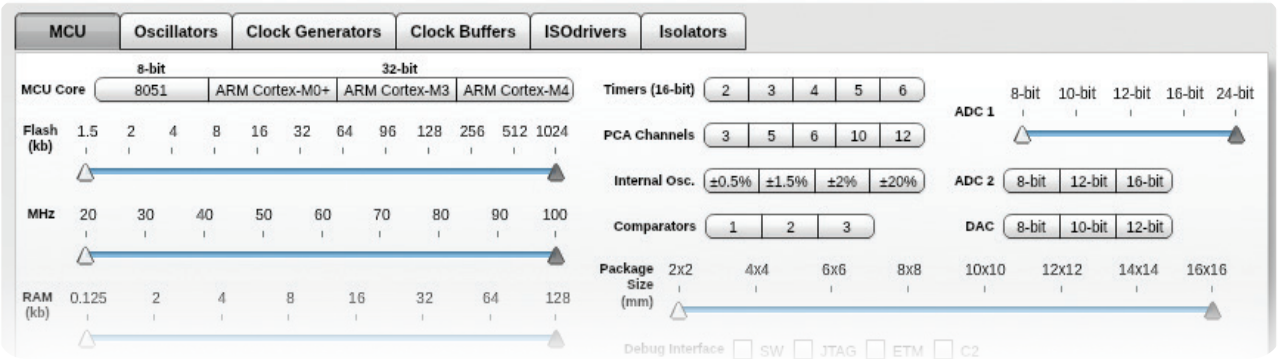
Silicon Labs Makes Finding the Right Part Easy!

Buy or Sample Mixed-Signal ICs

QUICKLY BUY OR SAMPLE PRODUCTS ON OUR WEBSITE AT www.silabs.com/buy

Parametric Searches

Silicon Labs offers easy-to-use parametric searches for MCU, Isolators, ISOdrivers, Oscillators, Clock Generators and Buffer products. Click the buttons to filter as you search for the features you require and find the perfect part to meet your needs. You can then buy or sample parts or export your results into a sortable Excel spreadsheet. www.silabs.com/parametric-search



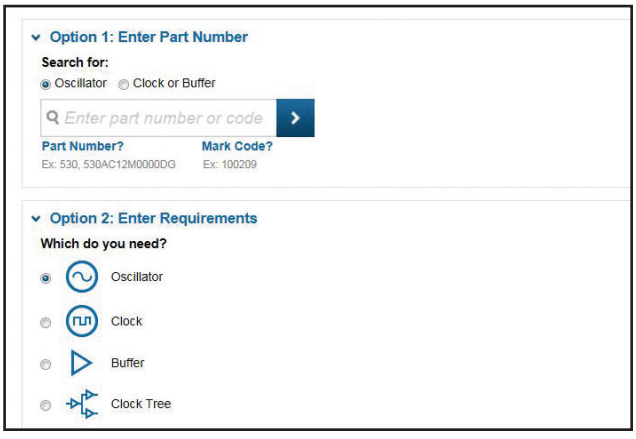
Parametric Search iPad App

Take the parametric search mobile! The Silicon Labs Parametric Search iPad app makes it easy to find exactly what you need for your next embedded design. Quickly jump between microcontroller, clock, oscillator, digital isolator, and isolated gate driver product families. Filter results using common technical and application requirements. Access data sheets and other documentation directly in the app and download to iBooks for offline access. Browse detailed product information – features, applications, block diagrams and even order samples and development kits, all from within the app. Offline access available—refresh data the next time you're connected to the Internet. www.silabs.com/parametric-search

Available on the App Store

Custom Clock and Oscillator Design Services

Silicon Labs offers the industry's broadest portfolio of embedded clocks and oscillators for communications, computing, broadcast video and consumer applications with the shortest lead times in the industry, with no minimum order quantities or NRE fees. Our timing IC portfolio leverages patented technologies to eliminate expensive discrete components while improving performance, minimizing board space and simplifying system design. To the right are three services we offer to help make your next project easier. www.silabs.com/custom-timing



Product Selector Tables

Audio Products

REQUEST SAMPLES AND DOWNLOAD DOCUMENTATION AT www.silabs.com/audio

FM/AM Multi-band Radio Receiver Solutions

PART NUMBER	DESCRIPTION	RDS SUPPORT	DIGITAL I/O
Si4682/4/8	FM/HD/DAB/DAB+ digital radio receivers with RDS	Si4682/4/8	Si4682/4/8
Si4702/03	FM radio receivers with RDS	Si4703	
Si4704/05[1]	FM radio receiver with RDS, no external antenna required	Si4705	Si4705
Si4706	Enhanced FM RDS/TMC radio receiver with RDS, no external antenna required	Si4706	Si4706
Si4707	Weather band radio receiver with SAME decoder		
Si4708/09	World's smallest FM tuner with RDS	Si4709	
Si4710/11	FM radio transmitter with RDS and digital audio compression	Si4711	Si4711
Si4712/13	FM radio transmitter with RDS, RPS[3] and digital audio compression	Si4713	Si4713
Si4720/21	FM radio transceivers with RDS, RPS and digital audio compression	Si4721	Si4721
Si4730/31	AM/FM radio receivers with RDS	Si4731	Si4731
Si4732/34/35[1]	AM/FM/shortwave/longwave radio receivers with RDS	Si4735	Si4735
Si4736/37	AM/FM/weather band radio receivers with RDS	Si4737	Si4737[2]
Si4738/39	FM/weather band radio receivers with RDS	Si4739	Si4739[2]
Si4740/1/2/3/4/5	Multi-band receivers with RDS, automotive qualified AEC-Q100	Si4741/3/5	Si4741/3/5
Si4749	FM RDS/RBDA data receiver/alternate frequency scanner, automotive qualified AEC-Q100	Si4749	
Si4750/1/2/3/4/5/6/7	Multi-band receivers with RDS, automotive qualified AEC-Q100	Si4750/1/3/5/7	
Si4760/1/2/3	Multi-band radio receivers, HD radio tuners with RDS, automotive qualified AEC-Q100	Si4760/1/2/3	Si4760/1/2/3
Si4764/5/6/7	Multi-band phase diversity radio receivers, HD radio tuners with RDS, automotive qualified AEC-Q100	Si4764/5/6/7	Si4764/5/6/7
Si4768	High-performance FM RDS/RBDS data receiver/alternative frequency scanner with MPX output, automotive qualified AEC-Q100	Si4768	
Si4769	High-performance FM RDS/RBDS data receiver/alternative frequency scanner with MPX output/FM HD-radio tuner for HD radio data services, automotive qualified AEC-Q100	Si4769	Si4769
Si4770	High-performance AM/FM broadcast radio receiver with RDS	Si4770	Si4770
Si4777	High-performance AM/FM broadcast radio receiver and HD radio tuner with RDS	Si4777	Si4777
Si4820/24/25[1]	Entry-level mono mechanically tuned, analog display AM/FM/SW radio receiver		
Si4822/26/27[1]	Entry-level mono mechanically tuned, digital display AM/FM/SW radio receiver		
Si4831/35/36[1]	High-performance stereo mechanically tuned, analog display AM/FM/SW radio receiver		
Si4840/44[1]	High-performance stereo mechanically tuned, digital display AM/FM/SW radio receiver		

[1]Extended FM tuning range 64-108 MHz; [2]AM/FM only; [3]Receive Power Scan

Class D Audio Drivers

PART NUMBER	INPUT TYPE	ISOLATION RATING	OUTPUT	DRIVE STRENGTH	UVLO VOLTAGE	PACKAGE
Si8241BB-B-1S1	PWM	2.5 kV rms	High-Side/Low-Side	0.5 A	8 V	NB SOIC16
Si8241CB-B-1S1	PWM		High-Side/Low-Side		10 V	NB SOIC16
Si8244BB-C-1S1	PWM		High-Side/Low-Side	4 A	8 V	NB SOIC16
Si8244CB-C-1S1	PWM		High-Side/Low-Side		10 V	NB SOIC16

Clocks and Oscillators

REQUEST SAMPLES AND DOWNLOAD DOCUMENTATION AT www.silabs.com/timing

Clock Generators: www.silabs.com/clock-generators

Any-Frequency, Any-Output Differential/CMOS Clocks

PART NUMBER	CONTROL	CLOCK INPUT/ OUTPUTS	INPUT FREQUENCY (MHz)	OUTPUT FREQUENCY (MHz)	PHASE JITTER (RMS)	VDD	VDDO	OUTPUT	PACKAGE
Si5334	Pin	1/4	5 - 710 (Clock), 8 - 30 (Xtal)	0.16 - 710 MHz 0.16 - 350 MHz 0.16 - 200 MHz	1.0 ps	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS, LVDS, LVPECL, HCSL, SSTL, HSTL	QFN24
Si5335	Pin	1/4	10 - 350 (Clock), 25 - 27 (Xtal)	1 - 350 MHz	1.0 ps	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS, LVDS, LVPECL, HCSL, SSTL, HSTL, CML	QFN24
Si5338	I^2C	1/4	5 - 710 (Clock), 8 - 30 (Xtal)	0.16 - 710 MHz 0.16 - 350 MHz 0.16 - 200 MHz	1.0 ps	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS, LVDS, LVPECL, HCSL, SSTL, HSTL, CML	QFN24

Tiny CMOS Clocks

PART NUMBER	CONTROL	CLOCK INPUT/ OUTPUTS	INPUT FREQUENCY (MHz)	OUTPUT FREQUENCY (MHz)	PHASE JITTER (RMS)	VDD	VDDO	OUTPUT	PACKAGE
Si51210	Pin	1/2	3 - 166 (Clock), 8 - 48 (Xtal)	3 - 200 MHz	—	2.5 - 3.3 V	—	LVCMOS	TDFN6
Si51211	Pin	1/3	3 - 166 (Clock), 8 - 48 (Xtal)	3 - 200 MHz	—	2.5 - 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	TDFN8
Si51214	Pin	1/2	3 - 166 (Clock), 8 - 48 (Xtal)	3 - 133 MHz	—	1.8 V	—	LVCMOS	TDFN6
Si51219	Pin	1/3	3 - 166 (Clock), 8 - 48 (Xtal)	3 - 200 MHz	—	2.5 - 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	TSSOP8
Si5350A	Pin	1/3	10 - 100 (Clock)	8 kHz - 160 MHz	100 ps	2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	MSOP10
Si5350C	Pin	1/3	10 - 100 (Clock), 25/27 (Xtal)	8 kHz - 160 MHz	100 ps	2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	MSOP10
Si5351A	I^2C	1/3	25/27 (Xtal)	8 kHz - 160 MHz	100 ps	2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	MSOP10

Any-Frequency, Any-Output CMOS Clocks

PART NUMBER	CONTROL	CLOCK INPUT/ OUTPUTS	INPUT FREQUENCY (MHz)	OUTPUT FREQUENCY (MHz)	PHASE JITTER (RMS)	VDD	VDDO	OUTPUT	PACKAGE
Si5350A/51A	Pin/I^2C	1/8	25/27 (Xtal)	8 kHz - 160 MHz	100 ps	2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	QFN20
Si5350B/51B	Pin/I^2C	1/8	25/27 (Xtal)	8 kHz - 160 MHz	100 ps	2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	QFN20
Si5350C/51C	Pin/I^2C	1/8	10 - 100 (Clock), 25/27 (Xtal)	8 kHz - 160 MHz	100 ps	2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	QFN20
Si5355	Pin	1/8	5 - 200 (Clock), 25/27 (Xtal)	1 - 200 MHz	50 ps	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	QFN24

CMOS Clock Generator +VCXOs

PART NUMBER	CONTROL	CLOCK INPUT/ OUTPUTS	INPUT FREQUENCY (MHz)	OUTPUT FREQUENCY (MHz)	PHASE JITTER (RMS)	VDD	VDDO	OUTPUT	PACKAGE
Si5350B	Pin	1/3 or 8	25/27 (Xtal) VCXO	8 kHz - 160 MHz	100 ps	2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	MSOP10/QFN20
Si5351B	I^2C	1/8	25/27 (Xtal) VCXO	8 kHz - 160 MHz	100 ps	2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	QFN20
SL38000	Pin/I^2C	1/12	3 - 166 (Clock), 8 - 48 (Xtal)	3 - 200 MHz	—	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	TSSOP28
SL38160	Pin/I^2C	1/8	3 - 166 (Clock), 8 - 48 (Xtal)	3 - 200 MHz	—	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	TSSOP16

PCI Express Clock Generators (PCIe)

PART NUMBER	CONTROL	CLOCK INPUT/ OUTPUTS	INPUT FREQUENCY (MHz)	OUTPUT FREQUENCY (MHz)	PHASE JITTER (RMS)	VDD	VDDO	OUTPUT	PACKAGE
Si52142	Pin/I^2C	1/3	25 MHz	100 MHz, 25 MHz	1.0 ps	3.3 V	3.3 V	HSCL, LVCMOS	QFN24
Si52143	Pin/I^2C	1/5	25 MHz	100 MHz, 25 MHz	1.0 ps	3.3 V	3.3 V	HSCL, LVCMOS	QFN24
Si52144	Pin/I^2C	1/4	25 MHz	100 MHz	1.0 ps	3.3 V	3.3 V	HSCL	QFN24
Si52146	Pin/I^2C	1/6	25 MHz	100 MHz	1.0 ps	3.3 V	3.3 V	HSCL	QFN32

Si52147	Pin/I²C	1/9	25 MHz	100 MHz	1.0 ps	3.3 V	3.3 V	HSCL	QFN48
SL28SRC01	Pin	1/1	14.318 MHz	100 MHz	1.0 ps	3.3 V	3.3 V	HCSL	TSSOP16
SL28SRC02	Pin	1/2	14.318 MHz	100 MHz	1.0 ps	3.3 V	3.3 V	HCSL	TSSOP20
SL28SRC04	Pin	1/4	14.318 MHz	100 MHz	1.0 ps	3.3 V	3.3 V	HCSL	TSSOP24
Si5334	Pin	1/4	5 - 710 (Clock), 8 - 30 (Xtal)	0.16 - 710 MHz	1.0 ps	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS, LVDS, LVPECL, HCSL, SSTL, HSTL	QFN24
Si5335	Pin	1/4	10 - 350 (Clock), 25/27 (Xtal)	1 - 350 MHz	1.0 ps	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS, LVDS, LVPECL, HCSL, SSTL, HSTL, CML	QFN24
Si5338	I²C	1/4	5 - 710 (Clock) 8 - 30 (Xtal)	0.16 - 710 MHz	1.0 ps	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVPECL, LVDS, LVCMOS, HCSL, SSTL, HSTL	QFN24

Embedded/Intel x86 Clocks

PART NUMBER	CONTROL	CLOCK INPUT/ OUTPUTS	INPUT FREQUENCY (MHz)	OUTPUT FREQUENCY (MHz)	VDD	VDDO	OUTPUT	PACKAGE
SL28EB717	Pin/I²C	1/13	25 MHz	12 MHz, 14.318 MHz, 25 MHz, 27 MHz, 33 MHz, 48 MHz, 75 MHz, 96 MHz, 83.33 MHz-166 MHz, 100 MHz	3.3 V	3.3 V	LVCMOS, HCSL	QFN48
SL28EB719	Pin/I²C	1/13	25 MHz	12 MHz, 14.318 MHz, 25 MHz, 27 MHz, 33 MHz, 48 MHz, 75 MHz, 96 MHz, 83.33 MHz-166 MHz, 100 MHz	3.3 V	3.3 V	LVCMOS, HCSL	TSSOP48
SL28EB740	Pin/I²C	1/16	25 MHz	12 MHz, 14.318 MHz, 25 MHz, 33 MHz, 48 MHz, 75 MHz, 96 MHz, 83.33 MHz-166 MHz, 100 MHz	3.3 V	3.3 V	LVCMOS, HCSL	TSSOP56
SL28EB742	Pin/I²C	1/16	14.318 MHz	14.3 MHz, 33 MHz, 48 MHz, 96 MHz, 100 MHz, 133 MHz, 166 MHz	3.3 V	3.3 V	LVCMOS, HCSL	QFN56

EMI Reduction Clocks

PART NUMBER	CONTROL	CLOCK INPUT/ OUTPUTS	INPUT FREQUENCY (MHz)	OUTPUT FREQUENCY (MHz)	PHASE JITTER (RMS)	VDD	VDDO	OUTPUT	PACKAGE
SL15300	Pin	1/4	3 - 166 (Clock), 8 - 48 (Xtal)	3 - 200 MHz	—	1.8, 2.5, 3.3 V	—	LVCMOS	TSSOP8
SL16020DC	Pin/I²C	1/2	27 (Xtal)	27 MHz/100 MHz	—	3.3 V	—	LVCMOS	TDFN10
Si5335	Pin	1/4	10 - 350 (Clock), 25/27 (Xtal)	1 - 350 MHz	1.0 ps	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS, LVDS, LVPECL, HCSL, SSTL, HSTL, CML	QFN24
Si51210	Pin	1/2	3 - 166 (Clock), 8 - 48 (Xtal)	3 - 200 MHz	—	2.5 - 3.3 V	—	LVCMOS	TDFN6
Si51211	Pin	2/3	3 - 166 (Clock), 8 - 48 (Xtal)	3 - 200 MHz	—	2.5 - 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	TDFN8
Si51214	Pin	1/2	3 - 166 (Clock), 8 - 48 (Xtal)	3 - 200 MHz	—	1.8 V	—	LVCMOS	TDFN6
Si51219	Pin	2/3	3 - 166 (Clock), 8 - 48 (Xtal)	3 - 200 MHz	—	2.5 - 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	TSSOP8
Si52142	Pin/I²C	1/3	25 MHz	25 MHz/100 MHz	1.0 ps	3.3 V	3.3 V	HSCL, LVCMOS	QFN24
Si52143	Pin/I²C	1/5	25 MHz	25 MHz/100 MHz	1.0 ps	3.3 V	3.3 V	HSCL, LVCMOS	QFN24
Si52144	Pin/I²C	1/4	25 MHz	100 MHz	1.0 ps	3.3 V	3.3 V	HSCL	QFN24
Si52146	Pin/I²C	1/6	25 MHz	100 MHz	1.0 ps	3.3 V	3.3 V	HSCL	QFN32
Si52147	Pin/I²C	1/9	25 MHz	100 MHz	1.0 ps	3.3 V	3.3 V	HSCL	QFN48

Clock Distribution: www.silabs.com/clock-buffers

Fanout Buffers/Level Translators

PART NUMBER	CONTROL	CLOCK INPUT/ OUTPUTS	ADDITIVE JITTER (RMS)	INPUT FREQUENCY (MHz)	OUTPUT FREQUENCY (MHz)	VDD	VDDO	OUTPUT	PACKAGE
Si53302	Pin	2/10	100 fs	1 - 725 MHz	1 - 725 MHz	1.8, 2.5, 3.3 V	1.8, 2.5 V	LVCMOS, LVDS, LVPECL, HCSL, CML	QFN44
Si53301	Pin	2/6	100 fs	1 - 725 MHz	1 - 725 MHz	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS, LVDS, LVPECL, HCSL, CML	QFN44

Si53306	Pin	1/4	100 fs	1 - 725 MHz	1 - 725 MHz	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS, LVDS, LVPECL, HCSL, CML	QFN16
Si53315	Pin	2/10	100 fs	1 - 1250 MHz	1 - 1250 MHz	1.8, 2.5, 3.3 V	1.8, 2.5 V	LVCMOS, LVDS, LVPECL, HCSL, CML	QFN44
Si53320	Pin	2/10	100 fs	1 - 725 MHz	1 - 725 MHz	2.5, 3.3 V	2.5, 3.3 V	LVPECL	TSSOP20
Si53360	Pin	1/8	100 fs	1 - 200 MHz	1 - 200 MHz	1.8, 2.5, 3.3 V	1.8, 2.5 V	LVCMOS	TSSOP16
Si53152	Pin/I²C	1/2	1.0 ps	100 MHz	100 MHz	3.3 V	3.3 V	HCSL	QFN24
Si53154	Pin/I²C	1/4	1.0 ps	100 MHz	100 MHz	3.3 V	3.3 V	HCSL	QFN24
Si53156	Pin/I²C	1/6	1.0 ps	100 MHz	100 MHz	3.3 V	3.3 V	HCSL	QFN32
Si53159	Pin/I²C	1/9	1.0 ps	100 MHz	100 MHz	3.3 V	3.3 V	HCSL	QFN48
SL2304NZ	Pin	1/4	—	1 - 140 MHz	1 - 140 MHz	3.3 V	—	LVCMOS	8TSSOP/8SOIC
SL23EP04NZ	Pin	1/4	—	DC - 220 MHz	DC - 220 MHz	2.5 V, 3.3 V	—	LVCMOS	TSSOP8
SL2305NZ	Pin	1/5	—	1 - 140 MHz	1 - 140 MHz	3.3 V	—	LVCMOS	TSSOP8/SOIC8
SL2309NZ	Pin	1/9	—	DC - 140 MHz	DC - 140 MHz	3.3 V	3.3 V	LVCMOS	SOIC16
SL23EP09NZ	Pin	1/9	—	1 - 220 MHz	1 - 220 MHz	2.5 V, 3.3 V	—	LVCMOS	TSSOP16/SOIC16
SL28PCIe14	Pin/I²C	2/4	1.0 ps	25 MHz/100 MHz	100 MHz	3.3 V	3.3 V	HCSL	QFN32
Si5330	Pin	1/4	150 fs	5 - 710 MHz	5 - 710 MHz	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVPECL, LVDS, HCSL, SSTL, HSTL	QFN24
Si5330F	Pin	1/8	—	5 - 200 MHz	5 - 200 MHz	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS	QFN24
Si5335	Pin	1/4	150 fs	1 - 350 MHz	1 - 350 MHz	1.8, 2.5, 3.3 V	1.8, 2.5, 3.3 V	LVCMOS, LVDS, LVPECL, HCSL, SSTL, HSTL, CML	QFN24
SL18860DC	Pin	1/3	—	10 - 52 MHz	10 - 52 MHz	1.8, 2.5, 3.3 V	—	LVCMOS	TDFN10

Zero Delay Buffers

PART NUMBER	CONTROL	CLOCK INPUT/ OUTPUTS	INPUT FREQUENCY (MHz)	OUTPUT FREQUENCY (MHz)	PHASE JITTER (RMS)	VDD	VDDO	OUTPUT	PACKAGE
SL2305	Pin	1/5	1 - 140 MHz	1 - 140 MHz	—	3.3 V	—	LVCMOS	TSSOP8/SOIC8
SL2309	Pin	1/9	10 - 140 MHz	10 - 140 MHz	—	3.3 V	—	LVCMOS	TSSOP16/SOIC16
SL23EP04	Pin	1/4	10 - 220 MHz	10 - 220 MHz	—	2.5 V/3.3 V	—	LVCMOS	SOIC8
SL23EP05	Pin	1/5	10 - 220 MHz	10 - 220 MHz	—	2.5 V/3.3 V	—	LVCMOS	TSSOP8/SOIC8
SL23EP08	Pin	1/8	10 - 220 MHz	10 - 220 MHz	—	2.5 V/3.3 V	—	LVCMOS	TSSOP16/SOIC16
SL23EP09	Pin	1/9	10 - 220 MHz	10 - 220 MHz	—	2.5 V/3.3 V	—	LVCMOS	TSSOP16/SOIC16

PCI Express Clock Buffers (PCIe)

PART NUMBER	CONTROL	CLOCK INPUT/ OUTPUTS	INPUT FREQUENCY (MHz)	OUTPUT FREQUENCY (MHz)	PHASE JITTER (RMS)	VDD	VDDO	OUTPUT	PACKAGE
Si53102	—	1/2	100 MHz	100 MHz	0.5 ps	2.5, 3.3 V	—	HSCL	TDFN8
Si53154	Pin/I²C	1/4	100 MHz	100 MHz	1.0 ps	3.3 V	3.3 V	HSCL	QFN24
Si53156	Pin/I²C	1/6	100 MHz	100 MHz	1.0 ps	3.3 V	3.3 V	HSCL	QFN32
Si53159	Pin/I²C	1/9	100 MHz	100 MHz	1.0 ps	3.3 V	3.3 V	HSCL	QFN48
CY28400-2	Pin/I²C	1/4	100 MHz	100 MHz	—	3.3 V	3.3 V	HCSL	SSOP28/TSSOP28
CY28800	Pin/I²C	1/8	100 MHz	100 MHz	—	3.3 V	3.3 V	HCSL	SSOP48

Oscillators: www.silabs.com/timing

CMEMS® Oscillators

PART NUMBER	CONTROL	FREQ.	FREQUENCY RANGE	STABILITY (PPM)	PERIOD JITTER (PkPk)	SUPPLY VOLTAGE	IDD (TYP)	OE	TEMP RANGE	OUTPUT FORMAT	PACKAGE
Si501	Pin	Single	0.032 - 100 MHz	±20, ±30, ±50	11	1.8 V 2.5 V 3.3 V	1-5 mA (freq. depedent)	Pin 1	-40 to 85 °C, -20 to 70 °C	LVCMOS	3.2 mm x 5.0 mm 2.5 mm x 3.2 mm 2.0 mm x 2.5 mm DFN4
Si502	Pin	Dual						Pin 1			
Si503	Pin	Quad						Pin 1			
Si504	1-Pin	Any						—			

Crystal Oscillators (XOs)

PART NUMBER	FREQUENCY	FREQUENCY RANGE	JITTER (RMS)	STABILITY/APR (PPM)	OUTPUT FORMAT	VOLTAGE	TEMP RANGE	PACKAGE
Si535	Single	100 - 312.5 MHz	0.2 ps	±20, ±31.5	LVPECL, LVDS	2.5, 3.3 V	-40 to +85 °C	5 x 7 mm 6-pad
Si530/31	Single	10 - 1417 MHz	0.3 ps	±20, ±31.5, ±61.5	CML, CMOS, LVDS, LVPECL	1.8, 2.5, 3.3 V	-40 to +85 °C	5 x 7 mm 6-pad, 8-pad
Si532/33	Dual							
Si534	Quad							
Si570	Any (I^2C Prog)							
Si590/91	Single	10 - 810 MHz	0.5 ps	±20, ±30, ±50, ±100	CML, CMOS, LVDS, LVPECL	1.8, 2.5, 3.3 V	-40 to +85 °C	5 x 7 mm 6-pad, 8-pad
Si598	Any (I^2C Prog)							
Si510/11	Single	0.1 - 250 MHz	0.8 ps	±30, ±50, ±100	CMOS, Dual CMOS, HCSL, LVDS, LVPECL	1.8, 2.5, 3.3 V	-40 to +85 °C	5 mm x 7 mm 3.2 mm x 5 mm 6-pad
Si512/13	Dual							
Si514	Any (I^2C Prog)							

Voltage-Controlled Crystal Oscillators (VCXOs)

PART NUMBER	# OF CENTER FREQUENCIES	FREQUENCY RANGE	JITTER (RMS)	STABILITY/APR (PPM)	OUTPUT FORMAT	VOLTAGE	TEMP RANGE	PACKAGE
Si550	Single	10 - 1417 MHz	0.5 ps	±12 - ±375	CML, CMOS, LVDS, LVPECL	1.8, 2.5, 3.3 V	-40 to +85 °C	5 x 7 mm 6-pad
Si552	Dual							
Si554	Quad							
Si571	Any (I^2C Prog)							
Si595	Single	10 - 810 MHz	0.7 ps	±10 - ±370	CML, CMOS, LVDS, LVPECL	1.8, 2.5, 3.3 V	-40 to +85 °C	5 x 7 mm 6-pad, 8-pad
Si597	Dual							
Si599	Any (I^2C Prog)							
Si515	Single	0.1 - 250 MHz	1.0 ps	±30 - ±100	CMOS, Dual CMOS, HCSL, LVDS, LVPECL	2.5, 3.3 V	-40 to +85 °C	5 mm x 7 mm 3.2 mm x 5 mm 6-pad
Si516	Dual							

Silicon Oscillators

PART NUMBER	TYPE	FREQUENCY	TEMPERATURE STABILITY	TOTAL STABILITY (PPM)	OUTPUT FORMAT	PACKAGE
Si500S	XO	0.9 - 200 MHz	± 20 ppm typ	± 150 (0 - 70 °C) ± 250 (0 - 85 °C)	LVCMOS, SSTL	3.2 x 5 mm 4-pad
Si500D	XO	0.9 - 200 MHz	± 20 ppm typ	± 150 (0 - 70 °C) ± 250 (0 - 85 °C)	LVPECL, LVDS, HCSL, dual output LVCMOS, diff LVCMOS, dual output SSTL, diff SSTL	3.2 x 5 mm 6-pad

Jitter Attenuators/Clock Cleaners: www.silabs.com/clocks

PART NUMBER	# OF PLLS	CONTROL	CLOCK INPUTS/ OUTPUTS	INPUT FREQUENCY (MHz)	OUTPUT FREQUENCY (MHz)	JITTER (12 KHz TO 20 MHz)	PLL BANDWIDTH	HITLESS SWITCHING	DIGITAL HOLD	FREE RUN	SIGNAL FORMAT	PACKAGE
Si5315	1	Pin	2/2	0.008 - 644	0.008 - 644	450 fs rms typ	60 Hz - 8 kHz	•	•		CMOS, LVDS, LVPECL, CML	QFN36
Si5317	1	Pin	1/2	1 - 710	1 - 710	290 fs rms typ	60 Hz - 8 kHz		•		CMOS, LVDS, LVPECL, CML	QFN36

PART NUMBER	# OF PLLS	CONTROL	CLOCK INPUTS/ OUTPUTS	INPUT FREQUENCY (MHz)	OUTPUT FREQUENCY (MHz)	JITTER (12 KHz TO 20 MHz)	PLL BANDWIDTH	HITLESS SWITCHING	DIGITAL HOLD	FREE RUN	SIGNAL FORMAT	PACKAGE
Si5319	1	I²C/SPI	1/1	0.002 - 710	0.002 - 1417	300 fs rms typ	60 Hz - 8 kHz			•	CMOS, LVDS, LVPECL, CML	QFN36
Si5324	1	I²C/SPI	2/2	0.002 - 710	0.002 - 1417	290 fs rms typ	4 Hz - 525 Hz	•	•	•	CMOS, LVDS, LVPECL, CML	QFN36
Si5326	1	I²C/SPI	2/2	0.002 - 710	0.002 - 1417	300 fs rms typ	60 Hz - 8 kHz	•	•	•	CMOS, LVDS, LVPECL, CML	QFN36
Si5327	1	I²C/SPI	2/2	0.002 - 710	0.002 - 808	500 fs rms typ	4 Hz - 525 Hz	•	•	•	CMOS, LVDS, LVPECL, CML	QFN36
Si5368	1	I²C/SPI	4/5	0.002 - 710	0.002 - 1417	300 fs rms typ	60 Hz - 8 kHz	•	•	•	CMOS, LVDS, LVPECL, CML	TQFP100
Si5369	1	I²C/SPI	4/5	0.002 - 710	0.002 - 1417	300 fs rms typ	4 Hz - 525 Hz	•	•	•	CMOS, LVDS, LVPECL, CML	TQFP100
Si5374	4	I²C	8/8	0.002 - 710	0.002 - 808	410 fs rms typ	4 Hz - 525 Hz	•	•	•	CMOS, LVDS, LVPECL, CML	BGA80
Si5375	4	I²C	4/4	0.002 - 710	0.002 - 808	410 fs rms typ	60 Hz - 8 kHz		•	•	CMOS, LVDS, LVPECL, CML	BGA80
Si5376	4	I²C	8/8	0.002 - 710	0.002 - 808	410 fs rms typ	60 Hz - 8 kHz	•	•	•	CMOS, LVDS, LVPECL, CML	BGA80

Synchronous Ethernet Clock: www.silabs.com/synce

PART NUMBER	# OF INPUTS	INPUT CLOCK FREQUENCY RANGE	# OUTPUT CLOCKS	OUTPUT CLOCK FREQUENCY RANGE	PHASE JITTER (RMS TYP)	EEC OPTION 1 AND 2 WANDER FILTERING	PACKAGE
Si5328B	2	8 kHz - 710 MHz	2	8 kHz - 808 MHz	0.3 ps	Yes	QFN36
Si5328C	2	8 kHz - 346 MHz	2	8 kHz - 346 MHz	0.3 ps	Yes	QFN36

Interface Products

REQUEST SAMPLES AND DOWNLOAD DOCUMENTATION AT www.silabs.com/interface

Smart Interface Devices

PART NUMBER	DESCRIPTION	LCD SEGMENTS	EEPROM (kB)	RAM (BYTES)	DIGITAL PORT I/O PINS	COMM.	TIMERS (16-BIT)	INT. OSC	TEMP RANGE	OTHER	PACKAGE	EVALUATION KIT
CP2102	UART to USB Bridge	—	1024	1024	—	UART, USB 2.0	—	•	–40 to 85 ºC	Volt Reg	QFN28	CP2102EK
CP2103	UART to USB Bridge	—	1024	1024	4	UART, USB 2.0	—	•	–40 to 85 ºC	Volt Reg, RS-485	QFN28	CP2103EK
CP2104	UART to USB Bridge	—	1024	1152	4	UART, USB 2.0	—	•	–40 to 85 ºC	Volt Reg, RS-485	QFN24/ QFN28	CP2104EK/ CP2104-MINIEK
CP2105	USB to Dual UART	—	296	608	5	UART, USB 2.0	—	•	–40 to 85 ºC	Volt Reg, RS-485	QFN24	CP2105EK
CP2108	USB to Quad UART	—	1024	1536 (FIFO)	16	UART, USB 2.0	—	•	–40 to 85 ºC	RS-485	QFN64	CP2108EK
CP2109	USB to Quad UART	—	1024	1024	—	UART, USB 2.0	—	•	–40 to 85 ºC	Volt Reg	QFN28	CP2102EK
CP2110	HID USB to UART Bridge	—	343	960	10	UART, USB 2.0	—	•	–40 to 85 ºC	Volt Reg, RS-485	QFN24/ QFN28	CP2110EK
CP2112	USB to I²C Bridge	—	194	512	8	USB 2.0, I²C	—	•	–40 to 85 ºC	Volt Reg	QFN24	CP2112EK
CP2114	USB to I²S Audio Bridge	—	352	512 (FIFO)	12	USB 2.0, I²S	—	•	–40 to 85 ºC	Volt Reg	QFN32	CO2114EK
CP2120	SPI to I²C Bridge, GPIO Port Expander	—	0	512 (buffer RAM)	—	SPI to I²C	—	•	–40 to 85 ºC	Voltage Monitor	QFN20	CP2120EB
CP2130	USB to SPI Bridge	—	348	320 (FIFO)	11	USB to SPI	—	•	–40 to 85 ºC	Split VD-DIO; VREG	QFN24	CP2130EK
CP2200	Ethernet Controller	—	8 K	2 kB TX buffer, 4 kB RX buffer	—	8-bit non-muxed Ext. Mem I/F	—		–40 to 85 ºC	Integrated Ethernet Transceiver	TQFP48	ETHERNETDK
CP2201	Ethernet Controller	—	8 K	2 kB TX buffer, 4 kB RX buffer	—	8-bit muxed Ext. Mem I/F	—		–40 to 85 ºC	Integrated Ethernet Transceiver	QFN28	ETHERNETDK
CP2400	LCD Driver	128	0	256	36	SPI	2	•	–40 to 85 ºC	Ultra-low power mode	QFN48/ TQFP48	CP2400DK

PART NUMBER	DESCRIPTION	LCD SEGMENTS	EEPROM (kB)	RAM (BYTES)	DIGITAL PORT I/O PINS	COMM.	TIMERS (16-BIT)	INT. OSC	TEMP RANGE	OTHER	PACKAGE	EVALUATION KIT
CP2401	LCD Driver	128	0	256	36	I^2C	2	•	–40 to 85 °C	Ultra-low power mode	QFN48/ TQFP48	CP2401DK
CP2402	LCD Driver	64	0	256	20	SPI	2	•	–40 to 85 °C	Ultra-low power mode	QFN32	CP2400DK
CP2403	LCD Driver	64	0	256	20	I^2C	2	•	–40 to 85 °C	Ultra-low power mode	QFN32	CP2401DK

Isolation Products

REQUEST SAMPLES AND DOWNLOAD DOCUMENTATION AT www.silabs.com/isolation

Multi-Channel Unidirectional Digital Isolators (1 kVrms)

PART NUMBER	FORWARD CHANNELS	REVERSE CHANNELS	MAXIMUM DATA RATE (MBPS)	MAX PROPAGATION DELAY	ENABLE OUTPUT	ISOLATION RATING (kVrms)	VOLTAGE RANGE (V)	TEMPERATURE RANGE	PACKAGE
Si8440AA-D-IS1	4	0	1	35	•	1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8440BA-D-IS1	4	0	150	9.5	•	1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8441AA-D-IS1	3	1	1	35	•	1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8441BA-D-IS1	3	1	150	9.5	•	1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8442AA-D-IS1	2	2	1	35	•	1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8442BA-D-IS1	2	2	150	9.5	•	1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8442BA-D-IU	2	2	150	9.5	•	1.0	2.7 - 5.5 V	-40 to 125 °C	QSOP16
Si8445BA-D-IS1	4	0	150	9.5		1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8450AA-B-IS1	5	0	1	35	•	1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8450BA-B-IS1	5	0	150	9.5	•	1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8451AA-B-IS1	4	1	1	35	•	1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8451BA-B-IS1	4	1	150	9.5	•	1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8452AA-B-IS1	3	2	1	35	•	1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8452BA-B-IS1	3	2	150	9.5	•	1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8455BA-B-IS1	5	0	150	9.5		1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8455BA-B-IU	5	0	150	9.5		1.0	2.7 - 5.5 V	-40 to 125 °C	QSOP16
Si8460AA-B-IS1	6	0	1	35		1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8460BA-B-IS1	6	0	150	9.5		1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8461AA-B-IS1	5	1	1	35		1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8461BA-B-IS1	5	1	150	9.5		1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8462AA-B-IS1	4	2	1	35		1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8462BA-B-IS1	4	2	150	9.5		1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8463AA-B-IS1	3	3	1	35		1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8463BA-B-IS1	3	3	150	9.5		1.0	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8641BA-B-IU	3	1	150	13	•	1.0	2.5 - 5.5 V	-40 to 125 °C	QSOP16
Si8642BA-B-IU	2	2	150	13	•	1.0	2.5 - 5.5 V	-40 to 125 °C	QSOP16
Si8645BA-B-IU	4	0	150	13		1.0	2.5 - 5.5 V	-40 to 125 °C	QSOP16
Si8655BA-B-IS	5	0	150	13		1.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8655BA-B-IU	5	0	150	13		1.0	2.5 - 5.5 V	-40 to 125 °C	QSOP16

Multi-Channel Unidirectional Digital Isolators (2.5 kVrms)

PART NUMBER	FORWARD CHANNELS	REVERSE CHANNELS	MAXIMUM DATA RATE (MBPS)	MAX PROPAGATION DELAY	ENABLE OUTPUT	ISOLATION RATING (kVrms)	VOLTAGE RANGE (V)	TEMPERATURE RANGE	PACKAGE
Si8410AB-D-IS	1	0	1	35		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC8
Si8410BB-D-IS	1	0	150	9.5		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC8
Si8420AB-D-IS	2	0	1	35		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC8
Si8420BB-D-IS	2	0	150	9.5		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC8

PART NUMBER	FORWARD CHANNELS	REVERSE CHANNELS	MAXIMUM DATA RATE (MBPS)	MAX PROPAGATION DELAY	ENABLE OUTPUT	ISOLATION RATING (kVrms)	VOLTAGE RANGE (V)	TEMPERATURE RANGE	PACKAGE
Si8421AB-D-IS	1	1	1	35		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC8
Si8421BB-D-IS	1	1	150	9.5		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC8
Si8422AB-B-IS	1	1	1	35		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC8
Si8422BB-B-IS	1	1	150	9.5		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC8
Si8423AB-B-IS	2	0	1	35		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC8
Si8423BB-B-IS	2	0	150	9.5		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC8
Si8430AB-D-IS	3	0	1	35	•	2.5	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8430AB-D-IS1	3	0	1	35	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8430BB-D-IS	3	0	150	9.5	•	2.5	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8430BB-D-IS1	3	0	150	9.5	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8431AB-D-IS	2	1	1	35	•	2.5	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8431AB-D-IS1	2	1	1	35	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8431BB-D-IS	2	1	150	9.5	•	2.5	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8431BB-D-IS1	2	1	150	9.5	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8435BB-D-IS	3	0	150	9.5		2.5	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8435BB-D-IS1	3	0	150	9.5		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8440AB-D-IS	4	0	1	35	•	2.5	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8440AB-D-IS1	4	0	1	35	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8440BB-D-IS	4	0	150	9.5	•	2.5	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8440BB-D-IS1	4	0	150	9.5	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8441AB-D-IS	3	1	1	35	•	2.5	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8441AB-D-IS1	3	1	1	35	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8441BB-D-IS	3	1	150	9.5	•	2.5	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8441BB-D-IS1	3	1	150	9.5	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8442AB-D-IS	2	2	1	35	•	2.5	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8442AB-D-IS1	2	2	1	35	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8442BB-D-IS	2	2	150	9.5	•	2.5	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8442BB-D-IS1	2	2	150	9.5	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8445BB-D-IS	4	0	150	9.5		2.5	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8445BB-D-IS1	4	0	150	9.5		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8450AB-B-IS1	5	0	1	35	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8450BB-B-IS1	5	0	150	9.5	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8451AB-B-IS1	4	1	1	35	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8451BB-B-IS1	4	1	150	9.5	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8452AB-B-IS1	3	2	1	35	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8452BB-B-IS1	3	2	150	9.5	•	2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8455BB-B-IS1	5	0	150	9.5		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8460AB-B-IS1	6	0	1	35		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8460BB-B-IS1	6	0	150	9.5		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8461AB-B-IS1	5	1	1	35		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8461BB-B-IS1	5	1	150	9.5		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8462AB-B-IS1	4	2	1	35		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8462BB-B-IS1	4	2	150	9.5		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8463AB-B-IS1	3	3	1	35		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8463BB-B-IS1	3	3	150	9.5		2.5	2.7 - 5.5 V	-40 to 125 °C	NB SOIC16

Multi-Channel Unidirectional Digital Isolators (3.75 kVrms)

PART NUMBER	FORWARD CHANNELS	REVERSE CHANNELS	MAXIMUM DATA RATE (MBPS)	MAX PROPAGATION DELAY	ENABLE OUTPUT	ISOLATION RATING (kVrms)	VOLTAGE RANGE (V)	TEMPERATURE RANGE	PACKAGE
Si8610BC-B-IS	1	0	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	SOIC8

PART NUMBER	FORWARD CHANNELS	REVERSE CHANNELS	MAXIMUM DATA RATE (MBPS)	MAX PROPAGATION DELAY	ENABLE OUTPUT	ISOLATION RATING (kVrms)	VOLTAGE RANGE (V)	TEMPERATURE RANGE	PACKAGE
Si8610EC-B-IS	1	0	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	SOIC8
Si8620BC-B-IS	2	0	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	SOIC8
Si8620EC-B-IS	2	0	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	SOIC8
Si8621BC-B-IS	1	1	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	SOIC8
Si8621EC-B-IS	1	1	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	SOIC8
Si8622BC-B-IS	1	1	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	SOIC8
Si8622EC-B-IS	1	1	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	SOIC8
Si8630BC-B-IS1	3	0	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8630EC-B-IS1	3	0	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8631BC-B-IS1	2	1	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8631EC-B-IS1	2	1	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8635BC-B-IS1	3	0	150	13		3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8640BC-B-IS1	4	0	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8640EC-B-IS1	4	0	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8641BC-B-IS1	3	1	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8641EC-B-IS1	3	1	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8642BC-B-IS1	2	2	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8642EC-B-IS1	2	2	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8645BC-B-IS1	4	0	150	13		3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8650BC-B-IS1	5	0	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8650EC-B-IS1	5	0	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8651BC-B-IS1	4	1	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8651EC-B-IS1	4	1	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8652BC-B-IS1	3	2	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8652EC-B-IS1	3	2	150	13	•	3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8660BA-B-IS1	6	0	150	13		3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8660BC-B-IS1	6	0	150	13		3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8660EC-B-IS1	6	0	150	13		3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8661BC-B-IS1	5	1	150	13		3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8661EC-B-IS1	5	1	150	13		3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8662BC-B-IS1	4	2	150	13		3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8662EC-B-IS1	4	2	150	13		3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8663BC-B-IS1	3	3	150	13		3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8663EC-B-IS1	3	3	150	13		3.75	2.5 - 5.5 V	-40 to 125 °C	NB SOIC16
Si8710AC-B-IP	1	0	15	60		3.75	3 - 30 V	-40 to 125 °C	GW DIP8
Si8710AC-B-IS	1	0	15	60		3.75	3 - 30 V	-40 to 125 °C	NB SOIC8
Si8710BC-B-IP	1	0	15	60		3.75	3 - 30 V	-40 to 125 °C	GW DIP8
Si8710BC-B-IS	1	0	15	60		3.75	3 - 30 V	-40 to 125 °C	NB SOIC8
Si8710CC-B-IP	1	0	1	60		3.75	3 - 30 V	-40 to 125 °C	GW DIP8
Si8710CC-B-IS	1	0	1	60		3.75	3 - 30 V	-40 to 125 °C	NB SOIC8
Si8711AC-B-IP	1	0	15	60		3.75	3 - 30 V	-40 to 125 °C	GW DIP8
Si8711AC-B-IS	1	0	15	60		3.75	3 - 30 V	-40 to 125 °C	NB SOIC8
Si8711BC-B-IP	1	0	15	60		3.75	3 - 30 V	-40 to 125 °C	GW DIP8
Si8711BC-B-IS	1	0	15	60		3.75	3 - 30 V	-40 to 125 °C	NB SOIC8
Si8711CC-B-IP	1	0	1	60		3.75	3 - 30 V	-40 to 125 °C	GW DIP8
Si8711CC-B-IS	1	0	1	60		3.75	3 - 30 V	-40 to 125 °C	NB SOIC8
Si8712AC-B-IP	1	0	15	60	•	3.75	3 - 30 V	-40 to 125 °C	GW DIP8
Si8712AC-B-IS	1	0	15	60	•	3.75	3 - 30 V	-40 to 125 °C	NB SOIC8
Si8712BC-B-IP	1	0	15	60	•	3.75	3 - 30 V	-40 to 125 °C	GW DIP8
Si8712BC-B-IS	1	0	15	60	•	3.75	3 - 30 V	-40 to 125 °C	NB SOIC8
Si8712CC-B-IP	1	0	1	60	•	3.75	3 - 30 V	-40 to 125 °C	GW DIP8

PART NUMBER	FORWARD CHANNELS	REVERSE CHANNELS	MAXIMUM DATA RATE (MBPS)	MAX PROPAGATION DELAY	ENABLE OUTPUT	ISOLATION RATING (kVrms)	VOLTAGE RANGE (V)	TEMPERATURE RANGE	PACKAGE
Si8712CC-B-IS	1	0	1	60	•	3.75	3 - 30 V	-40 to 125 °C	NB SOIC8

Multi-Channel Unidirectional Digital Isolators (5 kVrms)

PART NUMBER	FORWARD CHANNELS	REVERSE CHANNELS	MAXIMUM DATA RATE (MBPS)	MAX PROPAGATION DELAY	ENABLE OUTPUT	ISOLATION RATING (kVrms)	VOLTAGE RANGE (V)	TEMPERATURE RANGE	PACKAGE
Si8410AD-A-IS	1	0	1	35		5.0	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8410BD-A-IS	1	0	150	9.5		5.0	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8420AD-A-IS	2	0	1	35		5.0	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8420BD-A-IS	2	0	150	9.5		5.0	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8421AD-B-IS	1	1	1	35		5.0	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8421BD-B-IS	1	1	150	9.5		5.0	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8422AD-B-IS	1	1	1	35		5.0	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8422BD-B-IS	1	1	150	9.5		5.0	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8423AD-B-IS	2	0	1	35		5.0	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8423BD-B-IS	2	0	150	9.5		5.0	2.7 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8610BD-B-IS	1	0	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8610ED-B-IS	1	0	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	SOIC8
Si8620BD-B-IS	2	0	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8620ED-B-IS	2	0	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8621BD-B-IS	1	1	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8621ED-B-IS	1	1	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8622BD-B-IS	1	1	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8622ED-B-IS	1	1	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8630BD-B-IS	3	0	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8630ED-B-IS	3	0	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8631BD-B-IS	2	1	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8631ED-B-IS	2	1	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8635BD-B-IS	3	0	150	13		5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8640BD-B-IS	4	0	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8640ED-B-IS	4	0	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8641BD-B-IS	3	1	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8641ED-B-IS	3	1	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8642BD-B-IS	2	2	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8642ED-B-IS	2	2	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8645BD-B-IS	4	0	150	13		5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8650BD-B-IS	5	0	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8650ED-B-IS	5	0	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8651BD-B-IS	4	1	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8651ED-B-IS	4	1	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8652BD-B-IS	3	2	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8652ED-B-IS	3	2	150	13	•	5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8655BD-B-IS	5	0	150	13		5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8660BD-B-IS	6	0	150	13		5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8660ED-B-IS	6	0	150	13		5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8661BD-B-IS	5	1	150	13		5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8661ED-B-IS	5	1	150	13		5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8662BD-B-IS	4	2	150	13		5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8662ED-B-IS	4	2	150	13		5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8663BD-B-IS	3	3	150	13		5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16
Si8663ED-B-IS	3	3	150	13		5.0	2.5 - 5.5 V	-40 to 125 °C	WB SOIC16

PART NUMBER	FORWARD CHANNELS	REVERSE CHANNELS	MAXIMUM DATA RATE (MBPS)	MAX PROPAGATION DELAY	ENABLE OUTPUT	ISOLATION RATING (kVrms)	VOLTAGE RANGE (V)	TEMPERATURE RANGE	PACKAGE
Si8710AD-B-IS	1	0	15	60		5.0	3 - 30 V	-40 to 125 °C	WB SOIC6
Si8710BD-B-IS	1	0	15	60		5.0	3 - 30 V	-40 to 125 °C	WB SOIC6
Si8710CD-B-IS	1	0	1	60		5.0	3 - 30 V	-40 to 125 °C	WB SOIC6
Si8711AD-B-IM	1	0	15	60		5.0	3 - 30 V	-40 to 125 °C	LGA8
Si8711BD-B-IM	1	0	15	60		5.0	3 - 30 V	-40 to 125 °C	LGA8
Si8711CD-B-IM	1	0	1	60		5.0	3 - 30 V	-40 to 125 °C	LGA8
Si8712AD-B-IM	1	0	15	60	•	5.0	3 - 30 V	-40 to 125 °C	LGA8
Si8712BD-B-IM	1	0	15	60	•	5.0	3 - 30 V	-40 to 125 °C	LGA8
Si8712CD-B-IM	1	0	1	60	•	5.0	3 - 30 V	-40 to 125 °C	LGA8

Bidirectional Digital Isolators

PART NUMBER	SERIAL DATA	SERIAL CLOCK	UNIDIRECTIONAL CHANNELS	MAXIMUM I²CCLOCK RATE	ISOLATION RATING (kVrms)	PACKAGE
Si8400AA-A-IS	•	•	—	1.7	1.0	NB SOIC8
Si8400AB-A-IS	•	•	—	1.7	2.5	NB SOIC8
Si8400AB-B-IS	•		—	1.7	2.5	NB SOIC8
Si8401AA-B-IS	•		—	1.7	1.0	NB SOIC8
Si8401AB-B-IS	•	•	—	1.7	2.5	NB SOIC8
Si8402AB-B-IS	•	•	1	1.7	2.5	NB SOIC8
Si8405AA-A-IS1	•	•	2	1.7	1.0	NB SOIC16
Si8405AB-A-IS1	•	•	2	1.7	2.5	NB SOIC16
Si8600AC-B-IS	•		1	1.7	3.75	SOIC8
Si8600AD-B-IS	•		—	1.7	5	WB SOIC16
Si8602AC-B-IS	•	•	1	1.7	3.75	SOIC8
Si8602AD-B-IS	•	•	1	1.7	5	WB SOIC16
Si8605AC-B-IS1	•	•	2	1.7	3.75	NB SOIC16
Si8605AD-B-IS	•	•	2	1.7	5	WB SOIC16
Si8606AC-B-IS1	•		2	1.7	3.75	NB SOIC16
Si8606AD-B-IS	•		2	1.7	5	WB SOIC16

Isolated Current Sensors

PART NUMBER	FULL SCALE CURRENT (A)	INITIAL ACCURACY %	TEMPERATURE RANGE	OUTPUT MODE	ISOLATION RATING	PIN 7 FUNCTION	PACKAGE
Si8501-B-GM	5	5%	-40 to 125 °C	Single	1 kV rms/ 5 kV rms	Integrator Reset Time Programming Input	QFN12/SOIC20
Si8501-C-IM	5	5%	-40 to 125 °C	Single	1 kV rms	Integrator Reset Time Programming Input	QFN12
Si8501-C-IS	5	5%	-40 to 125 °C	Single	5 kV rms	Integrator Reset Time Programming Input	SOIC20
Si8502-B-GM	10	5%	-40 to 125 °C	Single	1 kV rms/ 5 kV rms	Integrator Reset Time Programming Input	QFN12/SOIC20
Si8502-C-IM	10	5%	-40 to 125 °C	Single	1 kV rms	Integrator Reset Time Programming Input	QFN12
Si8502-C-IS	10	5%	-40 to 125 °C	Single	5 kV rms	Integrator Reset Time Programming Input	SOIC20
Si8503-B-GM	20	5%	-40 to 125 °C	Single	1 kV rms/ 5 kV rms	Integrator Reset Time Programming Input	QFN12/SOIC20
Si8503-C-IM	20	5%	-40 to 125 °C	Single	1 kV rms	Integrator Reset Time Programming Input	QFN12
Si8503-C-IS	20	5%	-40 to 125 °C	Single	5 kV rms	Integrator Reset Time Programming Input	SOIC20
Si8511-B-GM	5	5%	-40 to 125 °C	Ping-Pong	1 kV rms/ 5 kV rms	Integrator Reset Time Programming Input	QFN12/SOIC20
Si8511-C-IM	5	5%	-40 to 125 °C	Ping-Pong	1 kV rms	Integrator Reset Time Programming Input	QFN12
Si8511-C-IS	5	5%	-40 to 125 °C	Ping-Pong	5 kV rms	Integrator Reset Time Programming Input	SOIC20

PART NUMBER	FULL SCALE CURRENT (A)	INITIAL ACCURACY %	TEMPERATURE RANGE	OUTPUT MODE	ISOLATION RATING	PIN 7 FUNCTION	PACKAGE
Si8512-B-GM	10	5%	-40 to 125 °C	Ping-Pong	1 kV rms/ 5 kV rms	Integrator Reset Time Programming Input	SOIC20
Si8512-C-IM	10	5%	-40 to 125 °C	Ping-Pong	1 kV rms	Integrator Reset Time Programming Input	QFN12
Si8512-C-IS	10	5%	-40 to 125 °C	Ping-Pong	5 kV rms	Integrator Reset Time Programming Input	SOIC20
Si8513-B-GM	20	5%	-40 to 125 °C	Ping-Pong	1 kV rms/ 5 kV rms	Integrator Reset Time Programming Input	QFN12/SOIC20
Si8513-C-IM	20	5%	-40 to 125 °C	Ping-Pong	1 kV rms	Integrator Reset Time Programming Input	QFN12
Si8513-C-IS	20	5%	-40 to 125 °C	Ping-Pong	5 kV rms	Integrator Reset Time Programming Input	SOIC20
Si8517-B-GM	5	5%	-40 to 125 °C	Ping-Pong with Fault output	1 kV rms/ 5 kV rms	Fault Output	QFN12/SOIC20
Si8517-C-IM	5	5%	-40 to 125 °C	Ping-Pong	1 kV rms	Fault Output	QFN12
Si8517-C-IS	5	5%	-40 to 125 °C	Ping-Pong	1 kV rms	Fault Output	SOIC20
Si8518-B-GM	10	5%	-40 to 125 °C	Ping-Pong with Fault output	1 kV rms/ 5 kV rms	Fault Output	SOIC20
Si8518-C-IM	10	5%	-40 to 125 °C	Ping-Pong	1 kV rms	Fault Output	QFN12
Si8518-C-IS	10	5%	-40 to 125 °C	Ping-Pong	5 kV rms	Fault Output	SOIC20
Si8519-B-GM	20	5%	-40 to 125 °C	Ping-Pong with Fault output	1 kV rms/ 5 kV rms	Fault Output	SOIC20
Si8519-C-IM	20	5%	-40 to 125 °C	Ping-Pong	1 kV rms	Fault Output	QFN12
Si8519-C-IS	20	5%	-40 to 125 °C	Ping-Pong	5 kV rms	Fault Output	SOIC20
Si8540	0.1 to 10 A	0.2%	-40 to 85 °C	Current	—	—	SOT5

Isolated Gate Drivers

PART NUMBER	INPUT TYPE	OUTPUT CONFIGURATION	OVERLAP PROTECTION/ DEAD TIME CONTROL	ISOLATION RATING (INPUT-OUTPUT) (V)	ISOLATION RATING (OUTPUT-OUTPUT) (VDC)	MAXIMUM PROPAGATION DELAY	UVLO VOLTAGE	PEAK OUTPUT CURRENT	PACKAGE
Si8220BB-A-IS	Opto	Single		2.5	—	80	8 V	2.5	SOIC8
Si8220BD-A-IS	Opto	Single		5.0	—	80	8 V	2.5	WB SOIC16
Si8220CB-A-IS	Opto	Single		2.5	—	80	10 V	2.5	SOIC8
Si8220CD-A-IS	Opto	Single		5.0	—	80	10 V	2.5	WB SOIC16
Si8220DB-A-IS	Opto	Single		2.5	—	80	12.5 V	2.5	SOIC8
Si8220DD-A-IS	Opto	Single		5.0	—	80	12.5 V	2.5	WB SOIC16
Si8221CC-A-IS	Opto	Single		3.75	—	80	10 V	0.5	SOIC8
Si8221DC-A-IS	Opto	Single		3.75	—	80	12.5 V	0.5	SOIC8
Si8230AB-B-IS	VIA, VIB	High Side / Low Side	•	2.5	3500	60	5 V	0.5	WB SOIC16
Si8230AB-B-IS1	VIA, VIB	High Side / Low Side	•	2.5	3500	60	5 V	0.5	NB SOIC16
Si8230AD-B-IS	VIA, VIB	High Side / Low Side	•	5.0	3500	60	5 V	0.5	WB SOIC16
Si8230BB-B-IS	VIA, VIB	High Side / Low Side	•	2.5	3500	60	8 V	0.5	WB SOIC16
Si8230BB-B-IS1	VIA, VIB	High Side / Low Side	•	2.5	3500	60	8 V	0.5	NB SOIC16
Si8230BD-B-IS	VIA, VIB	High Side / Low Side	•	5.0	3500	60	8 V	0.5	WB SOIC16
Si8231AB-B-IS	PWM	High Side / Low Side	•	2.5	3500	60	5 V	0.5	WB SOIC16
Si8231AB-B-IS1	PWM	High Side / Low Side	•	2.5	3500	60	5 V	0.5	NB SOIC16
Si8231AD-B-IS	PWM	High Side / Low Side	•	5.0	3500	60	5 V	0.5	WB SOIC16
Si8231BB-B-IS	PWM	High Side / Low Side	•	2.5	3500	60	8 V	0.5	WB SOIC16
Si8231BB-B-IS1	PWM	High Side / Low Side	•	2.5	3500	60	8 V	0.5	NB SOIC16
Si8231BD-B-IS	PWM	High Side / Low Side	•	5.0	3500	60	8 V	0.5	WB SOIC16
Si8232AB-B-IS	VIA, VIB	Dual Driver		2.5	3500	60	5 V	0.5	WB SOIC16
Si8232AB-B-IS1	VIA, VIB	Dual Driver		2.5	3500	60	5 V	0.5	NB SOIC16
Si8232AD-B-IS	VIA, VIB	Dual Driver		5.0	3500	60	5 V	0.5	WB SOIC16
Si8232BB-B-IS	VIA, VIB	Dual Driver		2.5	3500	60	8 V	0.5	WB SOIC16
Si8232BB-B-IS1	VIA, VIB	Dual Driver		2.5	3500	60	8 V	0.5	NB SOIC16
Si8232BD-B-IS	VIA, VIB	Dual Driver		5.0	3500	60	8 V	0.5	WB SOIC16

PART NUMBER	INPUT TYPE	OUTPUT CONFIGURATION	OVERLAP PROTECTION/ DEAD TIME CONTROL	ISOLATION RATING (INPUT-OUTPUT) (V)	ISOLATION RATING (OUTPUT-OUTPUT) (VDC)	MAXIMUM PROPAGATION DELAY	UVLO VOLTAGE	PEAK OUTPUT CURRENT	PACKAGE
Si8233AB-C-IM	VIA, VIB	High Side / Low Side	•	2.5	900	60	5 V	4.0	LGA14
Si8233AB-C-IS	VIA, VIB	High Side / Low Side	•	2.5	3500	60	5 V	4.0	WB SOIC16
Si8233AB-C-IS1	VIA, VIB	High Side / Low Side	•	2.5	3500	60	5 V	4.0	NB SOIC16
Si8233AD-C-IS	VIA, VIB	High Side / Low Side	•	5.0	3500	60	5 V	4.0	WB SOIC16
Si8233BB-C-IM	VIA, VIB	High Side / Low Side	•	2.5	900	60	8 V	4.0	LGA14
Si8233BB-C-IS	VIA, VIB	High Side / Low Side	•	2.5	3500	60	8 V	4.0	WB SOIC16
Si8233BB-C-IS1	VIA, VIB	High Side / Low Side	•	2.5	3500	60	8 V	4.0	NB SOIC16
Si8233BD-C-IS	VIA, VIB	High Side / Low Side	•	5.0	3500	60	8 V	4.0	WB SOIC16
Si8234AB-C-IM	PWM	High Side / Low Side	•	2.5	900	60	5 V	4.0	LGA14
Si8234AB-C-IS	PWM	High Side / Low Side	•	2.5	3500	60	5 V	4.0	WB SOIC16
Si8234AB-C-IS1	PWM	High Side / Low Side	•	2.5	3500	60	5 V	4.0	NB SOIC16
Si8234AD-C-IS	PWM	High Side / Low Side	•	5.0	3500	60	5 V	4.0	WB SOIC16
Si8234BB-C-IM	PWM	High Side / Low Side	•	2.5	900	60	8 V	4.0	LGA14
Si8234BB-C-IS	PWM	High Side / Low Side	•	2.5	3500	60	8 V	4.0	WB SOIC16
Si8234BB-C-IS1	PWM	High Side / Low Side	•	2.5	3500	60	8 V	4.0	NB SOIC16
Si8234BD-C-IS	PWM	High Side / Low Side	•	5.0	3500	60	8 V	4.0	WB SOIC16
Si8235AB-C-IM	VIA, VIB	Dual Driver		2.5	900	60	5 V	4.0	LGA14
Si8235AB-C-IS	VIA, VIB	Dual Driver		2.5	3500	60	5 V	4.0	WB SOIC16
Si8235AB-C-IS1	VIA, VIB	Dual Driver		2.5	3500	60	5 V	4.0	NB SOIC16
Si8235AD-C-IS	VIA, VIB	Dual Driver		5.0	3500	60	5 V	4.0	WB SOIC16
Si8235BB-C-IM	VIA, VIB	Dual Driver		2.5	900	60	8 V	4.0	LGA14
Si8235BB-C-IS	VIA, VIB	Dual Driver		2.5	3500	60	8 V	4.0	WB SOIC16
Si8235BB-C-IS1	VIA, VIB	Dual Driver		2.5	3500	60	8 V	4.0	NB SOIC16
Si8235BD-C-IS	VIA, VIB	Dual Driver		5.0	3500	60	8 V	4.0	WB SOIC16
Si8236AA-C-IM	VIA, VIB	Dual Driver		1.5	—	60	5 V	4.0	LGA14
Si8236BA-C-IM	VIA, VIB	Dual Driver		1.5	—	60	8 V	4.0	LGA14
Si8261AAC-C-IP	LED emulator	Single Driver		3.75	—	60	5 V	0.6	GW DIP8
Si8261AAC-C-IS	LED emulator	Single Driver		3.75	—	60	5 V	0.6	SOIC8
Si8261AAD-C-IM	LED emulator	Single Driver		5	—	60	5 V	0.6	LGA8
Si8261AAD-C-IS	LED emulator	Single Driver		5	—	60	5 V	0.6	WB SO6
Si8261ABC-C-IP	LED emulator	Single Driver		3.75	—	60	8 V	0.6	GW DIP8
Si8261ABC-C-IS	LED emulator	Single Driver		3.75	—	60	8 V	0.6	SOIC8
Si8261ABD-C-IM	LED emulator	Single Driver		5	—	60	8 V	0.6	LGA8
Si8261ABD-C-IS	LED emulator	Single Driver		5	—	60	8 V	0.6	WB SO6
Si8261ACC-C-IP	LED emulator	Single Driver		3.75	—	60	12 V	0.6	GW DIP8
Si8261ACC-C-IS	LED emulator	Single Driver		3.75	—	60	12 V	0.6	SOIC8
Si8261ACD-C-IM	LED emulator	Single Driver		5	—	60	12 V	0.6	LGA8
Si8261ACD-C-IS	LED emulator	Single Driver		5	—	60	12 V	0.6	WB SO6
Si8261BAC-C-IP	LED emulator	Single Driver		3.75	—	60	5 V	4.0	GW DIP8
Si8261BAC-C-IS	LED emulator	Single Driver		3.75	—	60	5 V	4.0	SOIC8
Si8261BAD-C-IM	LED emulator	Single Driver		5	—	60	5 V	4.0	LGA8
Si8261BAD-C-IS	LED emulator	Single Driver		5	—	60	5 V	4.0	WB SO6
Si8261BBC-C-IP	LED emulator	Single Driver		3.75	—	60	8 V	4.0	GW DIP8
Si8261BBC-C-IS	LED emulator	Single Driver		3.75	—	60	8 V	4.0	SOIC8
Si8261BBD-C-IM	LED emulator	Single Driver		5	—	60	8 V	4.0	LGA8
Si8261BBD-C-IS	LED emulator	Single Driver		5	—	60	8 V	4.0	WB SO6
Si8261BCC-C-IP	LED emulator	Single Driver		3.75	—	60	12 V	4.0	GW DIP8
Si8261BCC-C-IS	LED emulator	Single Driver		3.75	—	60	12 V	4.0	SOIC8
Si8261BCD-C-IM	LED emulator	Single Driver		5	—	60	12 V	4.0	LGA8
Si8261BCD-C-IS	LED emulator	Single Driver		5	—	60	12 V	4.0	WB SO6

Isolated AC Mains Monitor

PART NUMBER	FULL SCALE CURRENT (A)	INITIAL ACCURACY	TEMPERATURE RANGE	ISOLATION RATING	SERIAL PORT	PACKAGE
Si8900B-A01-GS	10 A	10-bit	-40 to 85 °C	2.5 kV rms	UART	WB SOIC16
Si8900D-A01-GS	10 A	10-bit	-40 to 85 °C	5 kV rms	UART	WB SOIC16
Si8901B-A01-GS	10 A	10-bit	-40 to 85 °C	2.5 kV rms	I²C/SMBus	WB SOIC16
Si8901D-A01-GS	10 A	10-bit	-40 to 85 °C	5 kV rms	I²C/SMBus	WB SOIC16
Si8902B-A01-GS	10 A	10-bit	-40 to 85 °C	2.5 kV rms	I²C/SMBus	WB SOIC16
Si8902D-A01-GS	10 A	10-bit	-40 to 85 °C	5 kV rms	—	WB SOIC16

Power over Ethernet Controllers

PART NUMBER	PART DESCRIPTION	TEMPERATURE RANGE	MAX OUTPUT POWER	PACKAGE
Si3402	Powered device I/F with PWM controller and low-EMI	-40 to 85 °C	17 W	QFN20
Si3452A-B02-GM	Quad PoE+ PSE controller; Alt:A; PoE; Auto dV/dt	-40 to 85 °C	40 W	QFN40
Si3452-B02-GM	Quad PoE+ PSE controller; Alt:A; PoE; Shutdown; dV/dt	-40to 85 °C	40 W	QFN40
Si3452B-B02-GM	Quad PoE+ PSE controller; Alt:B; PoE; Auto; dV/dt	-40 to 85 °C	40 W	QFN40
Si3452C-B02-GM	Quad PoE+ PSE controller, Alt:A, PoE+; Auto dV/dt	-40 to 85 °C	40 W	QFN40
Si3452D-B02-GM	Quad PoE+ PSE controller, Alt:B, PoE+; Auto dV/dt	-40 to 85 °C	40 W	QFN40
Si3462	Single port PoE+ controller	-40 to 85 °C	30 W	QFN11
Si3480-A01-GM	8-port power management controller	-40 to 85 °C	30 W	QFN24
Si3482-A01-GM	48-port power management controller	-40 to 85 °C	30 W	QFN24
Si3500-A-GM	High voltage dc-dc controller (-42 to -57 V input range)	-40 to 85 °C	17 W	QFN20

8-bit Microcontroller Products

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Capacitive Touch Controller MCUs

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	TOUCH CH.	TEMP SENSOR	VREF	COMP.	PACKAGE	DEV KIT
C8051F760	32 kB	25	8	—	I²C; SPI; UART	—	—	—	10-bit, 9-ch., 300 ksps	—	—	—	—	QFN48	—
C8051F761	32 kB	25	8	—	I²C; SPI; UART	—	—	—	10-bit, 9-ch., 300 ksps	—	—	—	—	QFN32	—
C8051F762	32 kB	25	8	—	I²C; SPI; UART	—	—	—	10-bit, 9-ch., 300 ksps	—	—	—	—	QFN24	—
C8051F765	32 kB	25	8	—	I²C; SPI; UART	—	—	—	10-bit, 9-ch., 300 ksps	—	—	—	—	QFN48	—
C8051F766	32 kB	25	8	—	I²C; SPI; UART	—	—	—	10-bit, 9-ch., 300 ksps	—	—	—	—	QFN32	—
C8051F767	32 kB	25	8	—	I²C; SPI; UART	—	—	—	10-bit, 9-ch., 300 ksps	—	—	—	—	QFN24	—
C8051F702	16 kB	25	0.5	54	EMIF; I²C; SPI; UART	4	3	±2%	10-bit, 16-ch., 500 ksps	38	•	•	1	QFP64	C8051F700DK
C8051F703	16 kB	25	0.5	54	EMIF; I²C; SPI; UART	4	3	±2%	—	38	—	—	1	QFP64	C8051F700DK
C8051F706	16 kB	25	0.5	39	I²C; SPI; UART	4	3	±2%	10-bit, 12-ch., 500 ksps	27	•	•	1	QFN48/ QFP48	C8051F700DK
C8051F707	16 kB	25	0.5	39	I²C; SPI; UART	4	3	±2%	—	27	—	—	1	QFN48/ QFP48	C8051F700DK
C8051F716	16 kB	25	0.5	29	I²C; SPI; UART	4	3	±2%	10-bit, 3-ch., 500 ksps	26	•	•	1	QFN32	C8051F700DK
C8051F717	16 kB	25	0.5	20	I²C; SPI; UART	4	3	±2%	—	18	—	—	1	QFN24	C8051F700DK
C8051F800	16 kB	25	0.5	17	I²C; SPI; UART	3	3	±2%	10-bit, 16-ch., 500 ksps	16	•	•	1	QFN20/ QSOP24	C8051F800DK
C8051F801	16 kB	25	0.5	17	I²C; SPI; UART	3	3	±2%	10-bit, 16-ch., 500 ksps	8	•	•	1	QFN20/ QSOP24	C8051F800DK
C8051F802	16 kB	25	0.5	17	I²C; SPI; UART	3	3	±2%	10-bit, 16-ch., 500 ksps	—	•	•	1	QFN20/ QSOP24	C8051F800DK

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	TOUCH CH.	TEMP SENSOR	VREF	COMP.	PACKAGE	DEV KIT
C8051F803	16 kB	25	0.5	13	I^2C; SPI; UART	3	3	±2%	10-bit, 12-ch., 500 ksps	12	•	•	1	SOIC16	C8051F800DK
C8051F804	16 kB	25	0.5	13	I^2C; SPI; UART	3	3	±2%	10-bit, 12-ch., 500 ksps	8	•	•	1	SOIC16	C8051F800DK
C8051F806	16 kB	25	0.5	17	I^2C; SPI; UART	3	3	±2%	—	16	—	—	1	QFN20/ QSOP24	C8051F800DK
C8051F807	16 kB	25	0.5	17	I^2C; SPI; UART	3	3	±2%	—	8	—	—	1	QFN20/ QSOP24	C8051F800DK
C8051F808	16 kB	25	0.5	17	I^2C; SPI; UART	3	3	±2%	—	—	—	—	1	QFN20/ QSOP24	C8051F800DK
C8051F809	16 kB	25	0.5	13	I^2C; SPI; UART	3	3	±2%	—	12	—	—	1	SOIC16	C8051F800DK
C8051F810	16 kB	25	0.5	13	I^2C; SPI; UART	3	3	±2%	—	8	—	—	1	SOIC16	C8051F800DK
C8051F811	16 kB	25	0.5	13	I^2C; SPI; UART	3	3	±2%	—	—	—	—	1	SOIC16	C8051F800DK
C8051F805	16 kB	28	0.5	13	I^2C; SPI; UART	3	3	±2%	10-bit, 12-ch., 500 ksps	—	•	•	1	SOIC16	C8051F800DK
C8051F700	15 kB	25	0.5	54	EMIF; I^2C; SPI; UART	4	3	±2%	10-bit, 16-ch., 500 ksps	38	•	•	1	QFP64	C8051F700DK
C8051F701	15 kB	25	0.5	54	EMIF; I^2C; SPI; UART	4	3	±2%	—	38	—	—	1	QFP64	C8051F700DK
C8051F704	15 kB	25	0.5	39	I^2C; SPI; UART	4	3	±2%	10-bit, 12-ch., 500 ksps	27	•	•	1	QFN48/ QFP48	C8051F700DK
C8051F705	15 kB	25	0.5	39	I^2C; SPI; UART	4	3	±2%	—	27	—	—	1	QFN48/ QFP48	C8051F700DK
C8051F708	8 kB	25	0.5	54	EMIF; I^2C; SPI; UART	4	3	±2%	10-bit, 16-ch., 500 ksps	38	•	•	1	QFP64	C8051F700DK
C8051F709	8 kB	25	0.5	54	EMIF; I^2C; SPI; UART	4	3	±2%	—	38	—	—	1	QFP64	C8051F700DK
C8051F710	8 kB	25	0.5	54	EMIF; I^2C; SPI; UART	4	3	±2%	10-bit, 16-ch., 500 ksps	38	•	•	1	QFP64	C8051F700DK
C8051F711	8 kB	25	0.5	54	EMIF; I^2C; SPI; UART	4	3	±2%	—	39	—	—	1	QFP64	C8051F700DK
C8051F712	8 kB	25	0.5	39	I^2C; SPI; UART	4	3	±2%	10-bit, 12-ch., 500 ksps	27	•	•	1	QFN48/ QFP48	C8051F700DK
C8051F713	8 kB	25	0.5	39	I^2C; SPI; UART	4	3	±2%	—	27	—	—	1	QFN48/ QFP48	C8051F700DK
C8051F714	8 kB	25	0.5	39	I^2C; SPI; UART	4	3	±2%	10-bit, 12-ch., 500 ksps	27	•	•	1	QFN48/ QFP48	C8051F700DK
C8051F715	8 kB	25	0.5	39	I^2C; SPI; UART	4	3	±2%	—	27	—	—	1	QFN48/ QFP48	C8051F700DK
C8051F812	8 kB	25	0.5	17	I^2C; SPI; UART	3	3	±2%	10-bit, 16-ch., 500 ksps	16	•	•	1	QFN20/ QSOP24	C8051F800DK
C8051F813	8 kB	25	0.5	17	I^2C; SPI; UART	3	3	±2%	10-bit, 16-ch., 500 ksps	8	•	•	1	QSOP24	C8051F800DK
C8051F814	8 kB	25	0.5	17	I^2C; SPI; UART	3	3	±2%	10-bit, 16-ch., 500 ksps	—	•	•	1	QFN20/ QSOP24	C8051F800DK
C8051F815	8 kB	25	0.5	13	I^2C; SPI; UART	3	3	±2%	10-bit, 12-ch., 500 ksps	12	•	•	1	SOIC16	C8051F800DK
C8051F816	8 kB	25	0.5	13	I^2C; SPI; UART	3	3	±2%	10-bit, 12-ch., 500 ksps	8	•	•	1	SOIC16	C8051F800DK
C8051F817	8 kB	25	0.5	13	I^2C; SPI; UART	3	3	±2%	10-bit, 12-ch., 500 ksps	—	•	•	1	SOIC16	C8051F800DK
C8051F818	8 kB	25	0.5	17	I^2C; SPI; UART	3	3	±2%	—	16	—	—	1	QFN20/ QSOP24	C8051F800DK
C8051F819	8 kB	25	0.5	17	I^2C; SPI; UART	3	3	±2%	—	8	—	—	1	QFN20/ QSOP24	C8051F800DK
C8051F820	8 kB	25	0.5	17	I^2C; SPI; UART	3	3	±2%	—	—	—	—	1	QFN20/ QSOP24	C8051F800DK
C8051F821	8 kB	25	0.5	13	I^2C; SPI; UART	3	3	±2%	—	12	—	—	1	SOIC16	C8051F800DK
C8051F822	8 kB	25	0.5	13	I^2C; SPI; UART	3	3	±2%	—	8	—	—	1	SOIC16	C8051F800DK
C8051F823	8 kB	25	0.5	13	I^2C; SPI; UART	3	3	±2%	—	—	—	—	1	SOIC16	C8051F800DK
C8051F990	8 kB	25	0.5	16	I^2C; SPI; UART	4	3	±2%	12-bit, 9-ch., 75 ksps	13	•	•	1	QFN20	C8051F996DK
C8051F991	8 kB	25	0.5	16	I^2C; SPI; UART	4	3	±2%	—	13	—	—	1	QFN20	C8051F996DK
C8051F996	8 kB	25	0.5	17	I^2C; SPI; UART	4	3	±2%	12-bit, 10-ch., 75 ksps	14	•	•	1	QFN24/ QSOP24	C8051F996DK
C8051F997	8 kB	25	0.5	17	I^2C; SPI; UART	4	3	±2%	—	14	—	—	1	QFN24/ QSOP24	C8051F996DK
C8051F813	8 kB	25	0.5	17	I^2C; SPI; UART	3	3	±2%	10-bit, 16-ch., 500 ksps	8	•	•	1	QSOP24	C8051F800DK

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	TOUCH CH.	TEMP SENSOR	VREF	COMP.	PACKAGE	DEV KIT
C8051F824	8 kB	25	0.25	13	I²C; SPI; UART	3	3	±2%	10-bit, 12-ch., 500 ksps	12	•	•	1	SOIC16	C8051F800DK
C8051F825	8 kB	25	0.25	13	I²C; SPI; UART	3	3	±2%	10-bit, 12-ch., 500 ksps	8	•	•	1	SOIC16	C8051F800DK
C8051F826	8 kB	25	0.25	13	I²C; SPI; UART	3	3	±2%	10-bit, 12-ch., 500 ksps	—	•	•	1	SOIC16	C8051F800DK
C8051F827	8 kB	25	0.25	13	I²C; SPI; UART	3	3	±2%	—	12	—	—	1	SOIC16	C8051F800DK
C8051F828	8 kB	25	0.25	13	I²C; SPI; UART	3	3	±2%	—	8	—	—	1	SOIC16	C8051F800DK
C8051F829	8 kB	25	0.25	13	I²C; SPI; UART	3	3	±2%	—	—	—	—	1	SOIC16	C8051F800DK
C8051F830	4 kB	25	0.25	13	I²C; SPI; UART	3	3	±2%	10-bit, 12-ch., 500 ksps	12	•	•	1	SOIC16	C8051F800DK
C8051F831	4 kB	25	0.25	13	I²C; SPI; UART	3	3	±2%	10-bit, 12-ch., 500 ksps	8	•	•	1	SOIC16	C8051F800DK
C8051F832	4 kB	25	0.25	13	I²C; SPI; UART	3	3	±2%	10-bit, 12-ch., 500 ksps	—	•	•	1	SOIC16	C8051F800DK
C8051F833	4 kB	25	0.25	13	I²C; SPI; UART	3	3	±2%	—	12	—	—	1	SOIC16	C8051F800DK
C8051F834	4 kB	25	0.25	13	I²C; SPI; UART	3	3	±2%	—	8	—	—	1	SOIC16	C8051F800DK
C8051F835	4 kB	25	0.25	13	I²C; SPI; UART	3	3	±2%	—	—	—	—	1	SOIC16	C8051F800DK

Small Form Factor MCUs

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMM.	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	TEMP SENSOR	VREF	COMP.	OTHER	PACKAGE	OTP-EPROM VERSION	DEV KIT
C8051F388	64 kB	50	4.25	40	I²C; SPI; 2 x I²C; UART; 2 x UART	6	5	±1.5%	10-bit, 32-ch., 500 ksps	—	•	•	2	Ext, Mem. IF	TQFP48		C8051F380DK
C8051F389	64 kB	50	4.25	25	I²C; SPI; 2 x I²C; UART; 2 x UART	6	5	±1.5%	10-bit, 21-ch., 500 ksps	—	•	•	2	—	LQFP32/ QFN32		C8051F380DK
C8051F360	32 kB	100	1.25	39	EMIF; I²C; SPI; UART	4	6	±2%	10-bit, 21-ch., 200 ksps	10-bit, 1-ch.	•	•	2	16x16 MAC	TQFP48		C8051F360DK
C8051F361	32 kB	100	1.25	27	I²C; SPI; UART	4	6	±2%	10-bit, 21-ch., 200 ksps	10-bit, 1-ch.	•	•	2	16x16 MAC	LQFP32		C8051F360DK
C8051F362	32 kB	100	1.25	24	I²C; SPI; UART	4	6	±2%	10-bit, 17-ch., 200 ksps	10-bit, 1-ch.	•	•	2	16x16 MAC	QFN28		C8051F360DK
C8051F363	32 kB	100	1.25	39	EMIF; I²C; SPI; UART	4	6	±2%	—	—	—	—	2	16x16 MAC	TQFP48		C8051F360DK
C8051F364	32 kB	100	1.25	29	I²C; SPI; UART	4	6	±2%	—	—	—	—	2	16x16 MAC	LQFP32		C8051F360DK
C8051F365	32 kB	100	1.25	25	I²C; SPI; UART	4	6	±2%	—	—	—	—	2	16x16 MAC	QFN28		C8051F360DK
C8051F38A	32 kB	50	2.25	40	I²C; SPI; 2 x I²C; UART; 2 x UART	6	5	±1.5%	10-bit, 32-ch., 500 ksps	—	•	•	2	Ext, Mem. IF	TQFP48		C8051F380DK
C8051F38B	32 kB	50	2.25	25	I²C; SPI; 2 x I²C; UART; 2 x UART	6	5	±1.5%	10-bit, 21-ch., 500 ksps	—	•	•	2	—	LQFP32/ QFN32		C8051F380DK
C8051F366	32 kB	50	1.25	29	I²C; SPI; UART	4	6	±2%	10-bit, 21-ch., 200 ksps	10-bit, 1-ch.	•	•	2	16x16 MAC	LQFP32		C8051F360DK
C8051F367	32 kB	50	1.25	25	I²C; SPI; UART	4	6	±2%	10-bit, 17-ch., 200 ksps	10-bit, 1-ch.	•	•	2	16x16 MAC	QFN28		C8051F360DK
C8051F410	32 kB	50	2.25	24	I²C; SPI; UART	4	6	±2%	12-bit, 24-ch., 200 ksps	12-bit, 2-ch.	•	•	2	RTC; VREG	LQFP32		C8051F410DK
C8051F411	32 kB	50	2.25	20	I²C; SPI; UART	4	6	±2%	12-bit, 20-ch., 200 ksps	12-bit, 2-ch.	•	•	2	RTC; VREG	QFN28		C8051F410DK
C8051F368	16 kB	50	1.25	29	I²C; SPI; UART	4	6	±2%	10-bit, 21-ch., 200 ksps	10-bit, 1-ch.	•	•	2	16x16 MAC	LQFP32		C8051F360DK
C8051F369	16 kB	50	1.25	25	I²C; SPI; UART	4	6	±2%	10-bit, 17-ch., 200 ksps	10-bit, 1-ch.	•	•	2	16x16 MAC	QFN28		C8051F360DK
C8051F370	16 kB	50	1	21	I²C; SPI; 2 x I²C; UART	6	3	±2%	10-bit, 16-ch., 500 ksps	10-bit, 2-ch.	•	•	2	LFO; VREG	QFN24		C8051F370DK
C8051F371	16 kB	50	1	21	I²C; SPI; 2 x I²C; UART	6	3	±2%	—	—	—	—	2	LFO; VREG	QFN24		C8051F370DK

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMM.	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	TEMP SENSOR	VREF	COMP.	OTHER	PACKAGE	OTP-EPROM VERSION	DEV KIT
C8051F390	16 kB	50	1	21	I^2C; SPI; 2 x I^2C; UART	6	3	±2%	10-bit, 16-ch., 500 ksps	10-bit, 2-ch.	•	•	1	LFO; VREG	QFN24		C8051F390DK
C8051F391	16 kB	50	1	21	I^2C; SPI; 2 x I^2C; UART	6	3	±2%	—	—	—	—	1	LFO; VREG	QFN24		C8051F390DK
C8051F392	16 kB	50	1	17	I^2C; SPI; 2 x I^2C; UART	6	3	±2%	10-bit, 16-ch., 500 ksps	10-bit, 2-ch.	•	•	1	LFO; VREG	QFN20		C8051F390DK
C8051F393	16 kB	50	1	17	I^2C; SPI; 2 x I^2C; UART	6	3	±2%	—	—	—	—	1	LFO; VREG	QFN20		C8051F390DK
C8051F412	16 kB	50	2.25	24	I^2C; SPI; UART	4	6	±2%	12-bit, 24-ch., 200 ksps	12-bit, 2-ch.	•	•	2	RTC; VREG	LQFP32		C8051F410DK
C8051F413	16 kB	50	2.25	20	I^2C; SPI; UART	4	6	±2%	12-bit, 20-ch., 200 ksps	12-bit, 2-ch.	•	•	2	RTC; VREG	QFN28		C8051F410DK
C8051F310	16 kB	25	1.25	29	I^2C; SPI; UART	4	5	±2%	10-bit, 21-ch., 200 ksps	—	•	—	2	—	LQFP32	T610	C8051F310DK
C8051F311	16 kB	25	1.25	25	I^2C; SPI; UART	4	5	±2%	10-bit, 17-ch., 200 ksps	—	•	—	2	—	QFN28	T611	C8051F310DK
C8051F316	16 kB	25	1.25	21	I^2C; SPI; UART	4	5	±2%	10-bit, 13-ch.,200 ksps	—	•	—	2	—	QFN24	T616	C8051F310DK
C8051F317	16 kB	25	1.25	21	I^2C; SPI; UART	4	5	±2%	—	—	—	—	2	—	QFN24	T617	C8051F310DK
C8051F336	16 kB	25	0.75	17	I^2C; SPI; UART	4	3	±2%	10-bit, 16-ch., 200 ksps	10-bit, 1-ch.	•	•	1	LFO	QFN20		C8051F336DK
C8051F337	16 kB	25	0.75	17	I^2C; SPI; UART	4	3	±2%	—	—			1	LFO	QFN20		C8051F336DK
C8051F338	16 kB	25	0.75	21	I^2C; SPI; UART	4	3	±2%	10-bit, 16-ch., 200 ksps	10-bit, 1-ch.	•	•	1	LFO	QFN24		C8051F336DK
C8051F339	16 kB	25	0.75	21	I^2C; SPI; UART	4	3	±2%	—	—	—	—	1	LFO	QFN24		C8051F336DK
C8051F206	8 kB	25	1.25	32	SPI; UART	3	—	±20%	12-bit, 32-ch., 100 ksps	—	—	—	2	—	TQFP48		C8051F206DK
C8051F220	8 kB	25	0.25	32	SPI; UART	3	—	±20%	8-bit, 32-ch., 100 ksps	—	—	—	2	—	TQFP48		C8051F226DK
C8051F221	8 kB	25	0.25	22	SPI; UART	3	—	±20%	8-bit, 22-ch., 100 ksps	—	—	—	2	—	LQFP32		C8051F226DK
C8051F226	8 kB	25	1.25	32	SPI; UART	3	—	±20%	8-bit, 32-ch., 100 ksps	—	—	—	2	—	TQFP48		C8051F226DK
C8051F230	8 kB	25	0.25	32	SPI; UART	3	—	±20%	—	—	—	—	2	—	TQFP48		C8051F226DK
C8051F231	8 kB	25	0.25	22	SPI; UART	3	—	±20%	—	—	—	—	2	—	LQFP32		C8051F226DK
C8051F236	8 kB	25	1.25	32	SPI; UART	3	—	±20%	—	—	—	—	2	—	TQFP48		C8051F226DK
C8051F300	8 kB	25	0.25	8	I^2C; UART	3	3	±2%	8-bit, 8-ch., 500 ksps	—	•	—	1	—	QFN11/ SOIC14	T600	C8051F300DK
C8051F301	8 kB	25	0.25	8	I^2C; UART	3	3	±2%	—	—	—	—	1	—	QFN11/ SOIC14	T601	C8051F300DK
C8051F302	8 kB	25	0.25	8	I^2C; UART	3	3	±20%	8-bit, 8-ch., 500 ksps	—	•	—	1	—	QFN11/ SOIC14	T600	C8051F300DK
C8051F303	8 kB	25	0.25	8	I^2C; UART	3	3	±20%	—	—	—	—	1	—	QFN11/ SOIC14	T601	C8051F300DK
C8051F312	8 kB	25	1.25	29	I^2C; SPI; UART	4	5	±2%	10-bit, 21-ch., 200 ksps	—	•	—	2	—	LQFP32	T612	C8051F310DK
C8051F313	8 kB	25	1.25	25	I^2C; SPI; UART	4	5	±2%	10-bit, 17-ch., 200 ksps	—	•	—	2	—	QFN28	T613	C8051F310DK
C8051F314	8 kB	25	1.25	29	I^2C; SPI; UART	4	5	±2%	—	—	—	—	2	—	LQFP32	T614	C8051F310DK
C8051F315	8 kB	25	1.25	25	I^2C; SPI; UART	4	5	±2%	—	—	—	—	2	—	QFN28	T615	C8051F310DK
C8051F330	8 kB	25	0.75	17	I^2C; SPI; UART	4	3	±2%	10-bit, 16-ch., 200 ksps	10-bit, 1-ch.	•	•	1	LFO	QFN20/ DIP20	T630	C8051F330DK
C8051F331	8 kB	25	0.75	17	I^2C; SPI; UART	4	3	±2%	—	—	—	—	1	LFO	QFN20	T631	C8051F330DK
C8051F850	8 kB	25	0.50	16	I^2C; SPI; UART	4	3	±2%	12-bit, 15-ch., 200 ksps	—	•	•	2	LFO	QFN20/ QSOP24		C8051F850DK
C8051F853	8 kB	25	0.50	16	I^2C; SPI; UART	4	3	±2%	—	—	—	—	2	LFO	QFN20/ QSOP24		C8051F850DK

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMM.	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	TEMP SENSOR	VREF	COMP.	OTHER	PACKAGE	OTP-EPROM VERSION	DEV KIT
C8051F860	8 kB	25	0.50	13	I^2C; SPI; UART	4	3	±2%	12-bit, 12-ch., 200 ksps	—	•	•	2	LFO	SOIC16		C8051F850DK
C8051F863	8 kB	25	0.50	13	I^2C; SPI; UART	4	3	±2%	—	—	—	—	2	LFO	SOIC16		C8051F850DK
C8051F374	8 kB	50	1	21	I^2C; SPI; 2 x I^2C; UART	6	3	±2%	10-bit, 16-ch., 500 ksps	10-bit, 2-ch.	•	•	1	LFO; VREG	QFN24		C8051F370DK
C8051F375	8 kB	50	1	21	I^2C; SPI; 2 x I^2C; UART	6	3	±2%	—	—	—	—	1	LFO; VREG	QFN24		C8051F370DK
C8051F394	8 kB	50	1	21	I^2C; SPI; 2 x I^2C; UART	6	3	±2%	10-bit, 16-ch., 500 ksps	10-bit, 2-ch.	•	•	1	LFO; VREG	QFN24		C8051F390DK
C8051F395	8 kB	50	1	21	I^2C; SPI; 2 x I^2C; UART	6	3	±2%	—	—	—	—	1	LFO; VREG	QFN24		C8051F390DK
C8051F396	8 kB	50	1	17	I^2C; SPI; 2 x I^2C; UART	6	3	±2%	10-bit, 16-ch., 500 ksps	10-bit, 2-ch.	•	•	1	LFO; VREG	QFN20		C8051F390DK
C8051F397	8 kB	50	1	17	I^2C; SPI; 2 x I^2C; UART	6	3	±2%	—	—	—	—	1	LFO; VREG	QFN20		C8051F390DK
C8051F304	4 kB	25	0.25	8	I^2C; UART	3	3	±20%	—	—	—	—	1	—	QFN11/ SOIC14	T603	C8051F300DK
C8051F332	4 kB	25	0.75	17	I^2C; SPI; UART	4	3	±2%	10-bit, 16-ch., 200 ksps	—	•	•	1	LFO	QFN20	T632	C8051F330DK
C8051F333	4 kB	25	0.75	17	I^2C; SPI; UART	4	3	±2%	—	—	—	—	1	LFO	QFN20	T633	C8051F330DK
C8051F851	4 kB	25	0.50	16	I^2C; SPI; UART	4	3	±2%	12-bit, 15-ch., 200 ksps	—	•	•	2	LFO	QFN20/ QSOP24		C8051F850DK
C8051F854	4 kB	25	0.50	16	I^2C; SPI; UART	4	3	±2%	—	—	—	—	2	LFO	QFN20/ QSOP24		C8051F850DK
C8051F861	4 kB	25	0.50	13	I^2C; SPI; UART	4	3	±2%	12-bit, 12-ch., 200 ksps	—	•	•	2	LFO	SOIC16		C8051F850DK
C8051F864	4 kB	25	0.50	13	I^2C; SPI; UART	4	3	±2%	—	—	—	—	2	LFO	SOIC16		C8051F850DK
C8051F398	4 kB	50	1	17	I^2C; SPI; 2 x I^2C; UART	6	3	±2%	10-bit, 16-ch., 500 ksps	10-bit, 2-ch.	•	•	1	LFO; VREG	QFN20		C8051F390DK
C8051F399	4 kB	50	1	17	I^2C; SPI; 2 x I^2C; UART	6	3	±2%	—	—	—	—	1	LFO; VREG	QFN20		C8051F390DK
C8051F305	2 kB	25	0.25	8	I^2C; UART	3	3	±20%	—	—	—	—o	1	—	QFN11/ SOIC14	T605	C8051F300DK
C8051F334	2 kB	25	0.75	17	I^2C; SPI; UART	4	3	±2%	10-bit, 16-ch., 200 ksps	—	•	•	1	LFO	QFN20	T634	C8051F330DK
C8051F335	2 kB	25	0.75	17	I^2C; SPI; UART	4	3	±2%	—	—	—	—	1	LFO	QFN20	T635	C8051F330DK
C8051F852	2 kB	25	0.25	16	I^2C; SPI; UART	4	3	±2%	12-bit, 15-ch., 200 ksps	—	•	•	2	LFO	QFN20/ QSOP24		C8051F850DK
C8051F855	2 kB	25	0.25	16	I^2C; SPI; UART	4	3	±2%	—	—	—	—	2	LFO	QFN20/ QSOP24		C8051F850DK
C8051F862	2 kB	25	0.25	13	I^2C; SPI; UART	4	3	±2%	12-bit, 12-ch., 200 ksps	—	•	•	2	LFO	SOIC16		C8051F850DK
C8051F865	2 kB	25	0.25	13	I^2C; SPI; UART	4	3	±2%	—	—	—	—	2	LFO	SOIC16		C8051F850DK

Industrial and Automotive Qualified MCUs

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	TEMP SENSOR	VREF	COMP.	OTHER	PACKAGE	DEV KIT
C8051F580	128 kB	50	8	40	CAN; EMIF; I^2C; LIN; SPI; UART; 2 x UART	6	12	±0.5%	12-bit, 32-ch., 200 ksps	•	•	3	-40 to 125 °C	QFN48/ QFP48	C8051F580DK
C8051F581	128 kB	50	8	40	EMIF; I^2C; SPI; UART; 2 x UART	6	12	±0.5%	12-bit, 32-ch., 200 ksps	•	•	3	-40 to 125 °C	QFN48/ QFP48	C8051F580DK
C8051F582	128 kB	50	8	25	CAN; I^2C; LIN; SPI; UART; 2 x UART	6	12	±0.5%	12-bit, 25-ch., 200 ksps	•	•	3	-40 to 125 °C	QFN32/ QFP32	C8051F580DK
C8051F583	128 kB	50	8	25	I^2C; SPI; UART; 2 x UART	6	12	±0.5%	12-bit, 25-ch., 200 ksps	•	•	3	-40 to 125 °C	QFN32/ QFP32	C8051F580DK

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	TEMP SENSOR	VREF	COMP.	OTHER	PACKAGE	DEV KIT
C8051F588	128 kB	50	8	33	CAN; EMIF; I²C; LIN; SPI; UART; 2 x UART	6	12	±0.5%	12-bit, 32-ch., 200 ksps	•	•	3	-40 to 125 °C	QFN40	C8051F580DK
C8051F589	128 kB	50	8	33	EMIF; I²C; SPI; UART; 2 x UART	6	12	±0.5%	12-bit, 32-ch., 200 ksps	•	•	3	-40 to 125 °C	QFN40	C8051F580DK
C8051F584	96 kB	50	8	40	CAN; EMIF; I²C; LIN; SPI; UART; 2 x UART	6	12	±0.5%	12-bit, 32-ch., 200 ksps	•	•	3	-40 to 125 °C	QFN48/ QFP48	C8051F580DK
C8051F585	96 kB	50	8	40	EMIF; I²C; SPI; UART; 2 x UART	6	12	±0.5%	12-bit, 32-ch., 200 ksps	•	•	3	-40 to 125 °C	QFN48/ QFP48	C8051F580DK
C8051F586	96 kB	50	8	25	CAN; I²C; LIN; SPI; UART; 2 x UART	6	12	±0.5%	12-bit, 25-ch., 200 ksps	•	•	3	-40 to 125 °C	QFN32/ QFP32	C8051F580DK
C8051F587	96 kB	50	8	25	I²C; SPI; UART; 2 x UART	6	12	±0.5%	12-bit, 25-ch., 200 ksps	•	•	3	-40 to 125 °C	QFN32/ QFP32	C8051F580DK
C8051F590	96 kB	50	8	33	CAN; EMIF; I²C; LIN; SPI; UART; 2 x UART	6	12	±0.5%	12-bit, 32-ch., 200 ksps	•	•	3	-40 to 125 °C	QFN40	C8051F580DK
C8051F591	96 kB	50	8	33	EMIF; I²C; SPI; UART; 2 x UART	6	12	±0.5%	12-bit, 32-ch., 200 ksps	•	•	3	-40 to 125 °C	QFN40	C8051F580DK
C8051F500	64 kB	50	4.25	40	CAN; EMIF; I²C; LIN; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN48/ QFP48	C8051F500DK
C8051F501	64 kB	50	4.25	40	EMIF; I²C; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN48/ QFP48	C8051F500DK
C8051F502	64 kB	50	4.25	25	CAN; EMIF; I²C; LIN; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F500DK
C8051F503	64 kB	50	4.25	25	I²C; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F500DK
C8051F508	64 kB	50	4.25	33	CAN; EMIF; I²C; LIN; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN40	C8051F500DK
C8051F509	64 kB	50	4.25	33	EMIF; I²C; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN40	C8051F500DK
C8051F504	32 kB	50	4.25	40	CAN; EMIF; I²C; LIN; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN48/ QFP48	C8051F500DK
C8051F505	32 kB	50	4.25	40	EMIF; I²C; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN48/ QFP48	C8051F500DK
C8051F506	32 kB	50	4.25	25	CAN; I²C; LIN; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F500DK
C8051F507	32 kB	50	4.25	25	I²C; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F500DK
C8051F510	32 kB	50	4.25	33	CAN; EMIF; I²C; LIN; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN40	C8051F500DK
C8051F511	32 kB	50	4.25	33	EMIF; I²C; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN40	C8051F500DK
C8051F550	32 kB	50	2.25	18	CAN; I²C; LIN; SPI; UART	4	6	±0.5%	12-bit, 18-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN24	C8051F560DK
C8051F551	32 kB	50	2.25	18	CAN; I²C; SPI; UART	4	6	±0.5%	12-bit, 18-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN24	C8051F560DK
C8051F552	32 kB	50	2.25	18	I²C; LIN; SPI; UART	4	6	±0.5%	12-bit, 18-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN24	C8051F560DK
C8051F553	32 kB	50	2.25	18	I²C; SPI; UART	4	6	±0.5%	12-bit, 18-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN24	C8051F560DK
C8051F560	32 kB	50	2.25	25	CAN; I²C; LIN; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F560DK
C8051F561	32 kB	50	2.25	25	CAN; I²C; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F560DK
C8051F562	32 kB	50	2.25	25	I²C; LIN; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F560DK
C8051F563	32 kB	50	2.25	25	I²C; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps		•	2	-40 to 125 °C	QFN32/ QFP32	C8051F560DK
C8051F568	32 kB	50	2.25	33	CAN; EMIF; I²C; LIN; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN40	C8051F560DK
C8051F569	32 kB	50	2.25	33	CAN; EMIF; I²C; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN40	C8051F560DK
C8051F570	32 kB	50	2.25	33	EMIF; I²C; LIN; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN40	C8051F560DK
C8051F571	32 kB	50	2.25	33	EMIF; I²C; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN40	C8051F560DK
C8051F554	16 kB	50	2.25	18	CAN; I²C; LIN; SPI; UART	4	6	±0.5%	12-bit, 18-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN24	C8051F560DK

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	TEMP SENSOR	VREF	COMP.	OTHER	PACKAGE	DEV KIT
C8051F555	16 kB	50	2.25	18	CAN; I^2C; SPI; UART	4	6	±0.5%	12-bit, 18-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN24	C8051F560DK
C8051F556	16 kB	50	2.25	18	I^2C; LIN; SPI; UART	4	6	±0.5%	12-bit, 18-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN24	C8051F560DK
C8051F557	16 kB	50	2.25	18	I^2C; SPI; UART	4	6	±0.5%	12-bit, 18-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN24	C8051F560DK
C8051F564	16 kB	50	2.25	25	CAN; I^2C; LIN; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F560DK
C8051F565	16 kB	50	2.25	25	CAN; I^2C; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F560DK
C8051F566	16 kB	50	2.25	25	I^2C; LIN; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F560DK
C8051F567	16 kB	50	2.25	25	I^2C; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F560DK
C8051F572	16 kB	50	2.25	33	CAN; EMIF; I^2C; LIN; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN40	C8051F560DK
C8051F573	16 kB	50	2.25	33	CAN; EMIF; I^2C; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN40	C8051F560DK
C8051F574	16 kB	50	2.25	33	EMIF; I^2C; LIN; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN40	C8051F560DK
C8051F575	16 kB	50	2.25	33	EMIF; I^2C; SPI; UART	4	6	±0.5%	12-bit, 32-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN40	C8051F560DK
C8051F540	16 kB	50	1.25	25	I^2C; LIN; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F540DK
C8051F541	16 kB	50	1.25	25	I^2C; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F540DK
C8051F542	16 kB	50	1.25	18	I^2C; LIN; SPI; UART	4	6	±0.5%	12-bit, 18-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN24	C8051F540DK
C8051F543	16 kB	50	1.25	18	I^2C; SPI; UART	4	6	±0.5%	12-bit, 18-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN24	C8051F540DK
C8051F544	8 kB	50	1.25	25	I^2C; LIN; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F540DK
C8051F545	8 kB	50	1.25	25	I^2C; SPI; UART	4	6	±0.5%	12-bit, 25-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN32/ QFP32	C8051F540DK
C8051F546	8 kB	50	1.25	18	I^2C; LIN; SPI; UART	4	6	±0.5%	12-bit, 18-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN24	C8051F540DK
C8051F547	8 kB	50	1.25	18	I^2C; SPI; UART	4	6	±0.5%	12-bit, 18-ch., 200 ksps	•	•	2	-40 to 125 °C	QFN24	C8051F540DK
C8051F531	8 kB	26	0.25	16	SPI; UART	3	3	±0.5%	12-bit, 16-ch., 200 ksps	•	•	1	-40 to 125 °C	QFN20	C8051F530ADK
C8051F520	8 kB	25	0.25	6	LIN; SPI; UART	3	3	±0.5%	12-bit, 6-ch., 200 ksps	•	•	1	-40 to 125 °C	DFN10	C8051F530ADK
C8051F521	8 kB	25	0.25	6	SPI; UART	3	3	±0.5%	12-bit, 6-ch., 200 ksps	•	•	1	-40 to 125 °C	DFN10	C8051F530ADK
C8051F530	8 kB	25	0.25	16	LIN; SPI; UART	3	3	±0.5%	12-bit, 16-ch., 200 ksps	•	•	1	-40 to 125 °C	QFN20/ TSSOP20	C8051F530ADK
C8051F531	8 kB	26	0.25	16	SPI; UART	3	3	±0.5%	12-bit, 16-ch., 200 ksps	•	•	1	-40 to 125 °C	QFN20	C8051F530ADK
C8051F523	4 kB	25	0.25	6	LIN; SPI; UART	3	3	±0.5%	12-bit, 6-ch., 200 ksps	•	•	1	-40 to 125 °C	DFN10	C8051F530ADK
C8051F524	4 kB	25	0.25	6	SPI; UART	3	3	±0.5%	12-bit, 6-ch., 200 ksps	•	•	1	-40 to 125 °C	DFN10	C8051F530ADK
C8051F533	4 kB	25	0.25	16	LIN; SPI; UART	3	3	±0.5%	12-bit, 16-ch., 200 ksps	•	•	1	-40 to 125 °C	QFN20/ TSSOP20	C8051F530ADK
C8051F534	4 kB	25	0.25	16	SPI; UART	3	3	±0.5%	12-bit, 16-ch., 200 ksps	•	•	1	-40 to 125 °C	QFN20/ TSSOP20	C8051F530ADK
C8051F526	2 kB	25	0.25	6	LIN; SPI; UART	3	3	±0.5%	12-bit, 6-ch., 200 ksps	•	•	1	-40 to 125 °C	DFN10	C8051F530ADK
C8051F527	2 kB	25	0.25	6	SPI; UART	3	3	±0.5%	12-bit, 6-ch., 200 ksps	•	•	1	-40 to 125 °C	DFN10	C8051F530ADK
C8051F536	2 kB	25	0.25	16	LIN; SPI; UART	3	3	±0.5%	12-bit, 16-ch., 200 ksps	•	•	1	-40 to 125 °C	QFN20/ TSSOP20	C8051F530ADK
C8051F537	2 kB	25	0.25	16	SPI; UART	3	3	±0.5%	12-bit, 16-ch., 200 ksps	•	•	1	-40 to 125 °C	QFN20/ TSSOP20	C8051F530ADK

High Performance Analog-Intensive MCUs

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	ADC2	DAC	TEMP SENSOR	VREF	COMP.	PACKAGE	DEV KIT
C8051F120	128 kB	100	8	64	EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	12-bit, 8-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	2	TQFP100	C8051F120DK
C8051F121	128 kB	100	8	32	EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	12-bit, 8-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	2	TQFP64	C8051F120DK
C8051F122	128 kB	100	8	64	EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	10-bit, 8-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	2	TQFP100	C8051F120DK
C8051F123	128 kB	100	8	32	EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	10-bit, 8-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	2	TQFP64	C8051F120DK
C8051F130	128 kB	100	8	64	EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	10-bit, 8-ch., 100 ksps	—	—	•	•	2	TQFP100	C8051F120DK
C8051F131	128 kB	100	8	32	EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	10-bit, 8-ch., 100 ksps	—	—	•	•	2	TQFP64	C8051F120DK
C8051F124	128 kB	50	8	64	EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	12-bit, 8-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	2	TQFP100	C8051F120DK
C8051F125	128 kB	50	8	32	EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	12-bit, 8-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	2	TQFP64	C8051F120DK
C8051F126	128 kB	50	8	64	EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	10-bit, 8-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	2	TQFP100	C8051F120DK
C8051F127	128 kB	50	8	32	EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	10-bit, 8-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	2	TQFP64	C8051F120DK
C8051F132	64 kB	100	8	64	EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	10-bit, 8-ch., 100 ksps	—	—	•	•	2	TQFP100	C8051F120DK
C8051F133	64 kB	100	8	32	EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	10-bit, 8-ch., 100 ksps	—	—	•	•	2	TQFP64	C8051F120DK
C8051F020	64 kB	25	4.25	64	EMIF; I^2C; SPI; UART; 2 x UART	5	5	±20%	12-bit, 8-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	2	TQFP100	C8051F020DK
C8051F021	64 kB	25	4.25	32	EMIF; I^2C; SPI; UART; 2 x UART	5	5	±20%	12-bit, 8-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	2	TQFP64	C8051F020DK
C8051F022	64 kB	25	4.25	64	EMIF; I^2C; SPI; UART; 2 x UART	5	5	±20%	10-bit, 8-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	2	TQFP100	C8051F020DK
C8051F023	64 kB	25	4.25	32	EMIF; I^2C; SPI; UART; 2 x UART	5	5	±20%	10-bit, 8-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	2	TQFP64	C8051F020DK
C8051F040	64 kB	25	4.25	64	CAN; EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	12-bit, 13-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	3	TQFP100	C8051F040DK
C8051F041	64 kB	25	4.25	32	CAN; EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	12-bit, 13-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	3	TQFP64	C8051F040DK
C8051F042	64 kB	25	4.25	64	CAN; EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	10-bit, 13-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	3	TQFP100	C8051F040DK
C8051F043	64 kB	25	4.25	32	CAN; EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	10-bit, 13-ch., 100 ksps	8-bit, 8-ch., 500 ksps	12-bit, 2-ch.	•	•	3	TQFP64	C8051F040DK
C8051F044	64 kB	25	4.25	64	CAN; EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	10-bit, 13-ch., 100 ksps	—	—	•	•	3	TQFP100	C8051F040DK
C8051F045	64 kB	25	4.25	32	CAN; EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	10-bit, 13-ch., 100 ksps	—	—	•	•	3	TQFP64	C8051F040DK
C8051F060	64 kB	25	4.25	59	CAN; EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	16-bit, 1-ch., 1 Msps	16-bit,1-ch., 1 Msps	12-bit, 2-ch.	•	•	3	TQFP100	C8051F060DK
C8051F061	64 kB	25	4.25	24	CAN; I^2C; SPI; UART; 2 x UART	5	6	±2%	16-bit, 1-ch., 1 Msps	16-bit,1-ch., 1 Msps	12-bit, 2-ch.	•	•	3	TQFP64	C8051F060DK
C8051F062	64 kB	25	4.25	59	CAN; EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	16-bit, 1-ch., 1 Msps	16-bit,1-ch., 1 Msps	12-bit, 2-ch.	•	•	3	TQFP100	C8051F060DK
C8051F063	64 kB	25	4.25	24	CAN; I^2C; SPI; UART; 2 x UART	5	6	±2%	16-bit, 1-ch., 1 Msps	16-bit,1-ch., 1 Msps	12-bit, 2-ch.	•	•	3	TQFP64	C8051F060DK
C8051F064	64 kB	25	4.25	59	EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	16-bit, 1-ch., 1 Msps	16-bit,1-ch., 1 Msps	—	—	•	3	TQFP100	C8051F060DK
C8051F065	64 kB	25	4.25	24	I^2C; SPI; UART; 2 x UART	5	6	±2%	16-bit, 1-ch., 1 Msps	16-bit,1-ch., 1 Msps	—	—	•	3	TQFP64	C8051F060DK
C8051F046	32 kB	25	4.25	64	CAN; EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	10-bit, 13-ch., 100 ksps	—	—	•	•	3	TQFP100	C8051F040DK
C8051F047	32 kB	25	4.25	32	CAN; EMIF; I^2C; SPI; UART; 2 x UART	5	6	±2%	10-bit, 13-ch., 100 ksps	—	—	•	•	3	TQFP64	C8051F040DK

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	ADC2	DAC	TEMP SENSOR	VREF	COMP.	PACKAGE	DEV KIT
C8051F066	32 kB	25	4.25	59	EMIF; I²C; SPI; UART; 2 x UART	5	6	±2%	16-bit, 1-ch., 1 Msps	16-bit,1-ch., 1 Msps	—	—	•	3	TQFP100	C8051F060DK
C8051F067	32 kB	25	4.25	24	I²C; SPI; UART; 2 x UART	5	6	±2%	16-bit, 1-ch., 1 Msps	16-bit,1-ch., 1 Msps	—	—	•	3	TQFP64	C8051F060DK
C8051F005	32 kB	25	2.25	32	I²C; SPI; UART	4	5	±20%	12-bit, 8-ch., 100 ksps	—	12-bit, 2-ch.	•	•	2	TQFP64	C8051F005DK
C8051F006	32 kB	25	2.25	16	I²C; SPI; UART	4	5	±20%	12-bit, 8-ch., 100 ksps	—	12-bit, 2-ch.	•	•	2	TQFP48	C8051F005DK
C8051F007	32 kB	25	2.25	8	I²C; SPI; UART	4	5	±20%	12-bit, 4-ch., 100 ksps	—	12-bit, 2-ch.	•	•	1	LQFP32	C8051F005DK
C8051F015	32 kB	25	2.25	32	I²C; SPI; UART	4	5	±20%	10-bit, 8-ch., 100 ksps	—	12-bit, 2-ch.	•	•	2	TQFP64	C8051F005DK
C8051F016	32 kB	25	2.25	16	I²C; SPI; UART	4	5	±20%	10-bit, 8-ch., 100 ksps	—	12-bit, 2-ch.	•	•	2	TQFP48	C8051F005DK
C8051F017	32 kB	25	2.25	8	I²C; SPI; UART	4	5	±20%	10-bit, 4-ch., 100 ksps	—	12-bit, 2-ch.	•	•	1	LQFP32	C8051F005DK
C8051F000	32 kB	20	0.25	32	I²C; SPI; UART	4	5	±20%	12-bit, 8-ch., 100 ksps	—	12-bit, 2-ch.	•	•	2	TQFP64	C8051F005DK
C8051F001	32 kB	20	0.25	16	I²C; SPI; UART	4	5	±20%	12-bit, 8-ch., 100 ksps	—	12-bit, 2-ch.	•	•	2	TQFP48	C8051F005DK
C8051F002	32 kB	20	0.25	8	I²C; SPI; UART	4	5	±20%	12-bit, 4-ch., 100 ksps	—	12-bit, 2-ch.	•	•	1	LQFP32	C8051F005DK
C8051F010	32 kB	20	0.25	32	I²C; SPI; UART	4	5	±20%	10-bit, 8-ch., 100 ksps	—	12-bit, 2-ch.	•	•	2	TQFP64	C8051F005DK
C8051F011	32 kB	20	0.25	16	I²C; SPI; UART	4	5	±20%	10-bit, 8-ch., 100 ksps	—	12-bit, 2-ch.	•	•	2	TQFP48	C8051F005DK
C8051F012	32 kB	20	0.25	8	I²C; SPI; UART	4	5	±20%	10-bit, 4-ch., 100 ksps	—	12-bit, 2-ch.	•	•	1	LQFP32	C8051F005DK
C8051F018	16 kB	25	1.25	32	I²C; SPI; UART	4	5	±20%	10-bit, 8-ch., 100 ksps	—	—	•	•	2	TQFP64	C8051F005DK
C8051F019	16 kB	25	1.25	16	I²C; SPI; UART	4	5	±20%	10-bit, 8-ch., 100 ksps	—	—	•	•	2	TQFP48	C8051F005DK
C8051F350	8 kB	50	0.75	17	I²C; SPI; UART	4	3	±2%	24-bit, 8-ch., 1 ksps	—	8-bit, 2-ch.	•	•	1	LQFP32	C8051F350DK
C8051F351	8 kB	50	0.75	17	I²C; SPI; UART	4	3	±2%	24-bit, 8-ch., 1 ksps	—	8-bit, 2-ch.	•	•	1	QFN28	C8051F350DK
C8051F352	8 kB	50	0.75	17	I²C; SPI; UART	4	3	±2%	16-bit, 8-ch., 1 ksps	—	8-bit, 2-ch.	•	•	1	LQFP32	C8051F350DK
C8051F353	8 kB	50	0.75	17	I²C; SPI; UART	4	3	±2%	16-bit, 8-ch., 1 ksps	—	8-bit, 2-ch.	•	•	1	QFN28	C8051F350DK

Low Power MCUs

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	TEMP SENSOR	VREF	COMP.	OTHER	PACKAGE	DEV KIT
C8051F960	128 kB	25	8	57	I²C; SPI; 2 x SPI; UART	4	6	±2%	12-bit, 16-ch., 75 ksps	•	•	2	128 LCD Segments; AES; dc-dc; Low Power	DQFN76/ TQFP80	C8051F960DK
C8051F961	128 kB	25	8	34	I²C; SPI; 2 x SPI; UART	4	6	±2%	12-bit, 16-ch., 75 ksps	•	•	2	36 LCD Segments; AES; dc-dc; Low Power	QFN40	C8051F960DK
C8051F962	128 kB	25	8	57	I²C; SPI; 2 x SPI; UART	4	6	±2%	12-bit, 16-ch., 75 ksps	•	•	2	AES; dc-dc; Low Power	DQFN76/ TQFP80	C8051F960DK
C8051F963	128 kB	25	8	34	I²C; SPI; 2 x SPI; UART	4	6	±2%	12-bit, 16-ch., 75 ksps	•	•	2	AES; dc-dc; Low Power	QFN40	C8051F960DK
C8051F964	64 kB	25	8	57	I²C; SPI; 2 x SPI; UART	4	6	±2%	12-bit, 16-ch., 75 ksps	•	•	2	128 LCD Segments; AES; dc-dc; Low Power	DQFN76/ TQFP80	C8051F960DK
C8051F965	64 kB	25	8	34	I²C; SPI; 2 x SPI; UART	4	6	±2%	12-bit, 16-ch., 75 ksps	•	•	2	36 LCD Segments; AES; dc-dc; Low Power	QFN40	C8051F960DK
C8051F930	64 kB	25	4.25	24	EMIF; I²C; SPI; 2 x SPI; UART	4	6	±2%	10-bit, 23-ch., 300 ksps	•	•	2	170 µA/MHz active 50 nA sleep; dc-dc; Low Power	QFN32/ LQFP32	C8051F930DK
C8051F931	64 kB	25	4.25	16	I²C; SPI; 2 x SPI; UART	4	6	±2%	10-bit, 15-ch., 300 ksps	•	•	2	170 µA/MHz active 50 nA sleep; dc-dc; Low Power	QFN24	C8051F930DK
C8051F966	32 kB	25	8	57	I²C; SPI; 2 x SPI; UART	4	6	±2%	12-bit, 16-ch., 75 ksps	•	•	2	128 LCD Segments; AES; dc-dc; Low Power	DQFN76/ TQFP80	C8051F960DK
C8051F967	32 kB	25	8	34	I²C; SPI; 2 x SPI; UART	4	6	±2%	12-bit, 16-ch., 75 ksps	•	•	2	36 LCD Segments; AES; dc-dc; Low Power	QFN40	C8051F960DK

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	TEMP SENSOR	VREF	COMP.	OTHER	PACKAGE	DEV KIT
C8051F920	32 kB	25	4.25	24	EMIF; I²C; SPI; 2 x SPI; UART	4	6	±2%	10-bit, 23-ch., 300 ksps	•	•	2	170 µA/MHz active 50 nA sleep; dc-dc; Low Power	QFN32/ LQFP32	C8051F930DK
C8051F921	32 kB	25	4.25	16	I²C; SPI; 2 x SPI; UART	4	6	±2%	10-bit, 15-ch., 300 ksps	•	•	2	170 µA/MHz active 50 nA sleep; dc-dc; Low Power	QFN24	C8051F930DK
C8051F968	16 kB	25	4.25	57	I²C; SPI; 2 x SPI; UART	4	6	±2%	12-bit, 16-ch., 75 ksps	•	•	2	128 LCD Segments; AES; dc-dc; Low Power	DQFN76/ TQFP80	C8051F960DK
C8051F969	16 kB	25	4.25	34	I²C; SPI; 2 x SPI; UART	4	6	±2%	12-bit, 16-ch., 75 ksps	•	•	2	36 LCD Segments; AES; dc-dc; Low Power	QFN40	C8051F960DK
C8051F911	16 kB	25	0.75	16	I²C; SPI; 2 x SPI; UART	4	6	±2%	10-bit, 15-ch., 300 ksps	•	•	2	160 µA/MHz active 50 nA sleep; dc-dc; Low Power	QFN24/ QSOP24	C8051F912DK
C8051F912	16 kB	25	0.75	16	I²C; SPI; 2 x SPI; UART	4	6	±2%	12-bit, 15-ch., 75 ksps	•	•	2	160 µA/MHz active 50 nA sleep; dc-dc; Low Power	QFN24/ QSOP24	C8051F912DK
C8051F901	8 kB	25	0.75	16	I²C; SPI; 2 x SPI; UART	4	6	±2%	10-bit, 15-ch., 300 ksps	•	•	2	160 µA/MHz active 50 nA sleep; dc-dc; Low Power	QFN20/ QSOP24	C8051F912DK
C8051F902	8 kB	25	0.75	16	I²C; SPI; 2 x SPI; UART	4	6	±2%	12-bit, 15-ch., 75 ksps	•	•	2	160 µA/MHz active 50 nA sleep; dc-dc; Low Power	QFN24/ QSOP24	C8051F912DK
C8051F980	8 kB	25	0.5	16	I²C; SPI; UART	4	3	±2%	12-bit, 9-ch., 75 ksps	•	•	1	10 nA sleep; 150 uA/MHz active; Low Power	QFN20	C8051F996DK
C8051F981	8 kB	25	0.5	16	I²C; SPI; UART	4	3	±2%	—	—	—	1	10 nA sleep; 150 uA/MHz active; Low Power	QFN20	C8051F996DK
C8051F986	8 kB	25	0.5	17	I²C; SPI; UART	4	3	±2%	12-bit, 10-ch., 75 ksps	•	•	1	10 nA sleep; 150 uA/MHz active; Low Power	QFN24/ QSOP24	C8051F996DK
C8051F987	8 kB	25	0.5	17	I²C; SPI; UART	4	3	±2%	—	—	—	1	10 nA sleep; 150 uA/MHz active; Low Power	QFN24/ QSOP24	C8051F996DK
C8051F982	4 kB	25	0.5	16	I²C; SPI; UART	4	3	±2%	10-bit, 9-ch., 300 ksps	•	•	1	10 nA sleep; 150 uA/MHz active; Low Power	QFN20	C8051F996DK
C8051F983	4 kB	25	0.5	16	I²C; SPI; UART	4	3	±2%	—	—	—	1	10 nA sleep; 150 uA/MHz active; Low Power	QFN20	C8051F996DK
C8051F988	4 kB	25	0.5	17	I²C; SPI; UART	4	3	±2%	10-bit, 10-ch., 300 ksps	•	•	1	10 nA sleep; 150 uA/MHz active; Low Power	QFN24/ QSOP24	C8051F996DK
C8051F989	4 kB	25	0.5	17	I²C; SPI; UART	4	3	±2%	—	—	—	1	10 nA sleep; 150 uA/MHz active; Low Power	QFN24/ QSOP24	C8051F996DK
C8051F985	2 kB	25	0.5	16	I²C; SPI; UART	4	3	±2%	—	—	—	1	10 nA sleep; 150 uA/MHz active; Low Power	QFN20	C8051F996DK

USB MCUs

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	TEMP SENSOR	VREF	COMP.	PACKAGE	DEV KIT
C8051F34C	64 kB	48	5.25	40	EMIF; I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	—	—	—	2	TQFP48	C8051F340DK
C8051F34D	64 kB	48	5.25	25	I²C; SPI; UART; USB	4	5	±1.5%	—	—	—	2	LQFP32	C8051F340DK
C8051F340	64 kB	48	4.25	40	EMIF; I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	10-bit, 20-ch., 200 ksps	•	•	2	TQFP48	C8051F340DK
C8051F342	64 kB	48	4.25	25	I²C; SPI; UART; USB	4	5	±1.5%	10-bit, 21-ch., 200 ksps	•	•	2	QFN32/ LQFP32	C8051F340DK
C8051F34A	64 kB	48	4.25	25	I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	10-bit, 21-ch., 200 ksps	•	•	2	QFN32/ LQFP32	C8051F340DK
C8051F380	64 kB	48	4.25	40	I²C; 2 x I²C; SPI; UART; 2 x UART; USB	6	5	±1.5%	10-bit, 32-ch., 500 ksps	•	•	2	TQFP48	C8051F380DK
C8051F381	64 kB	48	4.25	25	I²C; 2 x I²C; SPI; UART; 2 x UART; USB	6	5	±0.5%	10-bit, 21-ch., 500 ksps	•	•	2	QFN32/ LQFP32	C8051F380DK
C8051F384	64 kB	48	4.25	40	I²C; 2 x I²C; SPI; UART; 2 x UART; USB	6	5	±1.5%	—	—	—	2	TQFP48	C8051F380DK
C8051F385	64 kB	48	4.25	25	I²C; 2 x I²C; SPI; UART; 2 x UART; USB	6	5	±1.5%	—	—	—	2	QFN32/ LQFP32	C8051F380DK
C8051T626	64 kB	48	3.328	24	I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	10-bit, 21-ch., 500 ksps	•	•	2	QFN32	C8051T620DK

PART NUMBER	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	TEMP SENSOR	VREF	COMP.	PACKAGE	DEV KIT
C8051F344	64 kB	25	4.25	40	EMIF; I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	10-bit, 20-ch., 200 ksps	•	•	2	TQFP48	C8051F340DK
C8051F346	64 kB	25	4.25	25	I²C; SPI; UART; USB	4	5	±1.5%	10-bit, 21-ch., 200 ksps	•	•	2	QFN32/ LQFP32	C8051F340DK
C8051T627	32 kB	48	3.328	24	I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	10-bit, 21-ch., 500 ksps	•	•	2	QFN32	C8051T620DK
C8051F341	32 kB	48	2.25	40	EMIF; I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	10-bit, 20-ch., 200 ksps	•	•	2	TQFP48	C8051F340DK
C8051F343	32 kB	48	2.25	25	I²C; SPI; UART; USB	4	5	±1.5%	10-bit, 21-ch., 200 ksps	•	•	2	QFN32/ LQFP32	C8051F340DK
C8051F34B	32 kB	48	2.25	25	I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	10-bit, 21-ch., 200 ksps	•	•	2	QFN32/ LQFP32	C8051F340DK
C8051F382	32 kB	48	2.25	40	I²C; 2 x I²C; SPI; UART; 2 x UART; USB	6	5	±1.5%	10-bit, 32-ch., 500 ksps	•	•	2	TQFP48	C8051F380DK
C8051F383	32 kB	48	2.25	25	I²C; 2 x I²C; SPI; UART; 2 x UART; USB	6	5	±1.5%	10-bit, 32-ch., 500 ksps	•	•	2	QFN32/ LQFP32	C8051F380DK
C8051F386	32 kB	48	2.25	40	I²C; 2 x I²C; SPI; UART; 2 x UART; USB	6	5	±1.5%	—	—	—	2	TQFP48	C8051F380DK
C8051F387	32 kB	48	2.25	25	I²C; 2 x I²C; SPI; UART; 2 x UART; USB	6	5	±1.5%	—	—	—	2	QFN32/ LQFP32	C8051F380DK
C8051F345	32 kB	25	2.25	40	EMIF; I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	10-bit, 20-ch., 200 ksps	•	•	2	TQFP48	C8051F340DK
C8051F347	32 kB	25	2.25	25	I²C; SPI; UART; USB	4	5	±1.5%	10-bit, 21-ch., 200 ksps	•	•	2	QFN32/ LQFP32	C8051F340DK
C8051F348	32 kB	25	2.25	40	EMIF; I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	—	—	—	2	TQFP48	C8051F340DK
C8051F349	32 kB	25	2.25	25	I²C; SPI; UART; USB	4	5	±1.5%	—	—	—	2	QFN32/ LQFP32	C8051F340DK
C8051F38C	16 kB	50	2.25	25	I²C; 2 x I²C; SPI; UART; 2 x UART; USB	6	5	±1.5%	10-bit, 21-ch., 500 ksps	•	•	2	QFN32/ LQFP32	C8051F380DK
C8051T320	16 kB	48	1.25	25	I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	10-bit, 16-ch., 500 ksps	•	•	2	LQFP32	C8051T620DK
C8051T321	16 kB	48	1.25	21	I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	10-bit, 16-ch., 500 ksps	•	•	2	QFN28	C8051T620DK
C8051T322	16 kB	48	1.25	25	I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	—	—	—	2	LQFP32	C8051T620DK
C8051T323	16 kB	48	1.25	21	I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	—	—	—	2	QFN28	C8051T620DK
C8051T326	16 kB	48	1.25	15	I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	—	—	—	—	QFN28	C8051T62DK
C8051T327	16 kB	48	1.25	15	I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	—	—	—	—	QFN28	C8051T622DK
C8051T620	16 kB	48	1.25	24	I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	10-bit, 20-ch., 500 ksps	•	•	2	QFN32	C8051T620DK
C8051T621	16 kB	48	1.25	24	I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	—	—	—	2	QFN32	C8051T620DK
C8051T622	16 kB	48	1.25	16	I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	—	—	—	—	QFN24	C8051T620DK
C8051F320	16 kB	25	2.25	25	I²C; SPI; UART; USB	4	5	±1.5%	10-bit, 17-ch., 200 ksps	•	•	2	LQFP32	C8051F320DK
C8051F321	16 kB	25	2.25	21	I²C; SPI; UART; USB	4	5	±1.5%	10-bit, 13-ch., 200 ksps	•	•	2	QFN28	C8051F320DK
C8051F326	16 kB	25	1.5	15	UART; USB	2	—	±1.5%	—	—	—	—	QFN28	C8051F326DK
C8051F327	16 kB	25	1.5	15	UART; USB	2	—	±1.5%	—	—	—	—	QFN28	C8051F326DK
C8051T623	8 kB	48	1.25	16	I²C; SPI; UART; 2 x UART; USB	4	5	±1.5%	—	—	—	—	QFN24	C8051T622DK

Wireless MCUs

PART NUMBER	FLASH MEM.	MHz	RAM (kB)	DIG. I/O	COMM.	FSK/ GFSK (kbps)	OOK (kbps)	OUTPUT POWER (dBm)	2/4.8 KBPS SENS.	TX CURRENT (mA) +11/+20 (dBM)	+13 (dBM)	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	COMP.	OTHER	PACKAGE	DEV KIT
Si1020	128 kB	25	8448	53	I²C, 2x SPI, UART	256	40	+1 to +20	-121/ -110	85 mA	—	4	6	±2%	12-bit, 16-ch., 75 ksps	2	128 LCD Segments	LGA85	Si1020DK
Si1024	128 kB	25	8448	53	I²C, 2x SPI, UART	256	40	-8 to +13	-121/ -110	17 mA	30 mA	4	6	±2%	12-bit, 16-ch., 75 ksps	2	128 LCD Segments	LGA85	Si1020DK

PART NUMBER	FLASH MEM.	MHz	RAM (kB)	DIG. I/O	COMM.	FSK/ GFSK (kbps)	OOK (kbps)	OUTPUT POWER (dBm)	2/4.8 KBPS SENS.	TX CURRENT (mA) +11/+20 (dBM)	+13	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	COMP.	OTHER	PACKAGE	DEV KIT
Si1030	128 kB	25	8448	53	I^2C, 2x SPI, UART	256	40	+1 to +20	-121/ -110	85 mA	—	4	6	±2%	12-bit, 16-ch., 75 ksps	2	—	LGA85	Si1020DK
Si1034	128 kB	25	8448	53	I^2C, 2x SPI, UART	256	40	-8 to +13	-121/ -110	17 mA	30 mA	4	6	±2%	12-bit, 16-ch., 75 ksps	2	—	LGA85	Si1020DK
Si1000	64 kB	25	4352	22	I^2C, SPI, UART	256	40	+1 to +20	-121/ -110	35 mA/ 85 mA	—	4	6	±2%	10-bit, 18-ch., 300 ksps	2	CRC; RTC	LGA42	Si1000DK
Si1002	64 kB	25	4352	22	I^2C, SPI, UART	256	40	-8 to +13	-121/ -110	—	30 mA	4	6	±2%	10-bit, 18-ch., 300 ksps	2	CRC; RTC	LGA42	Si1000DK
Si1004	64 kB	25	4352	19	I^2C, SPI, UART	256	40	-8 to +13	-121/ -110	—	30 mA	4	6	±2%	10-bit, 15-ch., 300 ksps	2	CRC; dc-dc; RTC	LGA42	Si1000DK
Si1021	64 kB	25	8448	53	I^2C, 2x SPI, UART	256	40	+1 to +20	-121/ -110	85 mA	—	4	6	±2%	12-bit, 16-ch., 75 ksps	2	128 LCD Segments	LGA85	Si1020DK
Si1025	64 kB	25	8448	53	I^2C, 2x SPI, UART	256	40	-8 to +13	-121/ -110	17 mA	30 mA	4	6	±2%	12-bit, 16-ch., 75 ksps	2	128 LCD Segments	LGA85	Si1020DK
Si1031	64 kB	25	8448	53	I^2C, 2x SPI, UART	256	40	+1 to +20	-121/ -110	85 mA	—	4	6	±2%	12-bit, 16-ch., 75 ksps	2	—	LGA85	Si1020DK
Si1035	64 kB	25	8448	53	I^2C, 2x SPI, UART	256	40	-8 to +13	-121/ -110	17 mA	30 mA	4	6	±2%	12-bit, 16-ch., 75 ksps	2	—	LGA85	Si1020DK
Si1060	64 kB	25	4352	15	I^2C; SPI; UART	512	120	-20 to +20	-126	18 mA/ 85 mA	29 mA	4	6	±2%	10-bit, 18-ch., 300 ksps	2	CRC; RTC	QFN36	Si106xDK
Si1062	64 kB	25	4352	15	I^2C; SPI; UART	512	120	-40 to +13	-126	18 mA	29 mA	4	6	±2%	10-bit, 18-ch., 300 ksps	2	CRC; RTC	QFN36	Si106xDK
Si1064	64 kB	25	4352	15	I^2C; SPI; UART	512	120	-40 to +13	-116	18 mA	29 mA	4	6	±2%	10-bit, 15-ch., 300 ksps	2	CRC; RTC	QFN36	Si1064DK
Si1001	32 kB	25	4352	22	I^2C, SPI, UART	256	40	+1 to +20	-121/ -110	35 mA/ 85 mA	—	4	6	±2%	10-bit, 18-ch., 300 ksps	2	CRC; RTC	LGA42	Si1000DK
Si1003	32 kB	25	4352	22	I^2C, SPI, UART	256	40	-8 to +13	-121/ -110	—	30 mA	4	6	±2%	10-bit, 18-ch., 300 ksps	2	CRC; RTC	LGA42	Si1000DK
Si1005	32 kB	25	4352	19	I^2C, SPI, UART	256	40	-8 to +13	-121/ -110	—	30 mA	4	6	±2%	10-bit, 15-ch., 300 ksps	2	CRC; dc-dc; RTC	LGA42	Si1000DK
Si1022	32 kB	25	8448	53	I^2C, 2x SPI, UART	256	40	+1 to +20	-121/ -110	85 mA	—	4	6	±2%	12-bit, 16-ch., 75 ksps	2	128 LCD Segments	LGA85	Si1020DK
Si1026	32 kB	25	8448	53	I^2C, 2x SPI, UART	256	40	-8 to +13	-121/ -110	17 mA	30 mA	4	6	±2%	12-bit, 16-ch., 75 ksps	2	128 LCD Segments	LGA85	Si1020DK
Si1032	32 kB	25	8448	53	I^2C, 2x SPI, UART	256	40	+1 to +20	-121/ -110	85 mA	—	4	6	±2%	12-bit, 16-ch., 75 ksps	2	—	LGA85	Si1020DK
Si1036	32 kB	25	8448	53	I^2C, 2x SPI, UART	256	40	-8 to +13	-121/ -110	17 mA	30 mA	4	6	±2%	12-bit, 16-ch., 75 ksps	2	—	LGA85	Si1020DK
Si1061	32 kB	25	4352	15	I^2C; SPI; UART	512	120	-20 to +20	-126	18 mA/ 85 mA	29 mA	4	6	±2%	10-bit, 18-ch., 300 ksps	2	CRC; RTC	QFN36	Si106xDK
Si1063	32 kB	25	4352	15	I^2C; SPI; UART	512	120	-40 to +13	-126	18 mA	29 mA	4	6	±2%	10-bit, 18-ch., 300 ksps	2	CRC; RTC	QFN36	Si106xDK
Si1065	32 kB	25	4352	15	I^2C; SPI; UART	512	120	-40 to +13	-116	18 mA	29 mA	4	6	±2%	10-bit, 18-ch., 300 ksps	2	CRC; RTC	QFN36	Si1064DK
Si1010	16 kB	25	768	15	I^2C, SPI, UART	256	40	+1 to +20	-121/ -110	35 mA/ 85 mA	—	4	6	±2%	12-bit, 11-ch., 75 ksps	2	CRC; RTC	LGA42	Si1010DK
Si1012	16 kB	25	768	15	I^2C, SPI, UART	256	40	-8 to +13	-121/ -110	—	30 mA	4	6	±2%	12-bit, 11-ch., 75 ksps	2	CRC; RTC	LGA42	Si1010DK
Si1014	16 kB	25	768	15	I^2C, SPI, UART	256	40	-8 to +13	-121/ -110	—	30 mA	4	6	±2%	12-bit, 11-ch., 75 ksps	2	CRC; dc-dc; RTC	LGA42	Si1010DK

PART NUMBER	FLASH MEM.	MHz	RAM (kB)	DIG. I/O	COMM.	FSK/ GFSK (kbps)	OOK (kbps)	OUTPUT POWER (dBm)	2/4.8 KBPS SENS.	TX CURRENT (mA) +11/+20 (dBM)	+13	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	COMP.	OTHER	PACKAGE	DEV KIT
Si1023	16 kB	25	4352	53	I²C, 2x SPI, UART	256	40	+1 to +20	-121/ -110	85 mA	—	4	6	±2%	12-bit, 16-ch., 75 ksps	2	128 LCD Segments	LGA85	Si1020DK
Si1027	16 kB	25	4352	53	I²C, 2x SPI, UART	256	40	-8 to +13	-121/ -110	17 mA	30 mA	4	6	±2%	12-bit, 16-ch., 75 ksps	2	128 LCD Segments	LGA85	Si1020DK
Si1033	16 kB	25	4352	53	I²C, 2x SPI, UART	256	40	+1 to +20	-121/ -110	85 mA	—	4	6	±2%	12-bit, 16-ch., 75 ksps	2	—	LGA85	Si1020DK
Si1037	16 kB	25	4352	53	I²C, 2x SPI, UART	256	40	-8 to +13	-121/ -110	17 mA	30 mA	4	6	±2%	12-bit, 16-ch., 75 ksps	2	—	LGA85	Si1020DK
Si1080	16 kB	25	768	15	I²C; SPI; UART	512	120	-20 to +20	-126	18 mA/ 85 mA	29 mA	4	6	±2%	10-bit, 18-ch., 300 ksps	2	CRC; RTC	QFN36	Si106xDK
Si1082	16 kB	25	768	15	I²C; SPI; UART	512	120	-40 to +13	-126	18 mA	29 mA	4	6	±2%	10-bit, 18-ch., 300 ksps	2	CRC; RTC	QFN36	Si106xDK
Si1084	16 kB	25	768	15	I²C; SPI; UART	512	120	-40 to +13	-116	18 mA	29 mA	4	6	±2%	10-bit, 15-ch., 300 ksps	2	CRC; RTC	QFN36	Si1064DK
Si1011	8 kB	25	768	15	I²C, SPI, UART	256	40	+1 to +20	-121/ -110	35 mA/ 85 mA	—	4	6	±2%	12-bit, 11-ch., 75 ksps	2	CRC; RTC	LGA42	Si1010DK
Si1013	8 kB	25	768	15	I²C, SPI, UART	256	40	-8 to +13	-121/ -110	—	30 mA	4	6	±2%	12-bit, 11-ch., 75 ksps	2	CRC; RTC	LGA42	Si1010DK
Si1015	8 kB	25	768	15	I²C, SPI, UART	256	40	-8 to +13	-121/- 110	—	30 mA	4	6	±2%	12-bit, 11-ch., 75 ksps	2	CRC; dc-dc; RTC	LGA42	Si1010DK
Si1081	8 kB	25	768	15	I²C; SPI; UART	512	120	-20 to +20	-126	18 mA/ 85 mA	29 mA	4	6	±2%	10-bit, 18-ch., 300 ksps	2	CRC; RTC	QFN36	Si106xDK
Si1083	8 kB	25	768	15	I²C; SPI; UART	512	120	-40 to +13	-126	18 mA	29 mA	4	6	±2%	10-bit, 15-ch., 300 ksps	2	CRC; RTC	QFN36	Si106xDK
Si1085	8 kB	25	768	15	I²C; SPI; UART	512	120	-40 to +13	-116	18 mA	29 mA	4	6	±2%	10-bit, 15-ch., 300 ksps	2	CRC; RTC	QFN36	Si1064DK

32-bit Microcontroller Products

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EFM32™ Zero Gecko 32-bit MCU

PART NUMBER	CORE	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	DEBUG I/F	COMP.	PACKAGE
EFM32ZG108F32-QFN24	M0+	32 kB	24	4	17	I²C; USART	2	—	±2%	—	—	SW	1	QFN24
EFM32ZG110F32-QFN24	M0+	32 kB	24	4	17	I²C; USART	2	—	±2%	12-bit, 1-ch., 1 Msps	—	SW	1	QFN24
EFM32ZG210F32-QFN32	M0+	32 kB	24	4	24	I²C; USART	2	—	±2%	12-bit, 1-ch., 1 Msps	—	SW	1	QFN32
EFM32ZG222F32-QFP48	M0+	32 kB	24	4	37	I²C; USART	2	—	±2%	12-bit, 1-ch., 1 Msps	—	SW	1	QFP48
EFM32ZG108F16-QFN24	M0+	16 kB	24	4	17	I²C; USART	2	—	±2%	—	—	SW	1	QFN24
EFM32ZG110F16-QFN24	M0+	16 kB	24	4	17	I²C; USART	2	—	±2%	12-bit, 1-ch., 1 Msps	—	SW	1	QFN24
EFM32ZG210F16-QFN32	M0+	16 kB	24	4	24	I²C; USART	2	—	±2%	12-bit, 1-ch., 1 Msps	—	SW	1	QFN32
EFM32ZG222F16-QFP48	M0+	16 kB	24	4	37	I²C; USART	2	—	±2%	12-bit, 1-ch., 1 Msps	—	SW	1	QFP48
EFM32ZG108F8-QFN24	M0+	8 kB	24	2	17	I²C; USART	2	—	±2%	—	—	SW	1	QFN24
EFM32ZG110F8-QFN24	M0+	8 kB	24	2	17	I²C; USART	2	—	±2%	12-bit, 1-ch., 1 Msps	—	SW	1	QFN24
EFM32ZG210F8-QFN32	M0+	8 kB	24	2	24	I²C; USART	2	—	±2%	12-bit, 1-ch., 1 Msps	—	SW	1	QFN32

PART NUMBER	CORE	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	DEBUG I/F	COMP.	PACKAGE
EFM32ZG222F8-QFP48	M0+	8 kB	24	2	37	I²C; USART	2	—	±2%	12-bit, 1-ch., 1 Msps	—	SW	1	QFP48
EFM32ZG108F4-QFN24	M0+	4 kB	24	2	17	I²C; USART	2	—	±2%	—	—	SW	1	QFN24
EFM32ZG110F4-QFN24	M0+	4 kB	24	2	17	I²C; USART	2	—	±2%	12-bit, 1-ch., 1 Msps	—	SW	1	QFN24
EFM32ZG210F4-QFN32	M0+	4 kB	24	2	24	I²C; USART	2	—	±2%	12-bit, 1-ch., 1 Msps	—	SW	1	QFN32
EFM32ZG222F4-QFP48	M0+	4 kB	24	2	37	I²C; USART	2	—	±2%	12-bit, 1-ch., 1 Msps	—	SW	1	QFP48

EFM32™ Tiny Gecko 32-bit MCU

PART NUMBER	CORE	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	DEBUG I/F	COMP.	PACKAGE
EFM32TG108F32-QFN24	M3	32 kB	32	4	17	I²C; USART	2	—	±2%	—	12-bit	SW	2	QFN24
EFM32TG110F32-QFN24	M3	32 kB	32	4	17	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN24
EFM32TG210F32-QFN32	M3	32 kB	32	4	24	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN32
EFM32TG222F32-QFP48	M3	32 kB	32	4	37	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP48
EFM32TG225F32-BGA48	M3	32 kB	32	4	37	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	BGA48
EFM32TG230F32-QFN64	M3	32 kB	32	4	56	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN64
EFM32TG232F32-QFP64	M3	32 kB	32	4	53	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP64
EFM32TG822F32-QFP48	M3	32 kB	32	4	37	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP48
EFM32TG825F32-BGA48	M3	32 kB	32	4	37	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	BGA48
EFM32TG840F32-QFN64	M3	32 kB	32	4	56	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN64
EFM32TG842F32-QFP64	M3	32 kB	32	4	53	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP64
EFM32TG108F16-QFN24	M3	16 kB	32	4	17	I²C; USART	2	—	±2%	—	12-bit	SW	2	QFN24
EFM32TG110F16-QFN24	M3	16 kB	32	4	17	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN24
EFM32TG210F16-QFN32	M3	16 kB	32	4	24	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN32
EFM32TG222F16-QFP48	M3	16 kB	32	4	37	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP48
EFM32TG225F16-BGA48	M3	16 kB	32	4	37	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	BGA48
EFM32TG230F16-QFN64	M3	16 kB	32	4	56	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN64
EFM32TG232F16-QFP64	M3	16 kB	32	4	53	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP64
EFM32TG822F16-QFP48	M3	16 kB	32	4	37	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP48
EFM32TG825F16-BGA48	M3	16 kB	32	4	37	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	BGA48
EFM32TG840F16-QFN64	M3	16 kB	32	4	56	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN64
EFM32TG842F16-QFP64	M3	16 kB	32	4	53	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP64
EFM32TG108F8-QFN24	M3	8 kB	32	2	17	I²C; USART	2	—	±2%	—	12-bit	SW	2	QFN24
EFM32TG110F8-QFN24	M3	8 kB	32	2	17	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN24
EFM32TG210F8-QFN32	M3	8 kB	32	2	24	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN32
EFM32TG222F8-QFP48	M3	8 kB	32	2	37	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP48
EFM32TG225F8-BGA48	M3	8 kB	32	2	37	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	BGA48
EFM32TG230F8-QFN64	M3	8 kB	32	2	56	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN64

PART NUMBER	CORE	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	DEBUG I/F	COMP.	PACKAGE
EFM32TG232F8-QFP64	M3	8 kB	32	2	53	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP64
EFM32TG822F8-QFP48	M3	8 kB	32	2	37	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP48
EFM32TG825F8-BGA48	M3	8 kB	32	2	37	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	BGA48
EFM32TG840F8-QFN64	M3	8 kB	32	2	56	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN64
EFM32TG842F8-QFP64	M3	8 kB	32	2	53	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP64
EFM32TG108F4-QFN24	M3	4 kB	32	2	17	I²C; USART	2	—	±2%	—	12-bit	SW	2	QFN24
EFM32TG110F4-QFN24	M3	4 kB	32	2	17	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN24

EFM32™ Gecko 32-bit MCU

PART NUMBER	CORE	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	DEBUG I/F	COMP.	PACKAGE
EFM32G210F128-QFN32	M3	128 kB	32	16	24	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 1-ch.	SW	2	QFN32
EFM32G222F128-QFP48	M3	128 kB	32	16	37	I²C; 2 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 1-ch.	SW	2	QFP48
EFM32G230F128-QFN64	M3	128 kB	32	16	56	I²C; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN64
EFM32G232F128-QFP64	M3	128 kB	32	16	53	I²C; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 1-ch.	SW	2	QFP64
EFM32G280F128-QFP100	M3	128 kB	32	16	86	I²C; UART; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP100
EFM32G290F128-BGA112	M3	128 kB	32	16	90	I²C; UART; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	BGA112
EFM32G840F128-QFN64	M3	128 kB	32	16	56	I²C; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN64
EFM32G842F128-QFP64	M3	128 kB	32	16	53	I²C; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 1-ch.	SW	2	QFP64
EFM32G880F128-QFP100	M3	128 kB	32	16	86	I²C; UART; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP100
EFM32G890F128-BGA112	M3	128 kB	32	16	90	I²C; UART; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	BGA112
EFM32G200F64-QFN32	M3	64 kB	32	16	24	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 1-ch.	SW	2	QFN32
EFM32G222F64-QFP48	M3	64 kB	32	16	37	I²C; 2 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 1-ch.	SW	2	QFP48
EFM32G230F64-QFN64	M3	64 kB	32	16	56	I²C; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN64
EFM32G232F64-QFP64	M3	64 kB	32	16	53	I²C; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 1-ch.	SW	2	QFP64
EFM32G280F64-QFP100	M3	64 kB	32	16	86	I²C; UART; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP100
EFM32G290F64-BGA112	M3	64 kB	32	16	90	I²C; UART; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	BGA112
EFM32G840F64-QFN64	M3	64 kB	32	16	56	I²C; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN64
EFM32G842F64-QFP64	M3	64 kB	32	16	53	I²C; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 1-ch.	SW	2	QFP64
EFM32G880F64-QFP100	M3	64 kB	32	16	86	I²C; UART; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP100
EFM32G890F64-BGA112	M3	64 kB	32	16	90	I²C; UART; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	BGA112
EFM32G200F32-QFN32	M3	32 kB	32	8	24	I²C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 1-ch.	SW	2	QFN32
EFM32G222F32-QFP48	M3	32 kB	32	8	37	I²C; 2 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 1-ch.	SW	2	QFP48
EFM32G230F32-QFN64	M3	32 kB	32	8	56	I²C; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN64
EFM32G232F32-QFP64	M3	32 kB	32	8	53	I²C; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 1-ch.	SW	2	QFP64
EFM32G280F32-QFP100	M3	32 kB	32	8	86	I²C; UART; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP100

PART NUMBER	CORE	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	DEBUG I/F	COMP.	PACKAGE
EFM32G290F32-BGA112	M3	32 kB	32	8	90	I^2C; UART; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	BGA112
EFM32G840F32-QFN64	M3	32 kB	32	8	56	I^2C; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFN64
EFM32G842F32-QFP64	M3	32 kB	32	8	53	I^2C; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 1-ch.	SW	2	QFP64
EFM32G880F32-QFP100	M3	32 kB	32	8	86	I^2C; UART; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	QFP100
EFM32G890F32-BGA112	M3	32 kB	32	8	90	I^2C; UART; 3 x USART	3	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	SW	2	BGA112
EFM32G200F16-QFN32	M3	16 kB	32	8	24	I^2C; 2 x USART	2	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 1-ch.	SW	2	QFN32

EFM32™ Leopard Gecko 32-bit MCU

PART NUMBER	CORE	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	DEBUG I/F	COMP.	PACKAGE
EFM32LG230F256-QFN64	M3	256 kB	48	32	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32LG232F256-QFP64	M3	256 kB	48	32	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32LG280F256-QFP100	M3	256 kB	48	32	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32LG290F256-BGA112	M3	256 kB	48	32	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32LG295F256-BGA120	M3	256 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32LG330F256-QFN64	M3	256 kB	48	32	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32LG332F256-QFP64	M3	256 kB	48	32	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32LG380F256-QFP100	M3	256 kB	48	32	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32LG390F256-BGA112	M3	256 kB	48	32	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32LG395F256-BGA120	M3	256 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32LG840F256-QFN64	M3	256 kB	48	32	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32LG842F256-QFP64	M3	256 kB	48	32	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32LG880F256-QFP100	M3	256 kB	48	32	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32LG890F256-BGA112	M3	256 kB	48	32	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32LG895F256-BGA120	M3	256 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32LG940F256-QFN64	M3	256 kB	48	32	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFN64
EFM32LG942F256-QFP64	M3	256 kB	48	32	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32LG980F256-QFP100	M3	256 kB	48	32	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32LG990F256-BGA112	M3	256 kB	48	32	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32LG995F256-BGA120	M3	256 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32LG230F128-QFN64	M3	128 kB	48	32	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32LG232F128-QFP64	M3	128 kB	48	32	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64

PART NUMBER	CORE	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	DEBUG I/F	COMP.	PACKAGE
EFM32LG280F128-QFP100	M3	128 kB	48	32	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32LG290F128-BGA112	M3	128 kB	48	32	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32LG295F128-BGA120	M3	128 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32LG330F128-QFN64	M3	128 kB	48	32	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32LG332F128-QFP64	M3	128 kB	48	32	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32LG380F128-QFP100	M3	128 kB	48	32	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32LG390F128-BGA112	M3	128 kB	48	32	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32LG395F128-BGA120	M3	128 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32LG840F128-QFN64	M3	128 kB	48	32	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32LG842F128-QFP64	M3	128 kB	48	32	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32LG880F128-QFP100	M3	128 kB	48	32	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32LG890F128-BGA112	M3	128 kB	48	32	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32LG895F128-BGA120	M3	128 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32LG940F128-QFN64	M3	128 kB	48	32	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFN64
EFM32LG942F128-QFP64	M3	128 kB	48	32	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32LG980F128-QFP100	M3	128 kB	48	32	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32LG990F128-BGA112	M3	128 kB	48	32	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32LG995F128-BGA120	M3	128 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32LG230F64-QFN64	M3	64 kB	48	32	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32LG232F64-QFP64	M3	64 kB	48	32	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32LG280F64-QFP100	M3	64 kB	48	32	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32LG290F64-BGA112	M3	64 kB	48	32	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32LG295F64-BGA120	M3	64 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32LG330F64-QFN64	M3	64 kB	48	32	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32LG332F64-QFP64	M3	64 kB	48	32	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32LG380F64-QFP100	M3	64 kB	48	32	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32LG390F64-BGA112	M3	64 kB	48	32	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32LG395F64-BGA120	M3	64 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32LG840F64-QFN64	M3	64 kB	48	32	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32LG842F64-QFP64	M3	64 kB	48	32	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32LG880F64-QFP100	M3	64 kB	48	32	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32LG890F64-BGA112	M3	64 kB	48	32	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32LG895F64-BGA120	M3	64 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32LG940F64-QFN64	M3	64 kB	48	32	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFN64

PART NUMBER	CORE	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	DEBUG I/F	COMP.	PACKAGE
EFM32LG942F64-QFP64	M3	64 kB	48	32	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32LG980F64-QFP100	M3	64 kB	48	32	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32LG990F64-BGA112	M3	64 kB	48	32	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32LG995F64-BGA120	M3	64 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120

EFM32™ Giant Gecko 32-bit MCU

PART NUMBER	CORE	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	DEBUG I/F	COMP.	PACKAGE
EFM32GG230F1024-QFN64	M3	1024 kB	48	128	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32GG232F1024-QFP64	M3	1024 kB	48	128	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32GG280F1024-QFP100	M3	1024 kB	48	128	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32GG290F1024-BGA112	M3	1024 kB	48	128	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32GG295F1024-BGA120	M3	1024 kB	48	128	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32GG330F1024-QFN64	M3	1024 kB	48	128	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32GG332F1024-QFP64	M3	1024 kB	48	128	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32GG380F1024-QFP100	M3	1024 kB	48	128	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32GG390F1024-BGA112	M3	1024 kB	48	128	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32GG395F1024-BGA120	M3	1024 kB	48	128	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32GG840F1024-QFN64	M3	1024 kB	48	128	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32GG842F1024-QFP64	M3	1024 kB	48	128	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32GG880F1024-QFP100	M3	1024 kB	48	128	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32GG890F1024-BGA112	M3	1024 kB	48	128	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32GG895F1024-BGA120	M3	1024 kB	48	128	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32GG940F1024-QFN64	M3	1024 kB	48	128	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFN64
EFM32GG942F1024-QFP64	M3	1024 kB	48	128	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32GG980F1024-QFP100	M3	1024 kB	48	128	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32GG990F1024-BGA112	M3	1024 kB	48	128	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32GG995F1024-BGA120	M3	1024 kB	48	128	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32GG230F512-QFN64	M3	512 kB	48	128	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32GG232F512-QFP64	M3	512 kB	48	128	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32GG280F512-QFP100	M3	512 kB	48	128	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32GG290F512-BGA112	M3	512 kB	48	128	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32GG295F512-BGA120	M3	512 kB	48	128	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32GG330F512-QFN64	M3	512 kB	48	128	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32GG332F512-QFP64	M3	512 kB	48	128	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32GG380F512-QFP100	M3	512 kB	48	128	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100

PART NUMBER	CORE	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	DEBUG I/F	COMP.	PACKAGE
EFM32GG390F512-BGA112	M3	512 kB	48	128	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32GG395F512-BGA120	M3	512 kB	48	128	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32GG840F512-QFN64	M3	512 kB	48	128	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32GG842F512-QFP64	M3	512 kB	48	128	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32GG880F512-QFP100	M3	512 kB	48	128	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32GG890F512-BGA112	M3	512 kB	48	128	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32GG895F512-BGA120	M3	512 kB	48	128	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32GG940F512-QFN64	M3	512 kB	48	128	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFN64
EFM32GG942F512-QFP64	M3	512 kB	48	128	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32GG980F512-QFP100	M3	512 kB	48	128	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32GG990F512-BGA112	M3	512 kB	48	128	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32GG995F512-BGA120	M3	512 kB	48	128	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120

EFM32™ Wonder Gecko 32-bit MCU

PART NUMBER	CORE	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	DEBUG I/F	COMP.	PACKAGE
EFM32WG230F256-QFN64	M4	256 kB	48	32	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32WG232F256-QFP64	M4	256 kB	48	32	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32WG280F256-QFP100	M4	256 kB	48	32	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32WG290F256-BGA112	M4	256 kB	48	32	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32WG295F256-BGA120	M4	256 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32WG330F256-QFN64	M4	256 kB	48	32	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32WG332F256-QFP64	M4	256 kB	48	32	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32WG380F256-QFP100	M4	256 kB	48	32	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32WG390F256-BGA112	M4	256 kB	48	32	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32WG395F256-BGA120	M4	256 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32WG840F256-QFN64	M4	256 kB	48	32	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32WG842F256-QFP64	M4	256 kB	48	32	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32WG880F256-QFP100	M4	256 kB	48	32	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32WG890F256-BGA112	M4	256 kB	48	32	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32WG895F256-BGA120	M4	256 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32WG940F256-QFN64	M4	256 kB	48	32	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFN64
EFM32WG942F256-QFP64	M4	256 kB	48	32	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32WG980F256-QFP100	M4	256 kB	48	32	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32WG990F256-BGA112	M4	256 kB	48	32	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32WG995F256-BGA120	M4	256 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120

PART NUMBER	CORE	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	DEBUG I/F	COMP.	PACKAGE
EFM32WG230F128-QFN64	M4	128 kB	48	32	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32WG232F128-QFP64	M4	128 kB	48	32	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32WG280F128-QFP100	M4	128 kB	48	32	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32WG290F128-BGA112	M4	128 kB	48	32	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32WG295F128-BGA120	M4	128 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32WG330F128-QFN64	M4	128 kB	48	32	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32WG332F128-QFP64	M4	128 kB	48	32	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32WG380F128-QFP100	M4	128 kB	48	32	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32WG390F128-BGA112	M4	128 kB	48	32	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32WG395F128-BGA120	M4	128 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32WG840F128-QFN64	M4	128 kB	48	32	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32WG842F128-QFP64	M4	128 kB	48	32	53	2 x I^2C; 3 x USAT	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32WG880F128-QFP100	M4	128 kB	48	32	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32WG890F128-BGA112	M4	128 kB	48	32	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32WG895F128-BGA120	M4	128 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32WG940F128-QFN64	M4	128 kB	48	32	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFN64
EFM32WG942F128-QFP64	M4	128 kB	48	32	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32WG980F128-QFP100	M4	128 kB	48	32	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32WG990F128-BGA112	M4	128 kB	48	32	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32WG995F128-BGA120	M4	128 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32WG230F64-QFN64	M4	64 kB	48	32	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32WG232F64-QFP64	M4	64 kB	48	32	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32WG280F64-QFP100	M4	64 kB	48	32	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32WG290F64-BGA112	M4	64 kB	48	32	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32WG295F64-BGA120	M4	64 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32WG330F64-QFN64	M4	64 kB	48	32	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32WG332F64-QFP64	M4	64 kB	48	32	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32WG380F64-QFP100	M4	64 kB	48	32	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32WG390F64-BGA112	M4	64 kB	48	32	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32WG395F64-BGA120	M4	64 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32WG840F64-QFN64	M4	64 kB	48	32	56	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFN64
EFM32WG842F64-QFP64	M4	64 kB	48	32	53	2 x I^2C; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP64
EFM32WG880F64-QFP100	M4	64 kB	48	32	86	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32WG890F64-BGA112	M4	64 kB	48	32	90	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112

PART NUMBER	CORE	FLASH MEMORY	MHz	RAM (kB)	DIG. I/O	COMMUNICATIONS	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	DEBUG I/F	COMP.	PACKAGE
EFM32WG895F64-BGA120	M4	64 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120
EFM32WG940F64-QFN64	M4	64 kB	48	32	53	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFN64
EFM32WG942F64-QFP64	M4	64 kB	48	32	50	2 x I^2C; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	1	QFP64
EFM32WG980F64-QFP100	M4	64 kB	48	32	83	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	QFP100
EFM32WG990F64-BGA112	M4	64 kB	48	32	87	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA112
EFM32WG995F64-BGA120	M4	64 kB	48	32	93	2 x I^2C; 2 x UART; 3 x USART; USB	4	—	±2%	12-bit, 1-ch., 1 Msps	12-bit, 2 ch.	ETM; SW	2	BGA120

Precision32™ Analog-Intensive 32-bit MCU

PART NUMBER	FLASH MEMORY	MHz	RAM	DIG. I/O	COMM.	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	ADC 2	DAC	TOUCH CH.	TEMP SENSOR	VREF	COMP.	PACKAGE
SiM3C164	256 kB	80	32 kB	28	2 x I^2C, I^2S, 3 x SPI, 2 x UART, 2 x USART, USB	5	10	±1.5%	12-bit, 7-ch.	12-bit, 11-ch.	10-bit, 2-ch.	12	•	•	2	QFN40
SiM3C166	256 kB	80	32 kB	50		5	10	±1.5%	12-bit, 13-ch.	12-bit, 15-ch.	10-bit, 2-ch.	15	•	•	2	QFN64/TQFP64
SiM3C167	256 kB	80	32 kB	65		5	10	±1.5%	12-bit, 16-ch.	12-bit, 16-ch.	10-bit, 2-ch.	16	•	•	2	LGA92/TQFP80
SiM3C154	128 kB	80	32 kB	28		5	10	±1.5%	12-bit, 7-ch.	12-bit, 11-ch.	10-bit, 2-ch.	12	•	•	2	QFN40
SiM3C156	128 kB	80	32 kB	50		5	10	±1.5%	12-bit, 13-ch.	12-bit, 15-ch.	10-bit, 2-ch.	15	•	•	2	QFN64/TQFP64
SiM3C157	128 kB	80	32 kB	65		5	10	±1.5%	12-bit, 16-ch.	12-bit, 16-ch.	10-bit, 2-ch.	16	•	•	2	LGA92/TQFP80
SiM3C144	64 kB	80	16 kB	28		5	10	±1.5%	12-bit, 7-ch.	12-bit, 11-ch.	10-bit, 2-ch.	12	•	•	2	QFN40
SiM3C146	64 kB	80	16 kB	50		5	10	±1.5%	12-bit, 13-ch.	12-bit, 15-ch.	10-bit, 2-ch.	15	•	•	2	QFN64/TQFP64
SiM3C134	32 kB	80	8 kB	28		5	10	±1.5%	12-bit, 7-ch.	12-bit, 11-ch.	10-bit, 2-ch.	12	•	•	2	QFN40
SiM3C136	32 kB	80	8 kB	50		5	10	±1.5%	12-bit, 13-ch.	12-bit, 15-ch.	10-bit, 2-ch.	15	•	•	2	QFN64/TQFP64

Precision32™ Low Power 32-bit MCUs

PART NUMBER	FLASH MEMORY	MHz	RAM	DIG. I/O	COMM.	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	DAC	TOUCH CH.	TEMP SENSOR	VREF	COMP.	PACKAGE
SiM3L164	256 kB	50	32 kB	28	I^2C, 2 x SPI, 2 x UART	3	1x6	±2%	12-bit, 20-ch.	10-bit	—	•	•	2	QFN40
SiM3L166	256 kB	50	32 kB	51		3	1x6	±2%	12-bit, 23-ch.	10-bit	—	•	•	2	QFN64/TQFP64
SiM3L167	256 kB	50	32 kB	62/64		3	1x6	±2%	12-bit, 24-ch.	10-bit	—	•	•	2	QFP80
SiM3L154	128 kB	50	16 kB	28	I^2C, 2 x SPI, 2 x UART	3	1x6	±2%	12-bit, 20-ch.	10-bit	—	•	•	2	QFN40
SiM3L156	128 kB	50	16 kB	51		3	1x6	±2%	12-bit, 23-ch.	10-bit	—	•	•	2	QFN64/TQFP64
SiM3L157	128 kB	50	16 kB	62/64		3	1x6	±2%	12-bit, 24-ch.	10-bit	—	•	•	2	QFP80/BGA80
SiM3L144	64 kB	50	8 kB	28		3	1x6	±2%	12-bit, 20-ch.	10-bit	—	•	•	2	QFN40
SiM3L146	64 kB	50	8 kB	51		3	1x6	±2%	12-bit, 23-ch.	10-bit	—	•	•	2	QFN64/TQFP64
SiM3L134	32 kB	50	8 kB	28		3	1x6	±2%	12-bit, 20-ch.	10-bit	—	•	•	2	QFN40
SiM3L136	32 kB	50	8 kB	47		3	1x6	±2%	12-bit, 23-ch.	10-bit	—	•	•	2	QFN64/TQFP64

Precision32™ USB 32-bit MCUs

PART NUMBER	FLASH MEMORY	MHz	RAM	DIG. I/O	COMM.	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	ADC 2	DAC	TOUCH CH.	TEMP SEN-SOR	VREF	COMP.	PACKAGE
SiM3U164	256 kB	80	32 kB	28	2 x I^2C, I^2S, 3 x SPI, 2 x UART, 2 x USART, USB	5	10	±1.5%	12-bit, 7-ch.	12-bit, 11-ch.	10-bit, 2-ch.	12	•	•	2	QFN40
SiM3U166	256 kB	80	32 kB	50		5	10	±1.5%	12-bit, 13-ch.	12-bit, 15-ch.	10-bit, 2-ch.	15	•	•	2	QFN64/TQFP64
SiM3U167	256 kB	80	32 kB	65		5	10	±1.5%	12-bit, 16-ch.	12-bit, 16-ch.	10-bit, 2-ch.	16	•	•	2	LGA92/TQFP80
SiM3U154	128 kB	80	32 kB	28		5	10	±1.5%	12-bit, 7-ch.	12-bit, 11-ch.	10-bit, 2-ch.	12	•	•	2	QFN40
SiM3U156	128 kB	80	32 kB	50		5	10	±1.5%	12-bit, 13-ch.	12-bit, 15-ch.	10-bit, 2-ch.	15	•	•	2	QFN64/TQFP64

PART NUMBER	FLASH MEMORY	MHz	RAM	DIG. I/O	COMM.	TIMERS (16-BIT)	PWM/ PCA	INT. OSC	ADC	ADC 2	DAC	TOUCH CH.	TEMP SEN-SOR	VREF	COMP.	PACKAGE
SiM3U157	128 kB	80	32 kB	65	2 x I^2C, I^2S, 3 x SPI, 2 x UART, 2 x USART, USB	5	10	±1.5%	12-bit, 16-ch.	12-bit, 16-ch.	10-bit, 2-ch.	16	•	•	2	LGA92/TQFP80
SiM3U144	64 kB	80	16 kB	28		5	10	±1.5%	12-bit, 7-ch.	12-bit, 11-ch.	10-bit, 2-ch.	12	•	•	2	QFN40
SiM3U146	64 kB	80	16 kB	50		5	10	±1.5%	12-bit, 13-ch.	12-bit, 15-ch.	10-bit, 2-ch.	15	•	•	2	QFN64/TQFP64
SiM3U134	32 kB	80	8 kB	28		5	10	±1.5%	12-bit, 7-ch.	12-bit, 11-ch.	10-bit, 2-ch.	12	•	•	2	QFN40
SiM3U136	32 kB	80	8 kB	50		5	10	±1.5%	12-bit, 13-ch.	12-bit, 15-ch.	10-bit, 2-ch.	15	•	•	2	QFN64/TQFP64

Modems

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ISOmodem® Embedded Modems

PART NUMBER	MAX DATA RATE (BPS)	LINE SIDE DEVICE	HOST INTERFACE	HANDSET, TAM AND SPEAKERPHONE	ERROR CORRECTION/ DATA COMPRESSION	PACKAGE
Si2401	2400	Si3010	UART			SOIC16
Si2404	2400	Si3010	SPI, UART or parallel		•	TSSOP24 or SOIC16
Si2415	14400	Si3018	SPI, UART or parallel		•	TSSOP24 or SOIC16
Si2434	33600	Si3018	SPI, UART or parallel		•	TSSOP24 or SOIC16
Si2439	33600	Si3018	SPI, UART or parallel	•	•	QFN38
Si2457	56000	Si3018	SPI, UART or parallel		•	TSSOP24 or SOIC16
Si2493	56000	Si3018	SPI, UART or parallel		•	TSSOP24 or SOIC16
Si2494	56000	Si3018	SPI, UART or parallel	•		QFN38

Silicon DAAs

PART NUMBER	DIGITAL INTERFACE	LINE VOLTAGE MONITOR	AC TERMINATION SETTINGS	BOM COMPONENTS	PACKAGE
Si3050	PCM/SPI or GCI	•	4/16	32	TSSOP20 + SOIC8 or SOIC16
Si3052	PCI		4	32	TQFP64 + SOIC8 or SOIC16
Si3054	HD Audio/AC-Link		4	32	SOIC16 + SOIC8 or SOIC16
Si3056	DSP serial	•	4/16	32	SOIC16 + SOIC8 or SOIC16

Optical Sensors

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Infrared Sensors

PART NUMBER	DESCRIPTION	UV INDEX	LED DRIVERS	GESTURE/MOTION SENSING	ALS	INTERFACE	PACKAGE	DEV KIT
Si1132	UV Index/Ambient Light Sensor	•	—	—	•	I^2C/SMBus	2 x 2 mm QFN10	UVIRSLIDER2EK
Si1147	UV Index/Proximity/Ambient Light Sensor	•	3	3D gesture detection	•	I^2C/SMBus	2 x 2 mm QFN10	UVIRSLIDER2EK
Si1146	UV Index/Proximity/Ambient Light Sensor	•	2	3D gesture detection	•	I^2C/SMBus	2 x 2 mm QFN10	UVIRSLIDER2EK
Si1145	UV Index/Proximity/Ambient Light Sensor	•	1	Motion sensing	•	I^2C/SMBus	2 x 2 mm QFN10	UVIRSLIDER2EK
Si1143	Proximity/Ambient Light Sensor		3	3D gesture detection	•	I^2C/SMBus	2 x 2 mm QFN10	Si1140DK
Si1142	Proximity/Ambient Light Sensor		2	3D gesture detection	•	I^2C/SMBus	2 x 2 mm QFN10	Si1140DK
Si1141	Proximity/Ambient Light Sensor		1	Motion sensing	•	I^2C/SMBus	2 x 2 mm QFN10	Si1140DK
Si1120	Proximity/Ambient Light Sensor		1	Motion sensing	•	PWM	3 x 3 mm ODFN8	Si1120EK
Si1102	Proximity Sensor		1	—		Digital (On/Off)	3 x 3 mm ODFN8	Si1102EK

Digital I²C Relative Humidity and Temperature Sensors

PART NUMBER	DESCRIPTION	TYP ACCURACY TEMP.	RH	TEMP. RANGE	FILTER COVER	PACKAGE FORMAT	PACKAGE
Si7013-A10-GM	Digital I²C RH and 2-zone temperature sensor IC	±0.4 °C	±3%	–40 to 85 ºC		Tube	DFN10
Si7013-A10-GM1R	Digital I²C RH and 2-zone temperature sensor IC with pre-installed protective cover	±0.4 °C	±3%	–40 to 85 ºC	•	Cut Tape	DFN10
Si7013-A10-IM	Digital I²C RH and 2-zone temperature sensor IC	±0.4 °C	±3%	–40 to 125 ºC		Tube	DFN10
Si7013-A10-IM1R	Digital I²C RH and 2-zone temperature sensor IC with pre-installed protective cover	±0.4 °C	±3%	–40 to 125 ºC	•	Cut Tape	DFN10
Si7020-A10-GM	Digital I²C RH and temperature sensor IC	±0.4 °C	±4%	–40 to 85 ºC		Tube	DFN6
Si7020-A10-GM1R	Digital I²C RH and temperature sensor IC with pre-installed protective cover	±0.4 °C	±4%	–40 to 85 ºC	•	Cut Tape	DFN6
Si7020-A10-IM	Digital I²C RH and temperature sensor IC	±0.4 °C	±4%	–40 to 125 ºC		Tube	DFN6
Si7020-A10-IM1R	Digital I²C RH and temperature sensor IC with pre-installed protective cover	±0.4 °C	±4%	–40 to 125 ºC	•	Cut Tape	DFN6
Si7021-A10-GM	Digital I²C RH and temperature sensor IC	±0.4 °C	±3%	–40 to 85 ºC		Tube	DFN6
Si7021-A10-GM1R	Digital I²C RH and temperature sensor IC with pre-installed protective cover	±0.4 °C	±3%	–40 to 85 ºC	•	Cut Tape	DFN6
Si7021-A10-IM	Digital I²C RH and temperature sensor IC	±0.4 °C	±3%	–40 to 125 ºC		Tube	DFN6
Si7021-A10-IM1	Digital I²C RH and temperature sensor IC with pre-installed protective cover	±0.4 °C	±3%	–40 to 125 ºC	•	Cut Tape	DFN6

Video

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Digital TV Demodulator

PART NUMBER	DESCRIPTION	PACKAGE
Si2160	DVB-C2/C/T/S2/S Digital Demodulator	7 x 7 mm QFN48
Si2161-D	DVB-T Demodulator	5 x 6 mm QFN36
Si2162	DVB-C2/C/T2/T Digital Demodulator	7 x 7 mm QFN48
Si2163	DVB-C Demodulator	5 x 6 mm QFN36
Si2164	Universal DVB-C2/C/T2/T/S2/S Digital Demodulator	7 x 7 mm QFN48
Si2165-D	DVB-T/C Demodulator	5 x 6 mm QFN36
Si2167-B	DVB-T/C/S2/S Digital Demodulator	7 x 7 mm QFN48
Si2168-B	DVB-T2/T/C Digital Demodulator	7 x 7 mm QFN48
Si2169-B	DVB-T2/T/C/S2/S Digital Demodulator	7 x 7 mm QFN48

Dual Digital TV Demodulator

PART NUMBER	DESCRIPTION	PACKAGE
Si21602	Dual DVB-C2/C/T/S2/S Digital Demodulator	8 x 8 mm QFN68
Si21622	Dual DVB-C2/C/T2/T Digital Demodulator	8 x 8 mm QFN68
Si21642	Universal Dual DVB-C2/C/T2/T/S2/S Digital Demodulator	8 x 8 mm QFN68
Si21662	Dual DVB-S2/S Digital Demodulator	8 x 8 mm QFN68
Si21672	Dual DVB-T/C/S2/S Digital Demodulator	8 x 8 mm QFN68
Si21682	Dual DVB-T2/T/C Digital Demodulator	8 x 8 mm QFN68
Si21692	Dual DVB-T2/T/C/S2/S Digital Demodulator	8 x 8 mm QFN68

Worldwide TV Tuners

PART NUMBER	DESCRIPTION	PACKAGE
Si2127	Worldwide Analog TV Tuner for NTSC, PAL/SECAM, ATSC/QAM, DVBT2/T/C2/C, ISDB-T/C, DTMB	QFN28
Si2137	Worldwide Analog TV Tuner with Analog Demodulator for NTSC, PAL/SECAM, ATSC/QAM, DVBT2/T/C2/C, ISDB-T/C, DTMB	QFN28
Si2147	Worldwide Digital TV Tuner for NTSC, PAL/SECAM, ATSC/QAM, DVBT2/T/C2/C, ISDB-T/C, DTMB	QFN28
Si2157	Worldwide Digital and Analog TV Tuner for NTSC, PAL/SECAM, ATSC/QAM, DVBT2/T/C2/C, ISDB-T/C, DTMB	QFN28
Si2177	Worldwide Digital and Analog TV Tuner with Analog Demodulator for NTSC, PAL/SECAM, ATSC/QAM, DVBT2/T/C2/C, ISDB-T/C, DTMB	QFN28
Si2128	Worldwide Analog TV Tuner for NTSC, PAL/SECAM	QFN28

PART NUMBER	DESCRIPTION	PACKAGE
Si2138	Worldwide Analog TV Tuner with Analog Demodulator for NTSC, PAL/SECAM.	QFN28
Si2148	Worldwide Digital TV Tuner for ATSC/QAM, DVBT2/T/C2/C, ISDB-T/C, DTMB	QFN28
Si2158	Worldwide Digital and Analog TV Tuner for NTSC, PAL/SECAM, ATSC/QAM, DVB-T2/T/C2/C, ISDB-T/C, DTMB	QFN28
Si2178	Worldwide Digital and Analog TV Tuner with Analog Demodulator for NTSC, PAL/SECAM, ATSC/QAM, DVBT2/T/C2/C, ISDB-T/C, DTMB	QFN28

Voice

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Digital PCM Interface ProSLICs

PART NUMBER	# OF FXS CHANNELS	# OF FXO CHANNELS	MAX V BATTERY	WIDEBAND	DTMF DETECTION	PULSE METERING	TRACKING DC-DC	SHARED DC-DC	DAISY-CHAIN MODE	PACKAGE
Si32170-C	1		-140 V		•	•	•		•	LGA42; 5x7 mm
Si32171-C	1		-110 V		•	•	•		•	LGA42; 5x7 mm
Si32174-C	1		-110 V	•	•		•		•	LGA42; 5x7 mm
Si32176-C	1		-110 V	•			•		•	LGA42; 5x7 mm
Si32177-C	1		-140 V	•			•		•	LGA42; 5x7 mm
Si32178-B	1	1	-110 V	•	•		•		•	LGA42; 5x7 mm
Si32179-B	1	1	-136 V	•	•		•		•	LGA42; 5x7 mm
Si32260-C	2		-110 V	•	•	•	•			LGA47; 6x8 mm
								•	•	LGA60; 8x8 mm
Si32261-C	2		-140 V	•	•	•	•			LGA47; 6x8 mm
								•	•	LGA60; 8x8 mm

Digital Integrated Serial Interface (ISI) ProSLICs

PART NUMBER	# OF FXS CHANNELS	# OF FXO CHANNELS	MAX V BATTERY	WIDEBAND	DTMF DETECTION	PULSE METERING	TRACKING DC-DC	SHARED DC-DC	DAISY-CHAIN MODE	PACKAGE
Si32172-C	1		-110 V	•			•			LGA42; 5x7 mm
Si32173-C	1		-140 V	•			•			LGA42; 5x7 mm
Si32175-C	1		-110 V		•	•	•			LGA42; 5x7 mm
Si32266-C	2		-110 V	•	•	•	•			LGA50; 6x8 mm
Si32267-C	2		-140 V	•	•	•	•			LGA50; 6x8 mm
Si32268-C	2		-110 V	•	•	•		•		LGA50; 6x8 mm
Si32269-C	2		-140 V	•	•	•		•		LGA50; 6x8 mm

Analog Interface ProSLICs

PART NUMBER	# OF FXS CHANNELS	# OF FXO CHANNELS	MAX V BATTERY	WIDEBAND AUDIO	DTMF DETECTION	PULSE METERING	TRACKING DC-DC	SHARED DC-DC	DAISY-CHAIN MODE	PACKAGE
Si32391-B	1		-136 V	•			•			QFN48; 6x6mm, 7x7 mm
Si32392-B	2		-136 V	•			•			QFN48; 6x6mm, 7x7 mm

Voice Codec

PART NUMBER	MICROPHONE AMPLIFIER	INPUT MIXER	HEADPHONE DRIVER	HANDSET HYBRID	PACKAGE
Si3000	•	•	•	•	SOIC16

Wireless Products

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Ember® ZigBee® ICs

PART NUMBER	FLASH (kB)	RAM (kB)	DATA RATE	FREQ. RANGE (MHz)	SENS. (boost)	ADJ CHANNEL REJECTION (15.4)	ALT CHANNEL REJECTION (15.4)	802.11g REJECTION +12/-13 MHz	TX POWER (boost)	TOTAL LINK BUDGET	DEEP SLEEP CURRENT	RX CURRENT	TX CURRENT (@ +3 dBm)	CRYSTAL FREQ.	VOLTAGE (V)	PACKAGE
EM351	128	12	250 kbps	2400-2500	-102 dBm	35 dB	46 dB	36 dB	-55 to +8 dBm	110 dB (boost)	0.4 µA (timer)	26.5 mA (normal)	31 mA (normal)	24 MHz	2.1 to 3.6	QFN48
EM357	192	12	250 kbps	2400-2500	-102 dBm	35 dB	46 dB	36 dB	-55 to +8 dBm	110 dB (boost)	0.4 µA (timer)	26.5 mA (normal)	31 mA (normal)	24 MHz	2.1 to 3.6	QFN48
EM3581	256	32	250 kbps	2400-2500	-102 dBm	35 dB	46 dB	36 dB	-55 to +8 dBm	110 dB (boost)	1.08 µA (timer)	26.5 mA (normal)	31 mA (normal)	24 MHz	2.1 to 3.6	QFN48
EM3582	256	32	250 kbps	2400-2500	-102 dBm	35 dB	46 dB	36 dB	-55 to +8 dBm	110 dB (boost)	1.08 µA (timer)	26.5 mA (normal)	31 mA (normal)	24 MHz	2.1 to 3.6	QFN48
EM3585	512	32	250 kbps	2400-2500	-102 dBm	35 dB	46 dB	36 dB	-55 to +8 dBm	110 dB (boost)	1.08 µA (timer)	26.5 mA (normal)	31 mA (normal)	24 MHz	2.1 to 3.6	QFN48
EM3586	512	32	250 kbps	2400-2500	-102 dBm	35 dB	46 dB	36 dB	-55 to +8 dBm	110 dB (boost)	1.08 µA (timer)	26.5 mA (normal)	31 mA (normal)	24 MHz	2.1 to 3.6	QFN48
EM3587	512	64	250 kbps	2400-2500	-102 dBm	35 dB	46 dB	36 dB	-55 to +8 dBm	110 dB (boost)	1.08 µA (timer)	26.5 mA (normal)	31 mA (normal)	24 MHz	2.1 to 3.6	QFN48
EM3588	512	64	250 kbps	2400-2500	-102 dBm	35 dB	46 dB	36 dB	-55 to +8 dBm	110 dB (boost)	1.08 µA (timer)	26.5 mA (normal)	31 mA (normal)	24 MHz	2.1 to 3.6	QFN48

EZRadio® Universal ISM Band RF ICs

PART NUMBER	TYPE	MODULATION SCHEME (MAX kbps) FSK	MODULATION SCHEME (MAX kbps) OOK	FREQUENCY BANDS (MHz) 315	434	868	915	OUTPUT POWER MAX (dBm) 868 MHz BAND	OUTPUT POWER MAX (dBm) 434 MHz BAND	SUPPLY VOLTAGE (V)	SENSITIVITY (dBm)	PACKAGE
Si4010	MCU +TX	100	50	27 - 960				10		1.8 - 3.6	—	MSOP10/SOIC14
Si4012	TX	100	50	27 - 960				10		1.8 - 3.6	—	MSOP10/SOIC14
Si4313	RX	256	40	•	•	•	•	—	—	1.8 - 3.6	-118/-107	QFN20
Si4355	RX	500	120	•	•	•	•	—	—	1.8 - 3.6	-116	QFN20
Si4356	RX	120	120	•	•	•	•	—	—	1.8 - 3.6	-113	QFN20
Si4455	TRX	500	120	•	•	•	•	12	13	1.8 - 3.6	-116	QFN20

EZRadioPRO® Enhanced Feature Universal ISM Band RF ICs

PART NUMBER	TYPE	MODULATION SCHEME (MAX kbps) FSK	MODULATION SCHEME (MAX kbps) OOK	FREQUENCY RANGE (MHz)	OUTPUT POWER RANGE (dBm)	SENSITIVITY (dBm) 2.0 kbps FSK	SENSITIVITY (dBm) 4.8 kbps OOK	RX CURRENT (mA)	TX CURRENT (dBm) 0	+11	+13	+20	PACKAGE
									mA				
Si4030	TX	256	40	900 - 960	-8 to +13	—	—	—	18		30		QFN20
Si4031	TX	256	40	240 - 930	-8 to +13	—	—	—	18		30		QFN20
Si4032	TX	256	40	240 - 930	+1 to +20	—	—	—		35		85	QFN20
Si4060	TX	1000	120	142 - 1050 Major Bands	-40 to +13	—	—	—		18			QFN20
Si4063	TX	1000	120	142 - 1050 Major Bands	-20 to +20	—	—	—				85	QFN20
Si4330	RX	256	40	240 - 960	—	-121	-110	18.5 mA					QFN20
Si4362	RX	1000	120	142 - 1050 Major Bands	—	-124	-112	10/13 mA					QFN20
Si4430	TRX	256	40	900 - 960	-8 to +13	-12	-110	18.5 mA	18		30		QFN20
Si4431	TRX	256	40	240 - 930	-8 to +13	-121	-110	18.5 mA	18		30		QFN20
Si4432	TRX	256	40	240 - 930	+1 to +20	-121	-110	18.5 mA		35		85	QFN20
Si4438	TRX	500	120	425 - 525	-20 to +20	-121	-110	14 mA				75	QFN20
Si4460	TRX	1000	120	142 - 1050 Major Bands	-40 to +13	-124	-112	10/13 mA		18	25		QFN20
Si4461	TRX	1000	120	142 - 1050 Major Bands	-30 to +16	-124	-112	10/13 mA			31		QFN20
Si4463	TRX	1000	120	142 - 1050 Major Bands	-20 to +20	-124	-112	10/13 mA				85	QFN20
Si4464	TRX	1000	120	119 - 960 Banded	-20 to +20	-124	-112	10/13 mA				85	QFN20

Silicon Labs' products are designed and manufactured to ISO 9001, ISO 14001 and ISO/TS 16949 standards.

ISO 9001
Quality Management System
Design and Manufacture of Integrated Circuits
Certificate Registration No: 951 08 4762

ISO 14001
Environmental Management System
Design and Manufacture of Integrated Circuits
Certificate Registration No: 951 09 4998

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