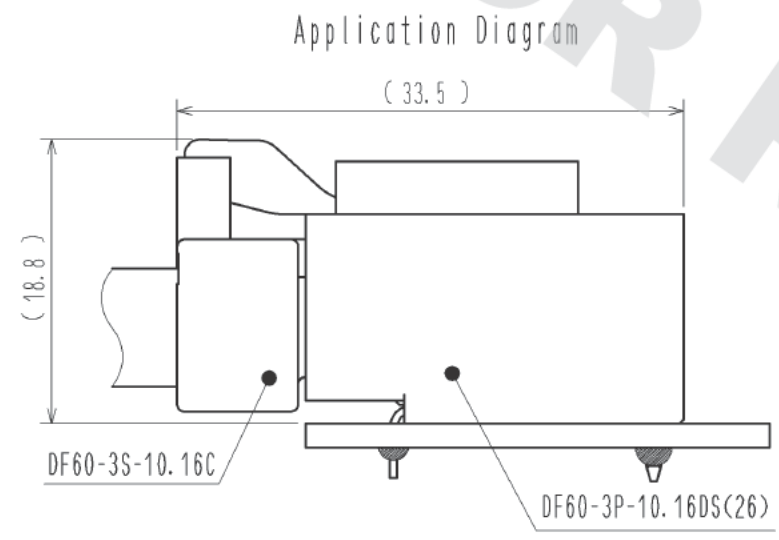
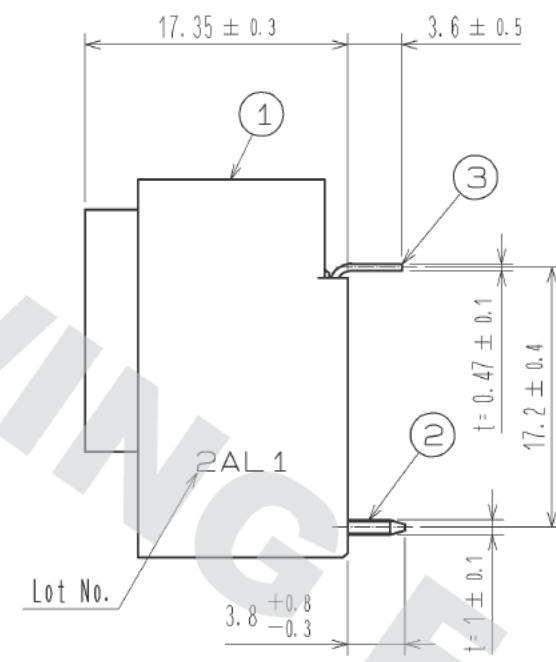
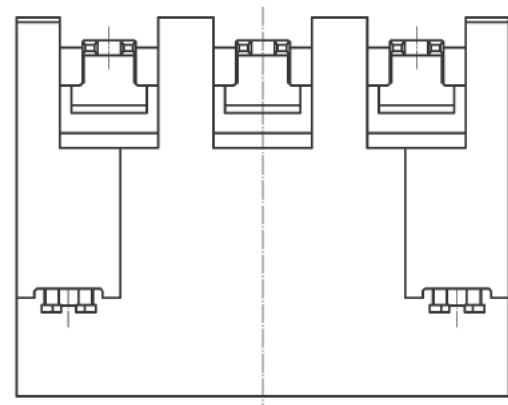
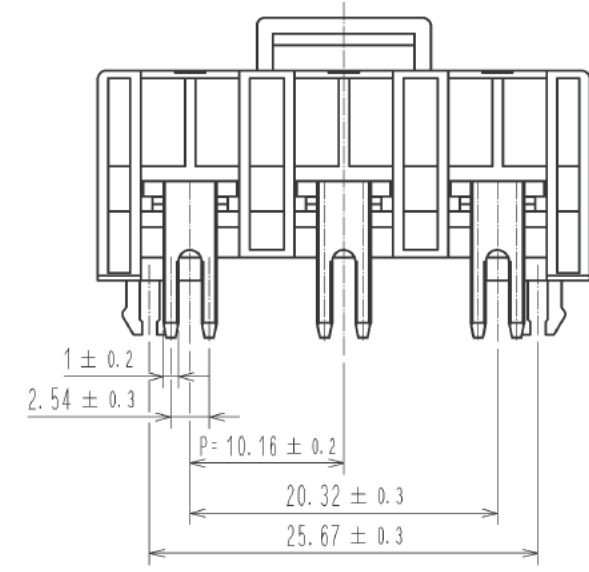
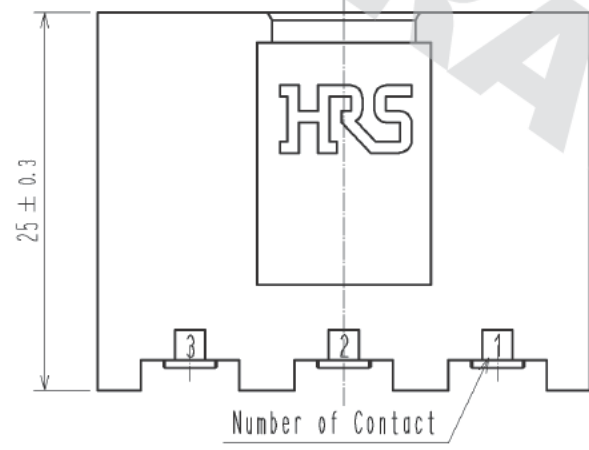
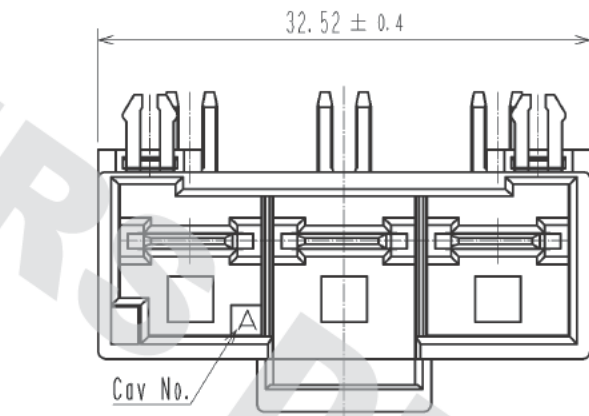
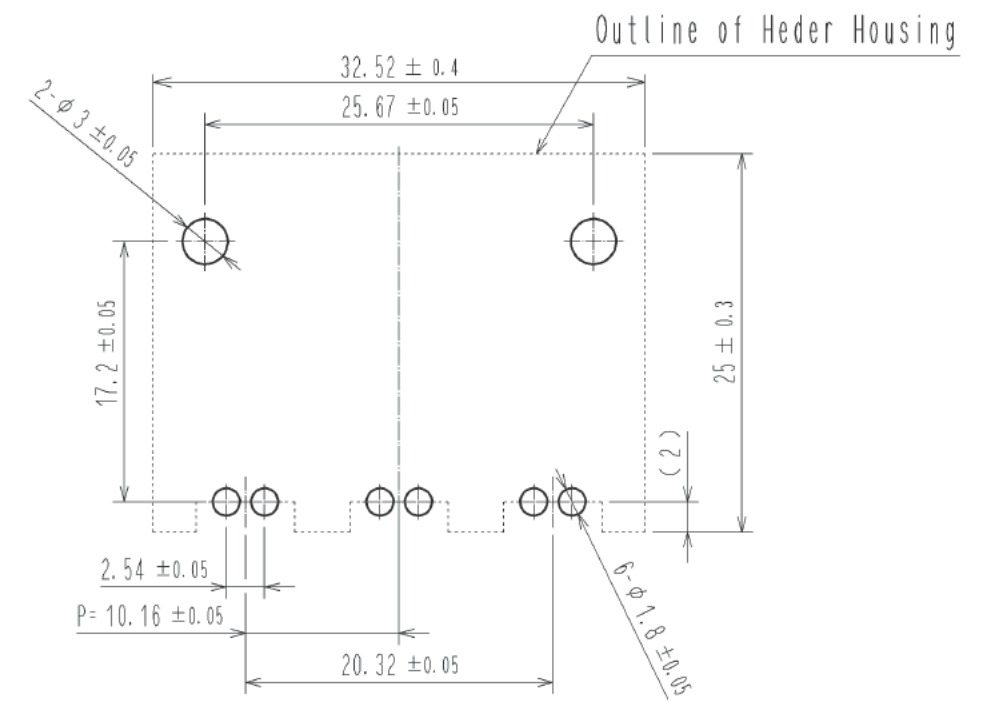


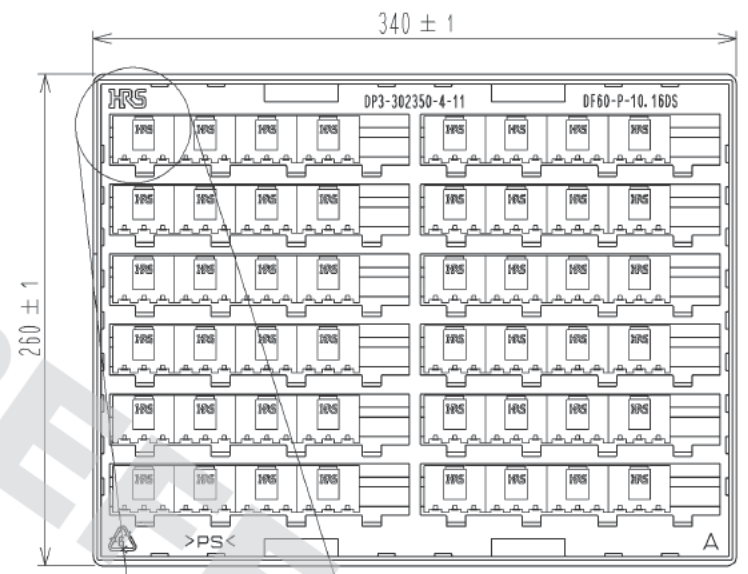
DRAWING FOR REFERENCE: This is subject to change without notice
05/31/2013



Recommended PCB Layout
(Board thickness : t=1.6±0.1mm)



Packing Diagram(FREE)



NOTE 1. Per TRAY : 48 Pieces.

1	PBT	BLACK, UL94-V0	3	PS	Clear
NO.	MATERIAL	FINISH, REMARKS	NO.	Brass	Contact Area : Au Plating 0.2μm min Lead area : Tin Plating 1μm min Under Plating : Ni Plating 1μm min
UNITS mm		SCALE 2 : 1	COUNT	DESCRIPTION OF REVISIONS	DESIGNED
	HIROSE ELECTRIC CO., LTD.	APPROVED : KI. AKIYAMA	12.05.29	DRAWING NO.	EDC3-342145-01
		CHECKED : SZ. ONO	12.05.29	PART NO.	DF60-3P-10.16DS(26)
		DESIGNED : TH. YOSHIKAWA	12.05.28	CODE NO.	CL680-3017-7-26
		DRAWN : TH. YOSHIKAWA	12.05.28		