

MC78XXE

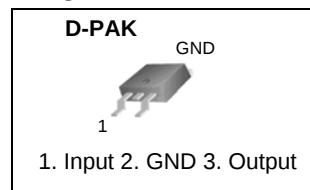
3-Terminal 1A Positive Voltage Regulator

Features

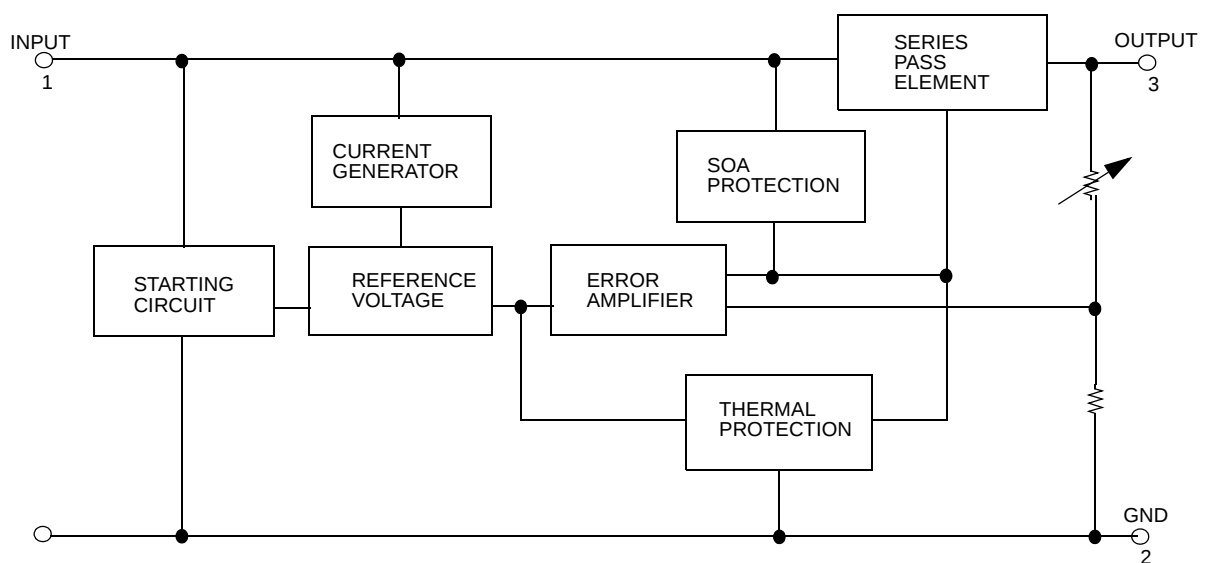
- Output Current up to 1A
- Output Voltages of 5, 12V
- Thermal Overload Protection
- Short Circuit Protection
- Output Transistor Safe Operating Area Protection

Description

The MC78XXE series of three terminal positive regulators are available in the D-PAK package and with several fixed output voltages, making them useful in a wide range of applications. Each type employs internal current limiting, thermal shut down and safe operating area protection, making it essentially indestructible. If adequate heat sinking is provided, they can deliver over 1A output current. Although designed primarily as fixed voltage regulators, these devices can be used with external components to obtain adjustable voltages and currents.



Internal Block Diagram



Rev. 1.0.2

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Input Voltage (for $V_O = 5V$ to $18V$)	V_I	35	V
Operating Temperature Range	T_{OPR}	$0 \sim +125$	$^{\circ}C$
Storage Temperature Range	T_{STG}	$-65 \sim +150$	$^{\circ}C$

Electrical Characteristics (MC7805E)

(Refer to test circuit , $0^{\circ}C < T_J < 125^{\circ}C$, $I_O = 500mA$, $V_I = 10V$, $C_I = 0.33\mu F$, $C_O = 0.1\mu F$, unless otherwise specified)

Parameter	Symbol	Conditions		MC7805E/LM7805E			Unit
				Min.	Typ.	Max.	
Output Voltage	VO	TJ = +25°C		4.8	5.0	5.2	V
		5.0mA ≤ IO ≤ 1.0A, PO ≤ 15W VI = 7V to 20V		4.75	5.0	5.25	
Line Regulation (Note1)	Regline	TJ = +25°C	VO = 7V to 25V	-	4.0	100	mV
			VI = 8V to 12V	-	1.6	50	
Load Regulation (Note1)	Regload	TJ = +25°C	IO = 5.0mA to1.5A	-	9	100	mV
			IO =250mA to 750mA	-	4	50	
Quiescent Current	IQ	TJ = +25°C		-	5.0	8.0	mA
Quiescent Current Change	ΔIQ	IO = 5mA to 1.0A		-	0.03	0.5	mA
		VI = 7V to 25V		-	0.3	1.3	
Output Voltage Drift (Note2)	ΔVO/ΔT	IO = 5mA		-	-0.8	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100kHz, TA = +25°C		-	42	-	μV/VO
Ripple Rejection (Note2)	RR	f = 120Hz VO = 8V to 18V		62	73	-	dB
Dropout Voltage	VDrop	IO = 1A, TJ =+25°C		-	2	-	V
Output Resistance (Note2)	ro	f = 1kHz		-	15	-	mΩ
Short Circuit Current	ISC	VI = 35V, TA =+25°C		-	230	-	mA
Peak Current (Note2)	IPK	TJ = +25°C		-	2.2	-	A

Note:

1. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
2. These parameters, although guaranteed, are not 100% tested in production.

Electrical Characteristics (MC7812E)

(Refer to test circuit , $0^{\circ}\text{C} < T_J < 125^{\circ}\text{C}$, $I_O = 500\text{mA}$, $V_I = 19\text{V}$, $C_I = 0.33\mu\text{F}$, $C_O = 0.1\mu\text{F}$, unless otherwise specified)

Parameter	Symbol	Conditions		MC7812E			Unit
				Min.	Typ.	Max.	
Output Voltage	V_O	$T_J = +25^{\circ}\text{C}$		11.5	12	12.5	V
		$5.0\text{mA} \leq I_O \leq 1.0\text{A}$, $P_O \leq 15\text{W}$ $V_I = 14.5\text{V to } 27\text{V}$		11.4	12	12.6	
Line Regulation (Note1)	Regline	$T_J = +25^{\circ}\text{C}$	$V_I = 14.5\text{V to } 30\text{V}$	-	10	240	mV
			$V_I = 16\text{V to } 22\text{V}$	-	3.0	120	
Load Regulation (Note1)	Regload	$T_J = +25^{\circ}\text{C}$	$I_O = 5\text{mA to } 1.5\text{A}$	-	11	240	mV
			$I_O = 250\text{mA to } 750\text{mA}$	-	5.0	120	
Quiescent Current	I_Q	$T_J = +25^{\circ}\text{C}$		-	5.1	8.0	mA
Quiescent Current Change	ΔI_Q	$I_O = 5\text{mA to } 1.0\text{A}$		-	0.1	0.5	mA
		$V_I = 14.5\text{V to } 30\text{V}$		-	0.5	1.0	
Output Voltage Drift (Note2)	$\Delta V_O / \Delta T$	$I_O = 5\text{mA}$		-	-1	-	mV/ $^{\circ}\text{C}$
Output Noise Voltage	V_N	$f = 10\text{Hz to } 100\text{kHz}$, $T_A = +25^{\circ}\text{C}$		-	76	-	$\mu\text{V}/V_O$
Ripple Rejection (Note2)	RR	$f = 120\text{Hz}$ $V_I = 15\text{V to } 25\text{V}$		55	71	-	dB
Dropout Voltage	V_{Drop}	$I_O = 1\text{A}$, $T_J = +25^{\circ}\text{C}$		-	2	-	V
Output Resistance (Note2)	r_O	$f = 1\text{kHz}$		-	18	-	$\text{m}\Omega$
Short Circuit Current	I_{SC}	$V_I = 35\text{V}$, $T_A = +25^{\circ}\text{C}$		-	230	-	mA
Peak Current (Note2)	I_{PK}	$T_J = +25^{\circ}\text{C}$		-	2.2	-	A

Note:

1. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.
2. These parameters, although guaranteed, are not 100% tested in production.

Typical Performance Characteristics

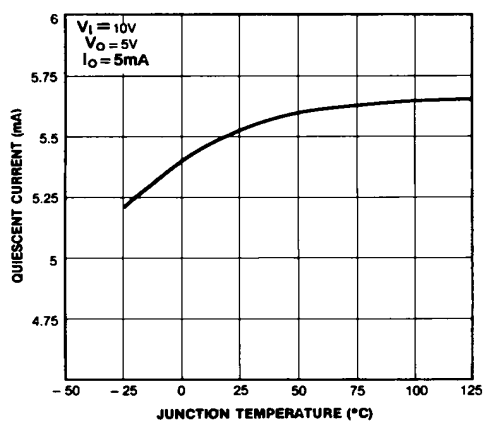


Figure 1. Quiescent Current

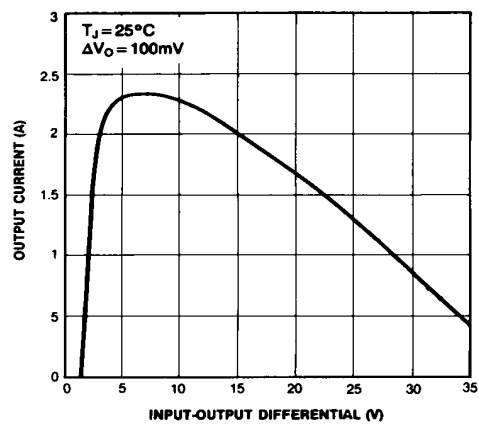


Figure 2. Peak Output Current

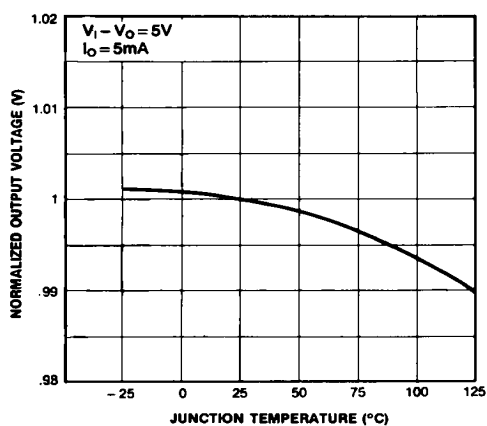


Figure 3. Output Voltage

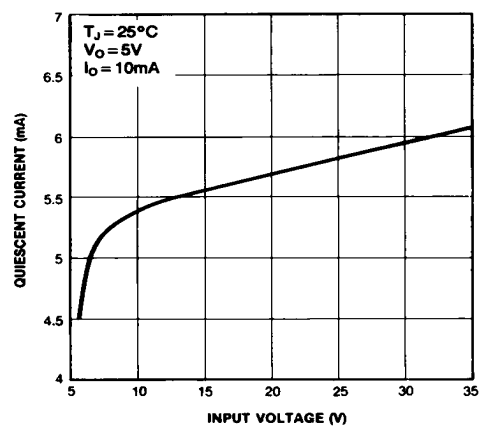


Figure 4. Quiescent Current

Typical Applications

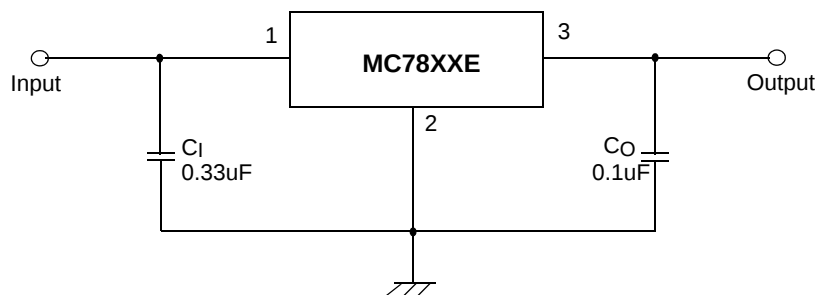


Figure 5. DC Parameters

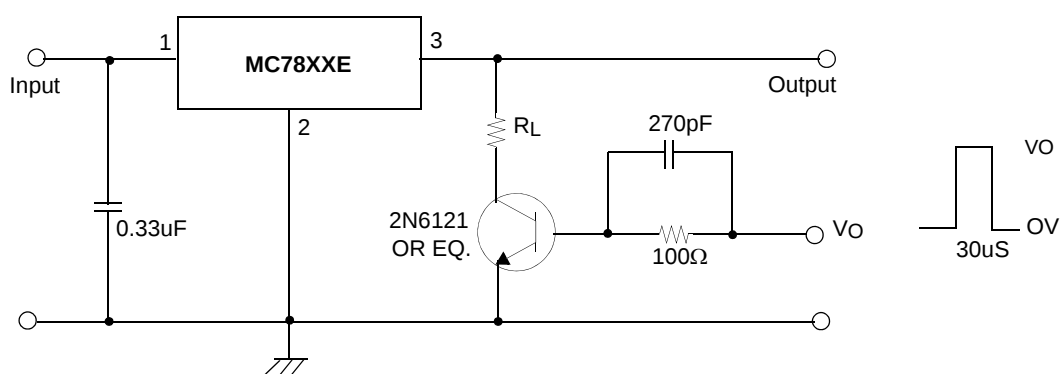


Figure 6. Load Regulation

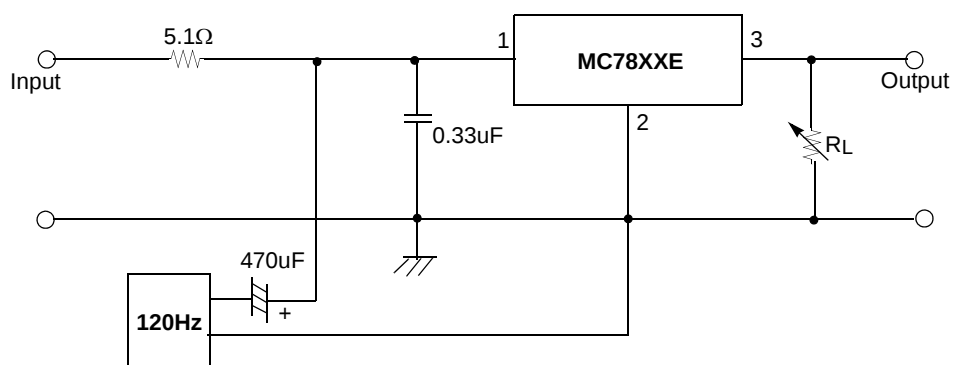


Figure 7. Ripple Rejection

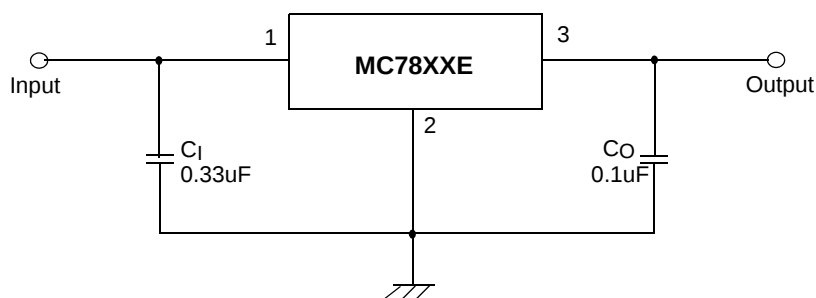


Figure 8. Fixed Output Regulator

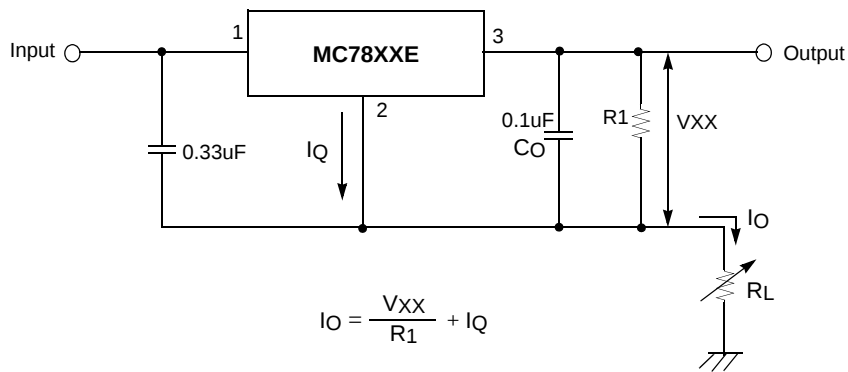


Figure 9. Constant Current Regulator

Notes:

- (1) To specify an output voltage, substitute voltage value for "XX." A common ground is required between the input and the Output voltage. The input voltage must remain typically 2.0V above the output voltage even during the low point on the input ripple voltage.
- (2) C_I is required if regulator is located an appreciable distance from power Supply filter.
- (3) C_O improves stability and transient response.

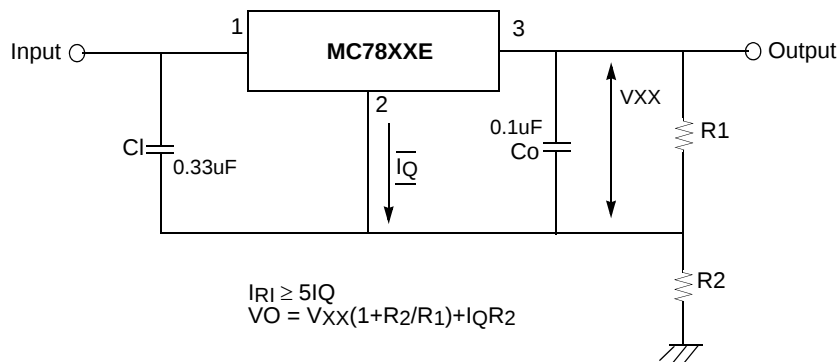


Figure 10. Circuit for Increasing Output Voltage

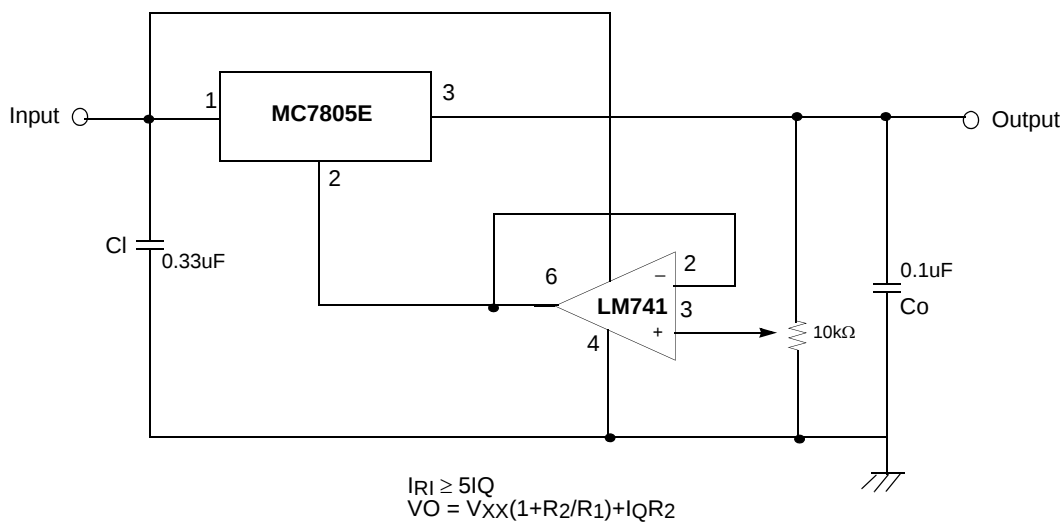


Figure 11. Adjustable Output Regulator (7 to 30V)

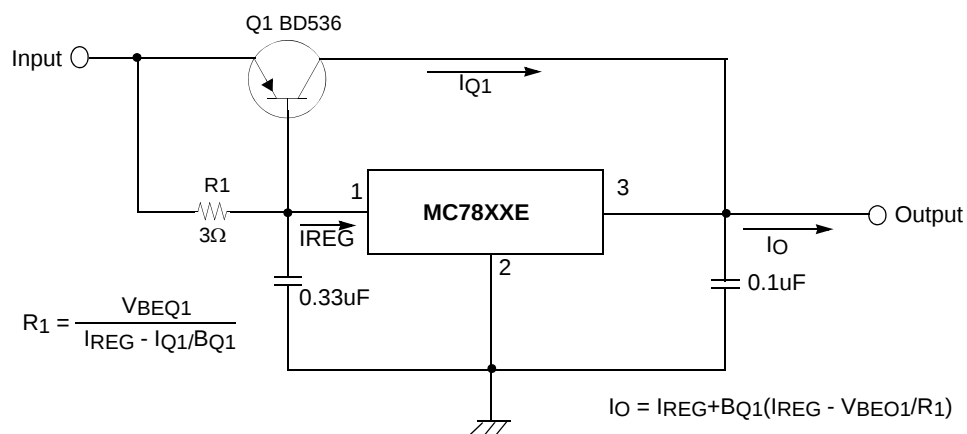


Figure 12. High Current Voltage Regulator

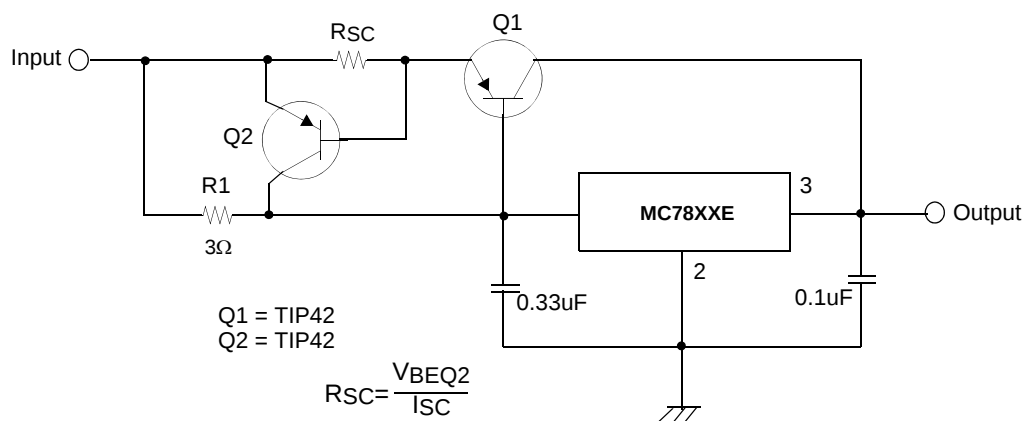


Figure 13. High Output Current with Short Circuit Protection

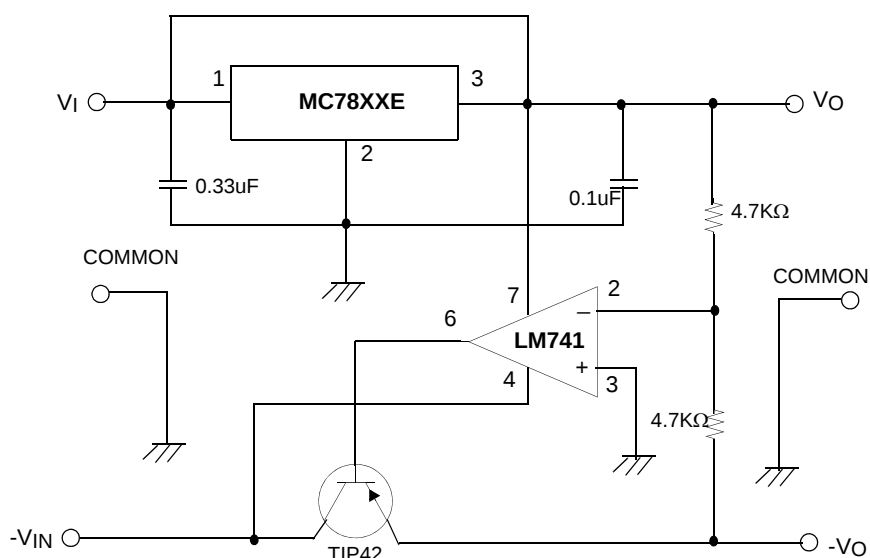


Figure 14. Tracking Voltage Regulator

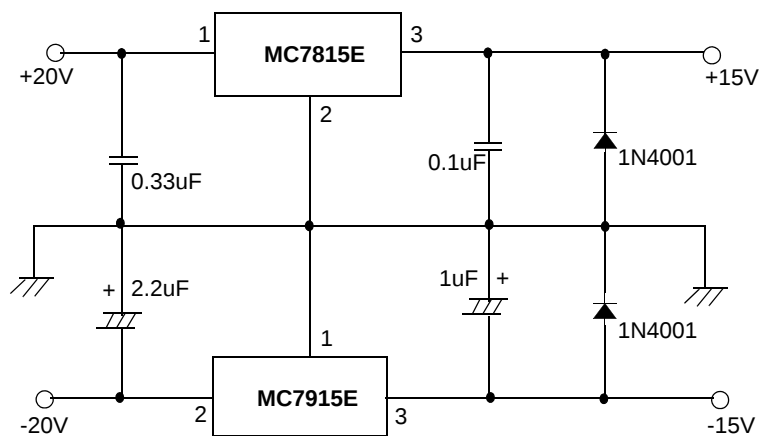


Figure 15. Split Power Supply (±15V-1A)

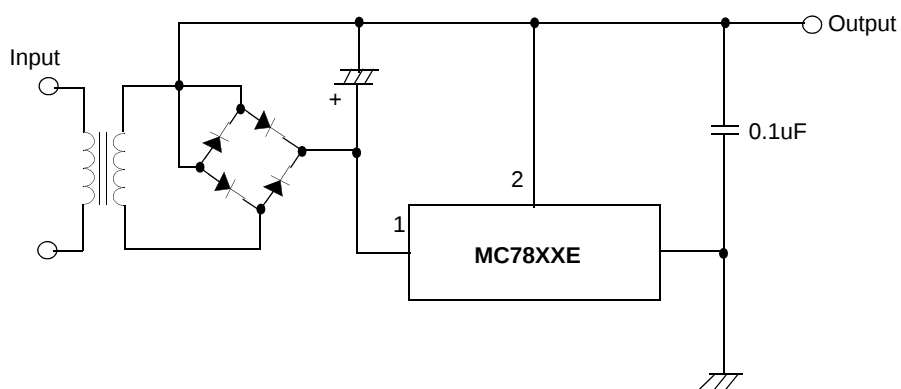


Figure 16. Negative Output Voltage Circuit

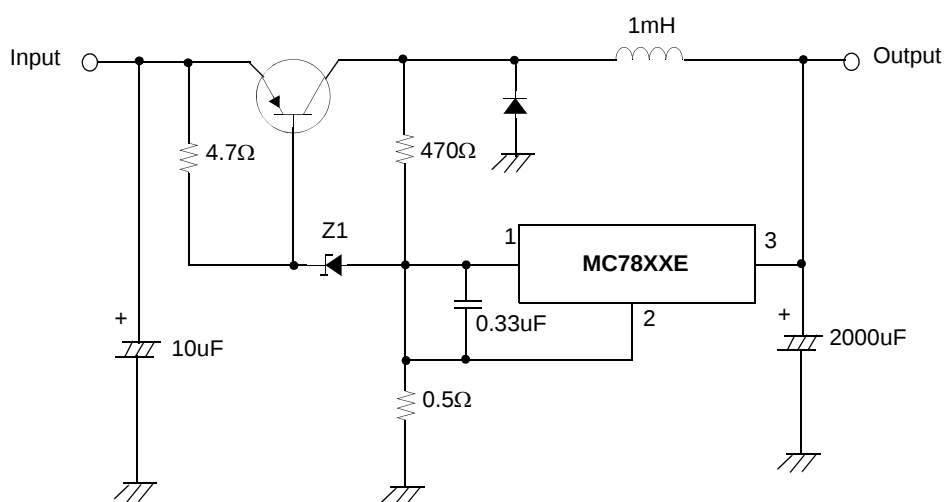


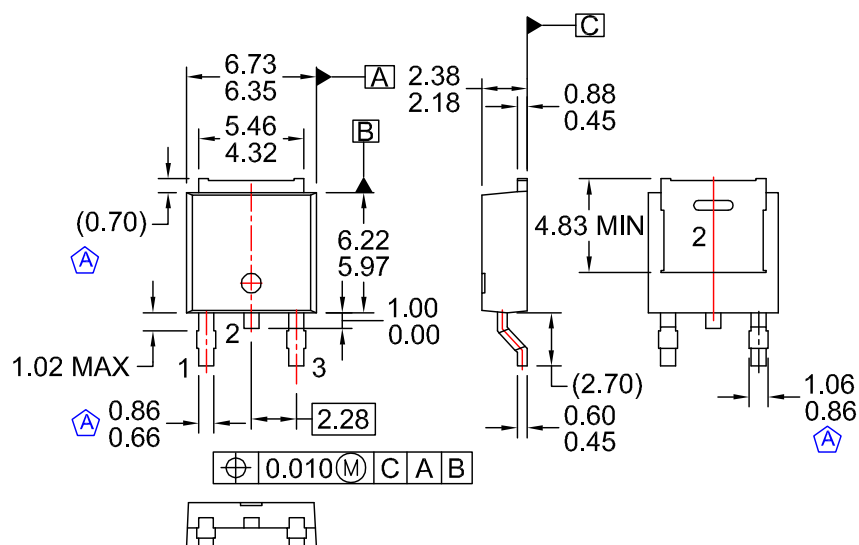
Figure 17. Switching Regulator

Mechanical Dimensions

Package

Dimensions in millimeters

D-PAK



NOTES: UNLESS OTHERWISE SPECIFIED

CONFORMS TO JEDEC TO-252 VARIATION AB
EXCEPT WHERE NOTED

B) ALL DIMENSIONS ARE IN MILLIMETERS.

C) DRAWING CONFORMS TO ASME Y14.5M-1994

D) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

E) FORMERLY NAMED BD1733

F) DRAWING FILE NAME: MKT-TO252D03REV1

Ordering Information

Product Number	Output Voltage Tolerance	Package	Operating Temperature
MC7805ECDTX	±4%	D-PAK	0 ~ +125°C
MC7812ECDTX			

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.