

Small Signal Product

Bi-directional ESD Protection Diode

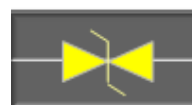
FEATURES

- Meet IEC61000-4-2 (ESD) $\pm 15\text{kV}$ (air), $\pm 8\text{kV}$ (contact)
- Designed for mounting on small surface
- Protects one Bi-directional I/O line
- Moisture sensitivity level 1
- Working Voltage : 5V, 12V, 24V
- Pb free version and RoHS compliant
- Packing code with suffix "G" means green compound (halogen-free)


0603


MECHANICAL DATA

- Case: 0603 small outline plastic package
- Terminal : Gold plated, solder per MIL-STD-705, method 2026 guaranteed
- High temperature soldering guaranteed : $260^{\circ}\text{C}/10\text{s}$
- Weight: $3 \pm 0.5 \text{ mg}$



APPLICATIONS

- Cell Phone Handsets and Accessories
- Notebooks, Desktops, and Servers
- Keypads, Side Keys, USB 2.0, LCD Displays
- Portable Instrumentation
- Touch Panel

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T _A =25°C unless otherwise noted)				
PARAMETER		SYMBOL	VALUE	UNIT
Peak Pulse Power (tp=8/20μs waveform)	TESDU5V0	P _{PP}	75	W
	TESDU12V		25	
	TESDU24V		47	
ESD per IEC 61000-4-2 (Air)		V _{ESD}	± 15	KV
ESD per IEC 61000-4-2 (Contact)			± 8	
Junction and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

PARAMETER			SYMBOL	MIN	MAX	UNIT
Reverse Stand-Off Voltage	TESDU5V0		V _{RWM}	-	5	V
	TESDU12V			-	12	
	TESDU24V			-	24	
Reverse Breakdown Voltage	TESDU5V0	I _R = 1 mA	V _(BR)	5.1	-	V
	TESDU12V			13	-	
	TESDU24V			25	-	
Reverse Leakage Current	TESDU5V0	V _R = 5 V	I _R	-	2	μA
	TESDU12V	V _R = 12 V				
	TESDU24V	V _R = 24 V				
Clamping Voltage	TESDU5V0	I _{PP} = 1 A	V _C	-	9.8	V
		I _{PP} = 5 A		-	15	
Clamping Voltage	TESDU12V	I _{PP} = 1 A	V _C	-	25	V
		I _{PP} = 5 A		-	33	
Clamping Voltage	TESDU24V	I _{PP} = 1 A	V _C	-	47	V
		I _{PP} = 5 A		-	51	
Junction Capacitance	TESDU5V0	V _R = 0 V	C _J	15		pF
	TESDU12V	f = 1.0 MHz		12		
	TESDU24V			10		

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RATINGS AND CHARACTERISTICS CURVES

($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Fig. 1 Non-Repetitive Peak Pulse Power VS. Pulse Time

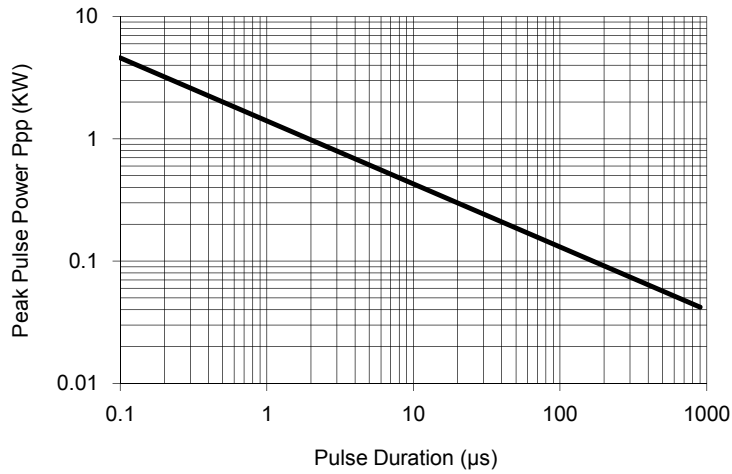


Fig. 2 Pulse Waveform

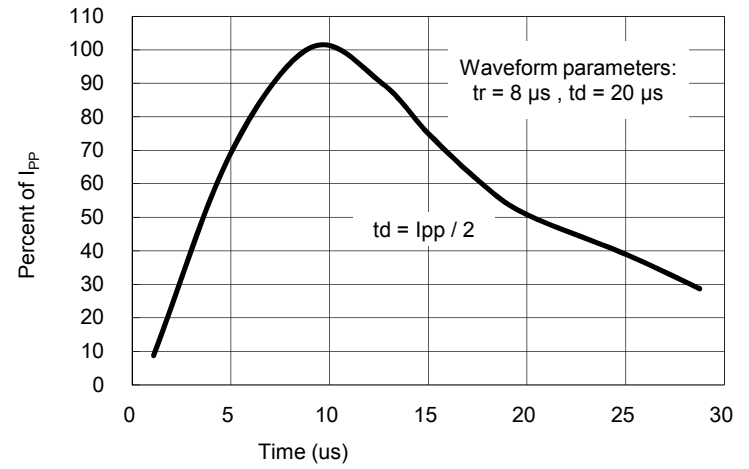


Fig. 3 Admissible Power Dissipation Curve

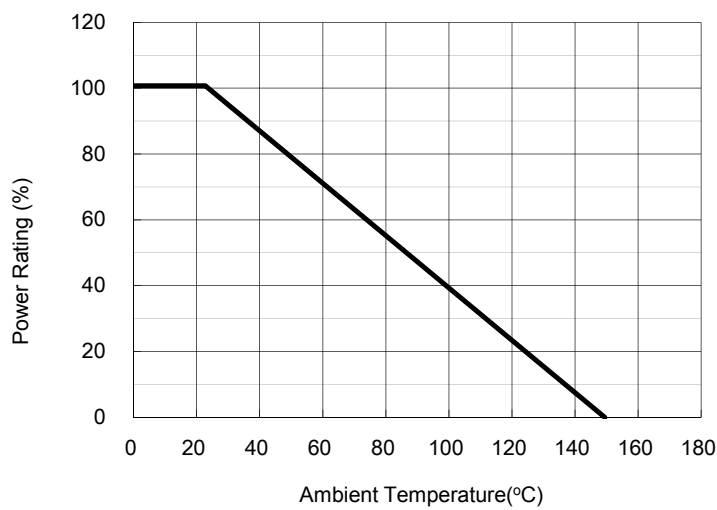


Fig. 4 Typical Junction Capacitance

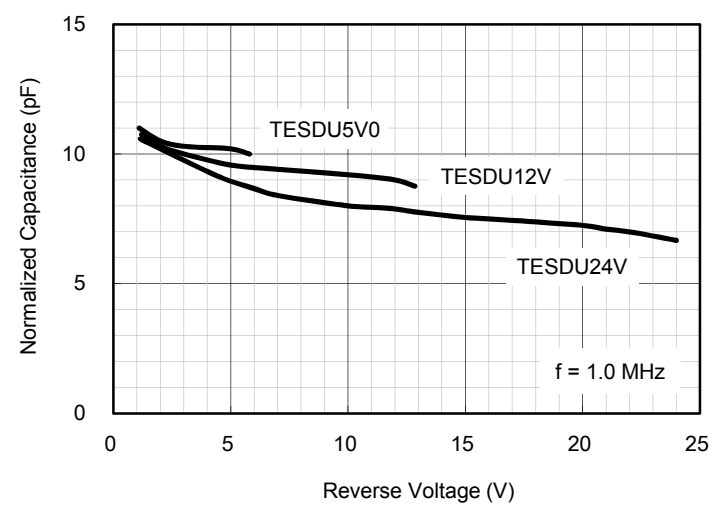
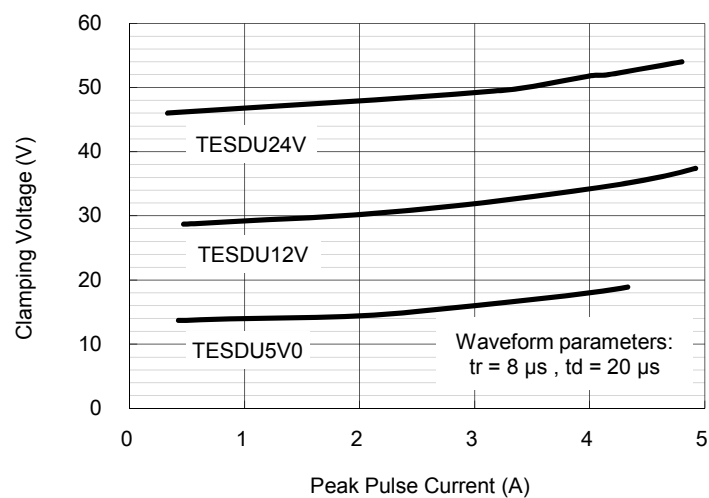
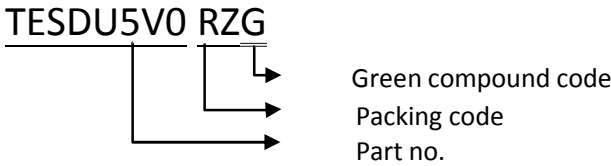


Fig. 5 Clamping Voltage VS. Peak Pulse Current



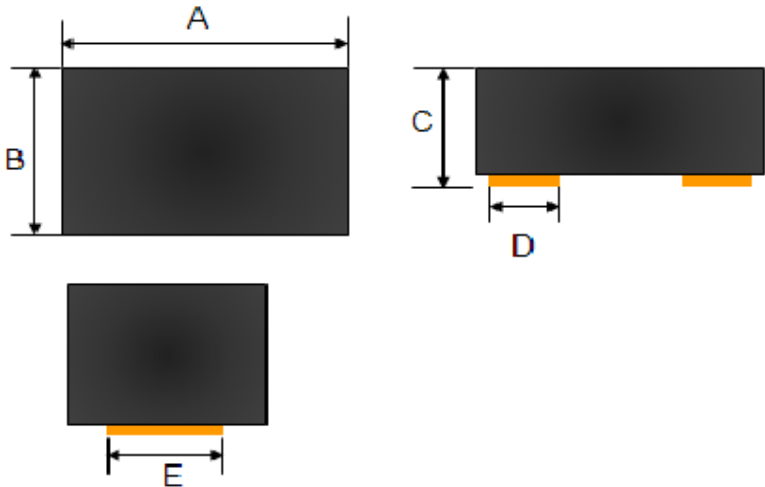
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ORDER INFORMATION (EXAMPLE)



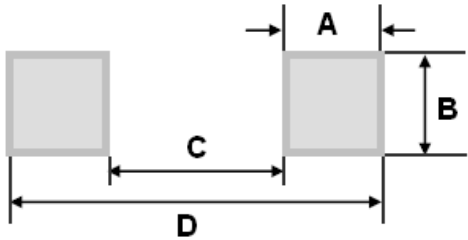
PACKAGE OUTLINE DIMENSIONS

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DIM.	Unit (mm)		Unit (inch)	
	Min	Max	Min	Max
A	1.60	1.80	0.063	0.071
B	0.80	1.00	0.031	0.039
C	0.70	0.85	0.028	0.033
D	0.45 (Typ.)		0.018 (Typ.)	
E	0.70 (Typ.)		0.028 (Typ.)	

SUGGEST PAD LAYOUT



DIM.	Unit (mm)	Unit (inch)
	Typ.	Typ.
A	0.60	0.024
B	1.00	0.039
C	0.65	0.026
D	1.85	0.073

Note: The suggested land pattern dimensions have been provided for reference only, as actual pad layouts may vary depending on application.

MARKING

Part NO.	Marking
TESDU5V0	E05
TESDU12V	E12
TESDU24V	E24

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APPLICATIONS INFORMATION

- ◇ Designed to protect one data, I/O, or power supply line
- ◇ Designed to protect sensitive electronics from damage or latch-up due to ESD
- ◇ Designed to replace multilayer varistors (MLVs) in portable applications
- ◇ Features large cross-sectional area junctions for conducting high transient currents
- ◇ Offers superior electrical characteristics such as lower clamping voltage and no device degradation when compared to MLVs
- ◇ The combination of small size and high ESD surge capability makes them ideal for use in portable applications

CIRCUIT BOARD LAYOUT RECOMMENDATIONS

Good circuit board layout is critical for the suppression of ESD induced transients

- ◇ Place the ESD Protection Diode near the input terminals or connectors to restrict transient coupling
- ◇ Minimize the path length between the ESD Protection Diode and the protected line
- ◇ Minimize all conductive loops including power and ground loops
- ◇ The ESD transient return path to ground should be kept as short as possible
- ◇ Never run critical signals near board edges
- ◇ Use ground planes whenever possible

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