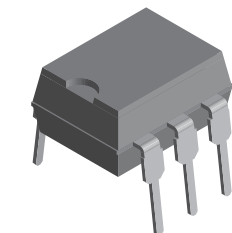
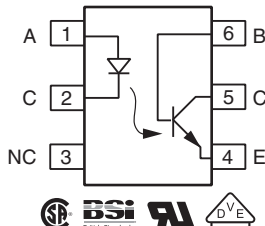


Optocoupler, Phototransistor Output, with Base Connection, 300 V BV_{CEO}



I179004-3



FEATURES

- Good CTR linearity with forward current
- Low CTR degradation
- Very high collector emitter breakdown voltage, $BV_{CEO} = 300\text{ V}$
- Isolation test voltage: 5300 V_{RMS}
- Low coupling capacitance
- High common mode transient immunity
- Phototransistor optocoupler 6 pin DIP package with base connection
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC


RoHS
COMPLIANT

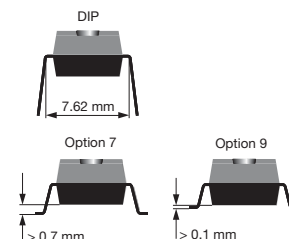
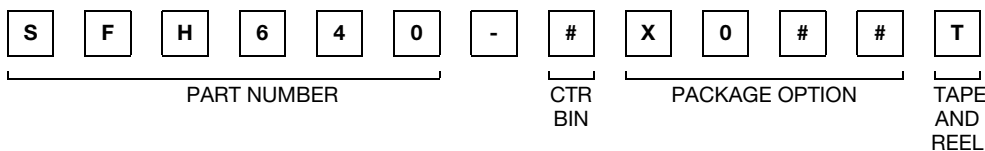
DESCRIPTION

The SFH640 is an optocoupler with very high BV_{CEO} , a minimum of 300 V. It is intended for telecommunications applications or any DC application requiring a high blocking voltage.

AGENCY APPROVALS

- UL1577, file no. E52744 system code H or J, double protection
- DIN EN 60747-5-2 (VDE 0884) available with option 1
- CSA 93751
- BSI IEC 60950; IEC 60065

ORDERING INFORMATION



AGENCY CERTIFIED/PACKAGE	CTR (%)	
	10 mA	
UL, CSA, BSI	63 to 125	100 to 200
DIP-6	SFH640-2	SFH640-3
SMD-6, option 7	SFH640-2X007	SFH640-3X007T ⁽¹⁾
VDE, UL, CSA, BSI	63 to 125	100 to 200
SMD-6, option 9	-	SFH640-3X019T ⁽¹⁾

Notes

- Additional options may be possible, please contact sales office.
- ⁽¹⁾ Also available in tubes, do not put T on the end.

ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		V_R	6.0	V
DC forward current		I_F	60	mA
Surge forward current	$t_p \leq 10\text{ }\mu\text{s}$	I_{FSM}	2.5	A
Total power dissipation		P_{diss}	100	mW



ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
OUTPUT				
Collector emitter voltage		V_{CEO}	300	V
Collector base voltage		V_{CBO}	300	V
Emitter base voltage		V_{EBO}	7.0	V
Collector current		I_C	50	mA
Surge collector current	$t_p \leq 10\text{ ms}$	I_C	100	mA
Total power dissipation		P_{diss}	300	mW
COUPLER				
Isolation test voltage between emitter and detector		V_{ISO}	5300	V_{RMS}
			7500	V_{PK}
Isolation resistance	$V_{IO} = 500\text{ V}, T_{amb} = 25\text{ }^{\circ}\text{C}$	R_{IO}	$\geq 10^{12}$	Ω
	$V_{IO} = 500\text{ V}, T_{amb} = 100\text{ }^{\circ}\text{C}$	R_{IO}	$\geq 10^{11}$	Ω
Insulation thickness between emitter and detector			≥ 0.4	mm
Creepage distance			≥ 7	mm
Clearance distance			≥ 7	mm
Comparative tracking index per DIN IEC 112/VDE 0303, part 1		CTI	175	
Storage temperature range		T_{stg}	- 55 to + 150	$^{\circ}\text{C}$
Operating temperature range		T_{amb}	- 55 to + 100	$^{\circ}\text{C}$
Soldering temperature ⁽¹⁾	max. 10 s, dip soldering: distance to seating plane $\geq 1.5\text{ mm}$	T_{sld}	260	$^{\circ}\text{C}$

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- ⁽¹⁾ Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	$I_F = 10\text{ mA}$		V_V		1.1	1.5	V
Reverse voltage	$I_R = 10\text{ }\mu\text{A}$		V_R	6			V
Reverse current	$V_R = 6\text{ V}$		I_R		0.01	10	μA
Capacitance	$V_F = 0\text{ V}, f = 1\text{ MHz}$		C_O		25		pF
Thermal resistance			R_{thja}		750		K/W
OUTPUT							
Collector emitter breakdown voltage	$I_{CE} = 1\text{ mA}, R_{BE} = 1\text{ M}\Omega$		BV_{CER}	300			V
Voltage emitter base	$I_{EB} = 10\text{ }\mu\text{A}$		BV_{EBO}	7			V
Collector emitter capacitance	$V_{CE} = 10\text{ V}, f = 1\text{ MHz}$		C_{CE}		7		pF
Collector base capacitance	$V_{CB} = 10\text{ V}, f = 1\text{ MHz}$		C_{CB}		8		pF
Emitter base capacitance	$V_{EB} = 5\text{ V}, f = 1\text{ MHz}$		C_{EB}		38		pF
Thermal resistance			R_{thja}		250		K/W
COUPLER							
Coupling capacitance			C_C		0.6		pF
Saturation voltage collector emitter	$I_F = 10\text{ mA}, I_C = 3.2\text{ mA}$	SFH640-2	V_{CEsat}		0.25	0.4	V
	$I_F = 10\text{ mA}, I_C = 5\text{ mA}$	SFH640-3	V_{CEsat}		0.25	0.4	V
Collector emitter leakage current	$V_{CE} = 200\text{ V}, R_{BE} = 1\text{ M}\Omega$		I_{CER}		1	100	nA

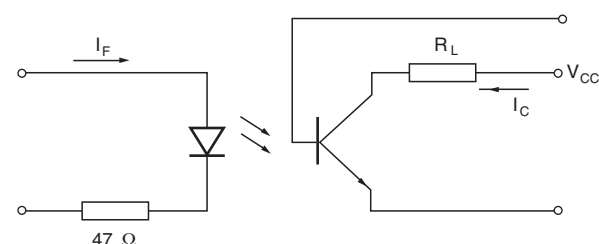
Note

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

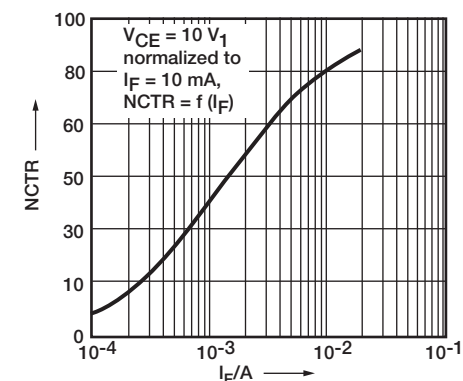
CURRENT TRANSFER RATIO ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio	$I_F = 10\text{ mA}$, $V_{CE} = 10\text{ V}$	SFH640-2	I_C/I_F	63		125	%
	$I_F = 1\text{ mA}$, $V_{CE} = 10\text{ V}$	SFH640-2	I_C/I_F	22	45		%
	$I_F = 10\text{ mA}$, $V_{CE} = 10\text{ V}$	SFH640-3	I_C/I_F	100		200	%
	$I_F = 1\text{ mA}$, $V_{CE} = 10\text{ V}$	SFH640-3	I_C/I_F	34	70		%

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	I _C = 2 mA, R _L = 100 Ω, V _{CC} = 10 V	t _{on}		5		μs
Rise time	I _C = 2 mA, R _L = 100 Ω, V _{CC} = 10 V	t _r		2.5		μs
Turn-off time	I _C = 2 mA, R _L = 100 Ω, V _{CC} = 10 V	t _{off}		6		μs
Fall time	I _C = 2 mA, R _L = 100 Ω, V _{CC} = 10 V	t _f		5.5		μs

TYPICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)



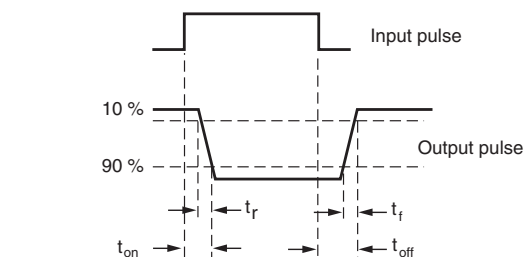
isfh640_01a



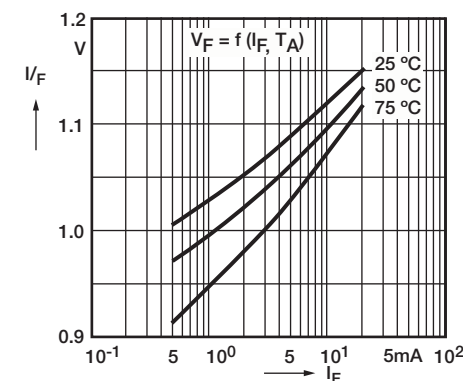
isfh640_02

Fig. 1 - Switching Times Measurement Test Circuit and Waveform

Fig. 3 - Current Transfer Ratio (typ.)



isfh640_01b



isfh640_03

Fig. 2 - Switching Times Measurement Test Circuit and Waveform

Fig. 4 - Diode Forward Voltage (typ.)

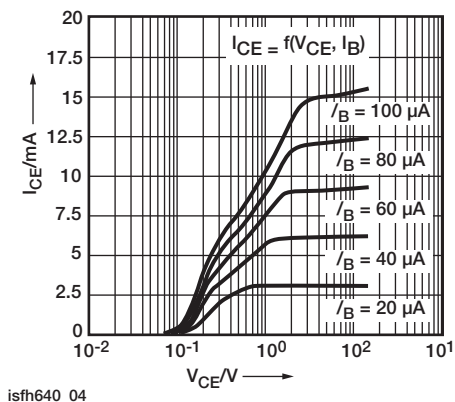


Fig. 5 - Output Characteristics (typ.)

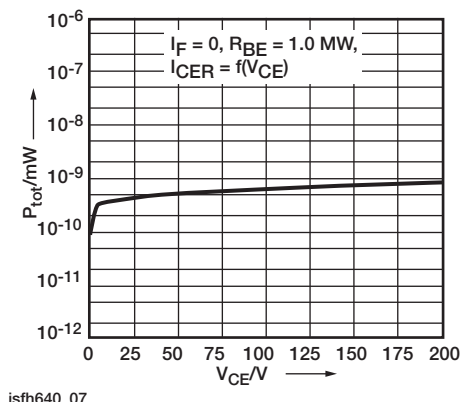


Fig. 8 - Collector-Emitter Leakage Current (typ.)

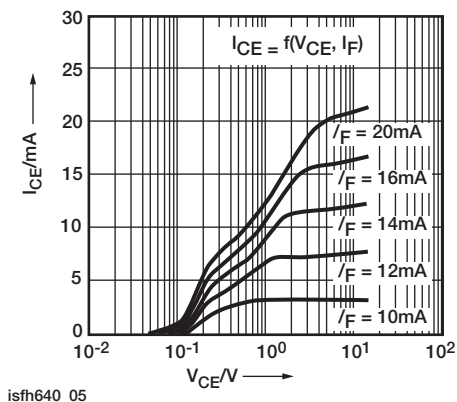


Fig. 6 - Output Characteristics (typ.)

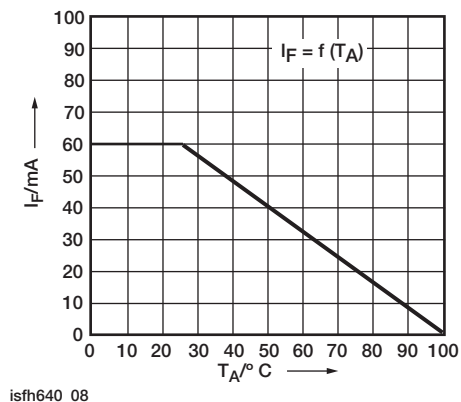


Fig. 9 - Permissible Loss Diode

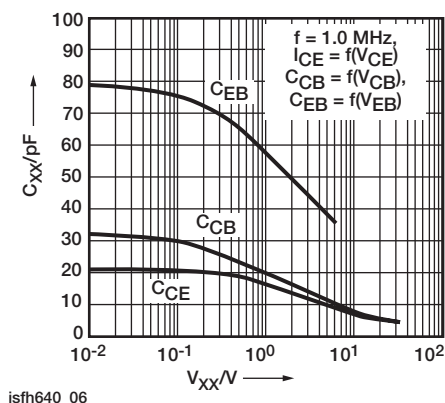


Fig. 7 - Transistor Capacitances (typ.)

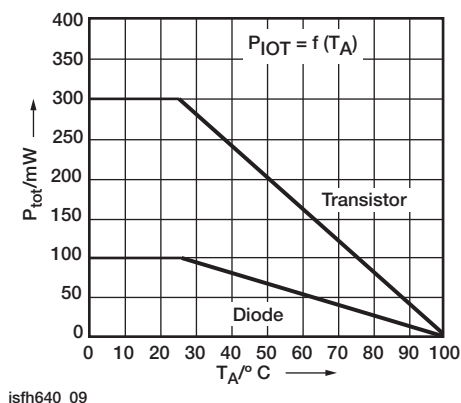
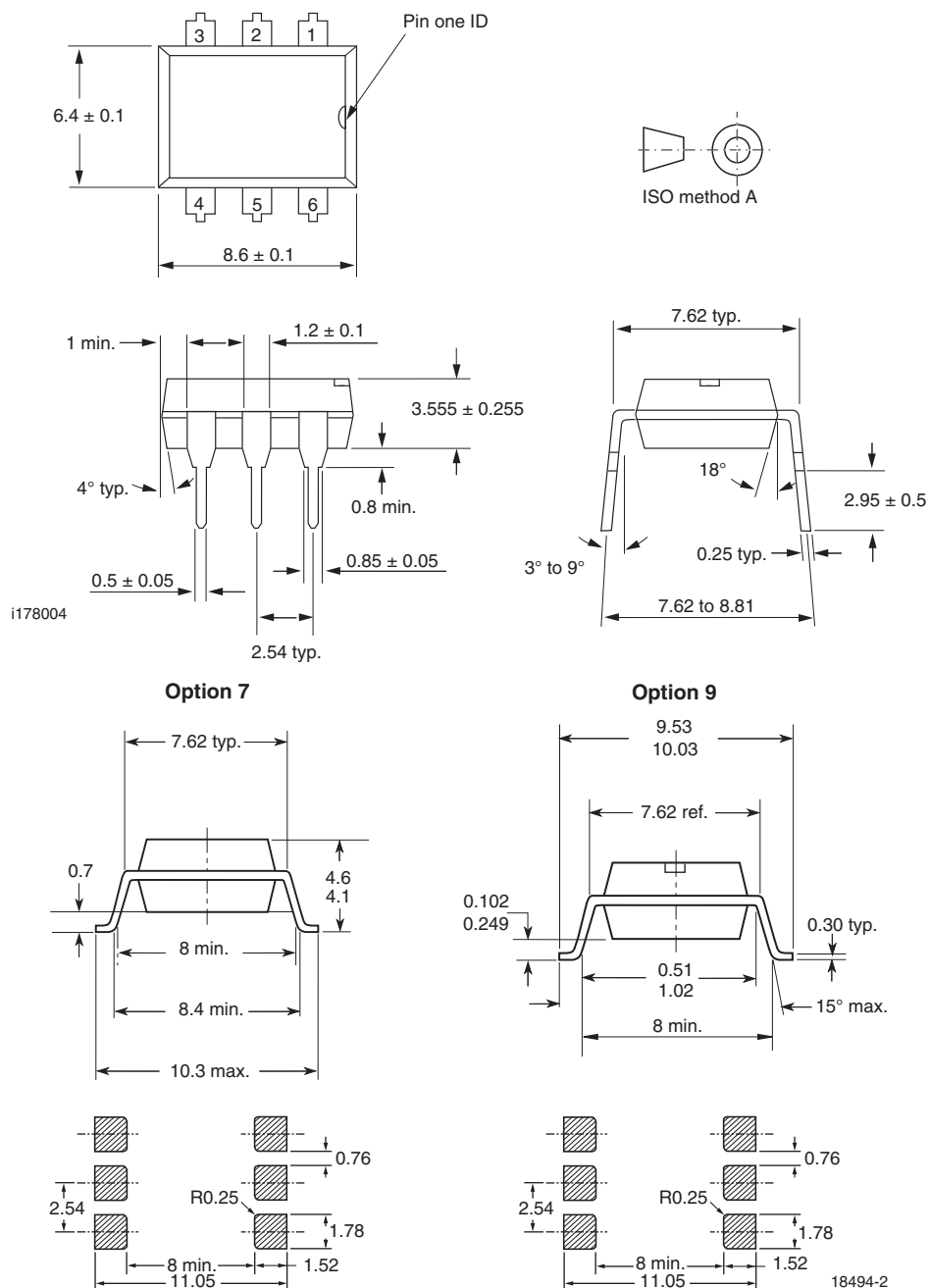
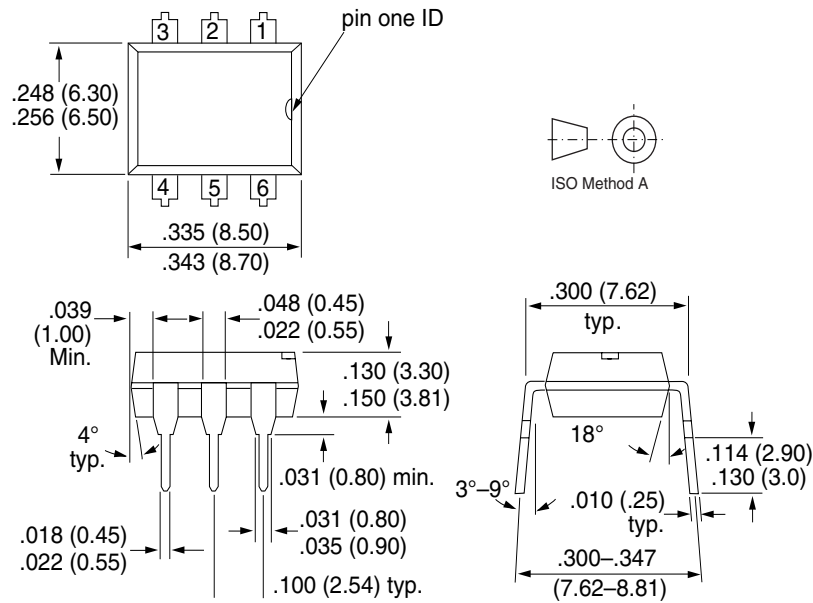


Fig. 10 - Permissible Power Dissipation

PACKAGE DIMENSIONS in millimeters


DIP-6A

Package Dimensions in Inches (mm)



i178004



Ozone Depleting Substances Policy Statement

It is the policy of **Vishay Semiconductor GmbH** to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

**We reserve the right to make changes to improve technical design
and may do so without further notice.**

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany
Telephone: 49 (0)7131 67 2831, Fax number: 49 (0)7131 67 2423



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