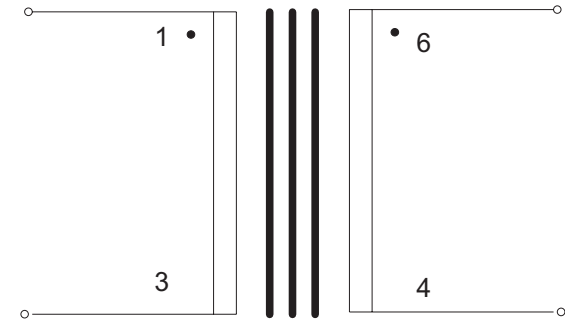


Y WW = date code

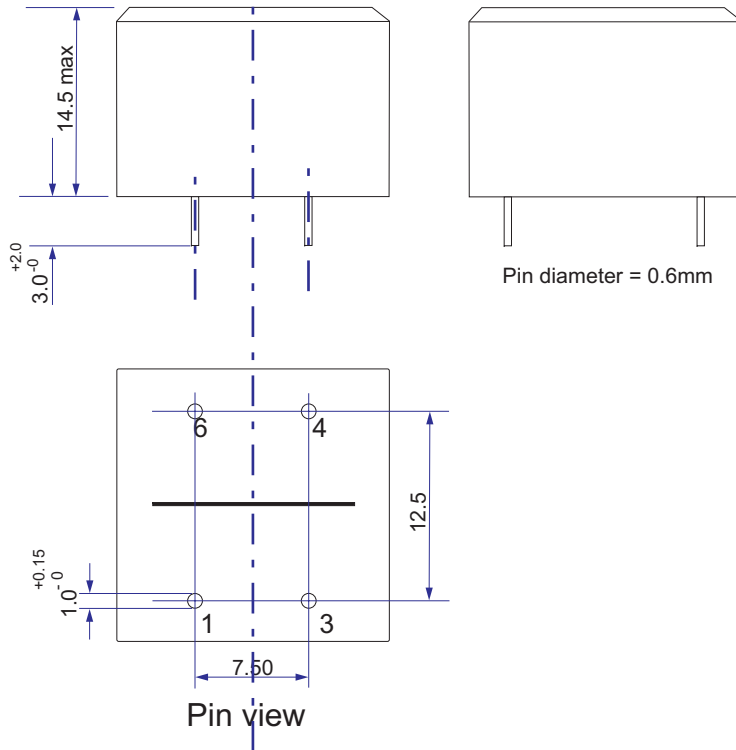


Electrical schematic

**Electrical Specification:**

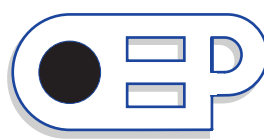
- Primary DC resistance:  $32.0\Omega$  (+/- 15%) (pins 1 - 3)
- Secondary DC resistance:  $42.0\Omega$  (+/-15%) (pins 6 - 4)
- Inductance, measured at 0.25V, 200Hz
- Primary: >1.9H
- Secondary: >1.9H
- Impedance: 600 ohms to 600 ohms
- Return loss (200Hz - 4kHz): >21dB
- Insertion loss @1kHz: 1.1dB (max.)
- Frequency response: 200Hz - 4kHz, +/- 0.4dB
- Third harmonic distortion @ -3dBm, 200Hz, -62dB (max.)
- Leakage inductance (@ 1kHz): 1.6mH (nom.)
- Hi-pot, primary to secondary: 3.75kV
- Storage temperature range: -25°C to +85°C

Do not pass DC through windings



Pin view

Tolerance on all dimensions +/-0.2mm unless stated otherwise



**Walters Group Holdings Ltd.**

Unit 5, Oxonian Park, Langford Locks,  
Kidlington, Oxfordshire. OX5 1FP  
Tel: (01865) 855085 Fax: (01865) 855075  
Website: www.oep.co.uk

DESCRIPTION	ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
600Ω line isolating transformer	1	26/08/05	CS		<b>Z1640E</b>

Scale: 2 to 1

All dimensions in mm unless stated otherwise