

LNA-IC for 2.1 GHz / 1.9 GHz Band Applications

FEATURES

- Low voltage operation +2.80 V typ.
- High gain 15.5 dB typ. fRX = 2132.5 MHz
15.5 dB typ. fRX = 1960 MHz
- Low noise figure 1.30 dB typ. fRX = 2132.5 MHz
1.30 dB typ. fRX = 1960 MHz
- Low distortion
(IIP3 +10 MHz offset)
+9.5 dBm typ. fRX = 2132.5 MHz
+10.0 dBm typ. fRX = 1960 MHz
- 5 pin Plastic Small Surface Mount Package (SMINI Type)

DESCRIPTION

AN26013A is LNA-IC for 1.9GHz -2.5 GHz Band Applications.

Realizing high performance by using 0.18 μm SiGeC Bi-CMOS process($f_T = 90 \text{ GHz}$, $f_{\text{max}} = 140 \text{ GHz}$).

Low Gain mode is available, controlled by integrated CMOS logic circuit.

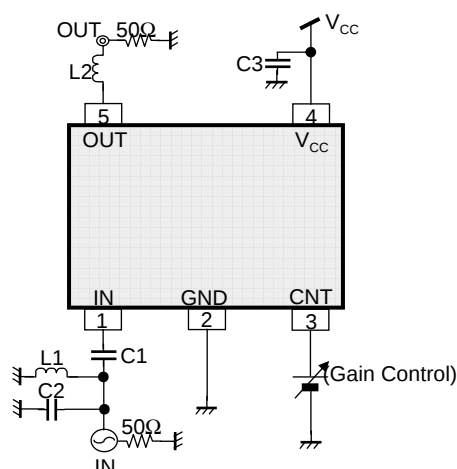
Achieving miniaturization by using small size package.

APPLICATIONS

- Cellular Phone 2.1GHz / 1.9GHz Band
- WLAN 2.4GHz
- WiMAX 2.5GHz

SIMPLIFIED APPLICATION

TOP VIEW



Components	Size	Value	Part Number	Vendor
L1	0603	3.0 nH	LQP03T3N0B04	Murata
L2	0603	5.6 nH	LQP03T5N6H04	Murata
C1	0603	1000 pF	GRM033B11C102KD01	Murata
C2	0603	2.0 pF	GJM0334C1E2R0BB01	Murata
C3	0603	0.1 μF	GRM033B30J104KE18	Murata

Notes) This application circuit is an example. The operation of mass production set is not guaranteed. You should perform enough evaluation and verification on the design of mass production set. You are fully responsible for the incorporation of the above application circuit and information in the design of your equipment.

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Rating	Unit	Note
Supply voltage	V_{CC}	3.6	V	*1
Supply current	I_{CC}	18	mA	—
Operating ambient temperature	T_{opr}	–20 to +70	°C	*2
Operating junction temperature	T_j	–40 to +125	°C	*2
Storage temperature	T_{stg}	–55 to +125	°C	*2
Input Voltage Range	IN (Pin No.1)	—	V	*3
	CNT (Pin No.3)	– 0.3 to ($V_{CC}+0.3$)	V	*4
	OUT (Pin No.5)	– 0.3 to ($V_{CC}+0.3$)	V	*4
ESD	HBM (Human Body Model)	2000	V	—

Notes). This product may sustain permanent damage if subjected to conditions higher than the above stated absolute maximum rating.

This rating is the maximum rating and device operating at this range is not guaranteeable as it is higher than our stated recommended operating range.

When subjected under the absolute maximum rating for a long time, the reliability of the product may be affected.

*1:The values under the condition not exceeding the above absolute maximum ratings and the power dissipation.

*2:Except for the operating ambient temperature, operating junction temperature and storage temperature, all ratings are for $T_a = 25^{\circ}\text{C}$.

*3:RF signal input pin. Do not apply DC. Do not apply more than 0 dBm to RF input.

*4:($V_{CC} + 0.3$) V must not be exceeded 3.6 V.

POWER DISSIPATION RATING

PACKAGE	θ_{JA}	PD ($T_a=25^{\circ}\text{C}$)	PD ($T_a=70^{\circ}\text{C}$)
SSMINI-5DC	833°C/W	0.12W	0.066W

Note). For the actual usage, please refer to the PD- T_a characteristics diagram in the package specification, supply voltage, load and ambient temperature conditions to ensure that there is enough margin follow the power and the thermal design does not exceed the allowable value.



CAUTION

Although this has limited built-in ESD protection circuit, but permanent damage may occur on it. Therefore, proper ESD precautions are recommended to avoid electrostatic damage to the MOS gates

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Supply voltage range	V_{CC}	2.65	2.8	2.95	V	*1

Note) *1: The values under the condition not exceeding the above absolute maximum ratings and the power dissipation

ELECTRICAL CHARACTERISTICS

Note) $V_{CC} = 2.80 \text{ V}$, $T_a = 25^\circ\text{C} \pm 2^\circ\text{C}$, unless otherwise specified.

Performance based on application circuit 1 on page 10.

Parameter	Symbol	Conditions	Limits			Unit	Note	
			Min	Typ	Max			
DC electrical characteristics								
Supply current (High Gain Mode)	IccHS	Vcc current at High Gain mode No input signal	—	10	13.5	mA	—	
Supply current (Low Gain Mode)	IccLS	Vcc current at Low Gain mode No input signal	—	1	10	μA	—	
CNT current (High Gain Mode)	IcntHS	Current at CNT Pin Voltage at CNT Pin = 2.80 V	—	2	35	μA	—	
CNT current (Low Gain Mode)	IcntLS	Current at CNT Pin Voltage at CNT Pin = 0 V	—	1	10	μA	—	
CNT voltage (High Gain Mode)	VIHS	—	2.52	—	Vcc + 0.3	V	—	
CNT voltage (Low Gain Mode)	VILS	—	−0.3	—	0.3	V	—	

ELECTRICAL CHARACTERISTICS (continued)

Note) $V_{CC} = 2.80\text{ V}$,

$T_a = 25^{\circ}\text{C} \pm 2^{\circ}\text{C}$, $f_{RX} = 2132.5\text{ MHz}$, $PRX = -30\text{ dBm}$, CW unless otherwise specified.

Performance based on application circuit 1 on page 10.

Parameter	Symbol	Conditions	Limits			Unit	Note	
			Min	Typ	Max			
LNA AC electrical characteristics (High Gain Mode)								
Power Gain HG	GHSa	—	13.5	15.5	17.5	dB	—	
IIP3 +10 MHz offset	IIP3Sa	f1 = fRXa + 10 MHz f2 = fRXa + 20 MHz Input 2 signals (f1, f2)	6.0	9.5	—	dBm	—	
LNA AC electrical characteristics (Low Gain Mode)								
Power Gain LG	GLSa	PRXa = −20 dBm	−9.5	−7.5	−5.5	dB	—	

APPLICATION INFORMATION

REFERENCE VALUES FOR DESIGN

Note) $V_{CC} = 2.80\text{ V}$

$T_a = 25^\circ\text{C} \pm 2^\circ\text{C}$, $f_{RXa} = 2110\text{ MHz}$, 2132.5 MHz , 2155 MHz , $PRXa = -30\text{ dBm}$, CW unless otherwise specified.

Performance based on application circuit 1 on page 10.

Parameter	Symbol	Conditions	Reference values			Unit	Note
			Min	Typ	Max		
LNA AC electrical characteristics (High Gain Mode)							
Power Gain HG	GHa	—	13.5	15.5	17.5	dB	*1
Noise Figure HG	NFHa	—	—	1.3	1.9	dB	*1
IIP3 +10 MHz offset HG	IIP3H1a	f1 = fRXa + 10 MHz f2 = fRXa + 20 MHz Input 2 signals (f1, f2)	6.0	9.5	—	dBm	*1
IIP3 –10 MHz offset HG	IIP3H2a	f1 = fRXa – 10 MHz f2 = fRXa – 20 MHz Input 2 signals (f1, f2)	6.0	9.5	—	dBm	*1
Input P1dB	IP1dBa	—	–15.0	–10.0	—	dBm	*1
Reverse Isolation HG	ISOHa	—	—	–30.0	–25.0	dB	*1
Input Return Loss HG	S11Ha	—	5.5	8.0	—	dB	*1
Output Return Loss HG	S22Ha	—	10.0	13.0	—	dB	*1
LNA AC electrical characteristics (Low Gain Mode)							
Power Gain LG	GLa	PRXa = – 20 dBm	–9.5	–7.5	–5.5	dB	*1
Noise Figure LG	NFLa	—	—	7.5	10.0	dB	*1
IIP3 +10 MHz offset LG	IIP3L1a	f1 = fRXa + 10 MHz f2 = fRXa + 20 MHz PRXa = –10 dBm Input 2 signals (f1, f2)	20.0	24.0	—	dBm	*1
Reverse Isolation LG	ISOLa	—	–9.5	–7.5	–5.5	dB	*1
Input Return Loss LG	S11La	—	4.0	6.0	—	dB	*1
Output Return Loss LG	S22La	—	11.0	15.0	—	dB	*1

Note) *1 : Checked by design, not production tested.

APPLICATION INFORMATION (continued)

REFERENCE VALUES FOR DESIGN (continued)

Note) $V_{cc} = 2.80\text{ V}$

$T_a = 25^\circ\text{C} \pm 2^\circ\text{C}$, $f_{RXb} = 1930\text{ MHz}$, 1960 MHz , 1990 MHz , $PRXb = -30\text{ dBm}$, CW unless otherwise specified.

Performance based on application circuit 2 on page 10.

Parameter	Symbol	Conditions	Reference values			Unit	Note
			Min	Typ	Max		
LNA AC electrical characteristics (High Gain Mode)							
Power Gain HG	GHb	—	13.5	15.5	17.5	dB	*1
Noise Figure HG	NFHb	—	—	1.3	1.9	dB	*1
IIP3 +10 MHz offset HG	IIP3H1b	f1 = fRXb + 10 MHz f2 = fRXb + 20 MHz Input 2 signals (f1, f2)	6.5	10.0	—	dBm	*1
IIP3 −10 MHz offset HG	IIP3H2b	f1 = fRXb − 10 MHz f2 = fRXb − 20 MHz Input 2 signals (f1, f2)	6.5	10.0	—	dBm	*1
Input P1dB	IP1dBHb	—	−15.0	−10.0	—	dBm	*1
Reverse Isolation HG	ISOHb	—	—	−30.0	−25.0	dB	*1
Input Return Loss HG	S11Hb	—	4.0	6.0	—	dB	*1
Output Return Loss HG	S22Hb	—	6.5	9.0	—	dB	*1
LNA AC electrical characteristics (Low Gain Mode)							
Power Gain LG	GLb	PRXb = −20 dBm	−9.5	−7.5	−5.5	dB	*1
Noise Figure LG	NFLb	—	—	7.5	10.0	dB	*1
IIP3 +10 MHz offset LG	IIP3L1b	f1 = fRXb + 10 MHz f2 = fRXb + 20 MHz PRXb = −10 dBm Input 2 signals (f1, f2)	20.0	24.0	—	dBm	*1
Reverse Isolation LG	ISOLb	—	−9.5	−7.5	−5.5	dB	*1
Input Return Loss LG	S11Lb	—	3.5	5.0	—	dB	*1
Output Return Loss LG	S22Lb	—	13.0	20.0	—	dB	*1

Note) *1 : Checked by design, not production tested.

APPLICATION INFORMATION (continued)

REFERENCE VALUES FOR DESIGN (continued)

Note) $V_{CC} = 2.65 \text{ V to } 2.95 \text{ V}$

$T_a = -20^\circ\text{C to } 70^\circ\text{C}$, CW unless otherwise specified.

Performance based on application circuit 1 on page 10.

Parameter	Symbol	Conditions	Reference values			Unit	Note
			Min	Typ	Max		
DC electrical characteristics							
Supply current (High Gain Mode)	I _{ccHT}	V _{CC} current at High Gain mode No input signal	—	10	14.5	mA	*1
Supply current (Low Gain Mode)	I _{ccLT}	V _{CC} current at Low Gain mode No input signal	—	1	12	μA	*1
CNT current (High Gain Mode)	I _{cntHT}	Current at CNT Pin Voltage at CNT Pin = 2.80 V	—	2	40	μA	*1
CNT current (Low Gain Mode)	I _{cntLT}	Current at CNT Pin Voltage at CNT Pin = 0 V	—	1	15	μA	*1

Note) *1 : Checked by design, not production tested.

APPLICATION INFORMATION (continued)

REFERENCE VALUES FOR DESIGN (continued)

Note) $V_{CC} = 2.65 \text{ V to } 2.95 \text{ V}$

$T_a = -20^\circ\text{C to } 70^\circ\text{C}$, $f_{RXa} = 2110\text{MHz, } 2132.5\text{MHz, } 2155\text{MHz}$, $PRXa = -30 \text{ dBm}$, CW unless otherwise specified.

Performance based on application circuit 1 on page 10.

Parameter	Symbol	Conditions	Reference values			Unit	Note
			Min	Typ	Max		
LNA AC electrical characteristics (High Gain Mode)							
Power Gain HG	GHTa	—	12.5	15.5	18.5	dB	*1
Noise Figure HG	NFHTa	—	—	1.3	2.2	dB	*1
IIP3 +10 MHz offset HG	IIP3H1Ta	f1 = fRXa + 10 MHz f2 = fRXa + 20 MHz Input 2 signals (f1, f2)	4.5	9.5	—	dBm	*1
IIP3 −10 MHz offset HG	IIP3H2Ta	f1 = fRXa − 10 MHz f2 = fRXa − 20 MHz Input 2 signals (f1, f2)	4.5	9.5	—	dBm	*1
Input P1dB	IP1dBHTa	—	−17.0	−10.0	—	dBm	*1
LNA AC electrical characteristics (Low Gain Mode)							
Power Gain LG	GLTa	PRXa = −20 dBm	−10.5	−7.5	−4.5	dB	*1
Noise Figure LG	NFLTa	—	—	7.5	11.5	dB	*1
IIP3 +10 MHz offset LG	IIP3L1Ta	f1 = fRXa + 10 MHz f2 = fRXa + 20 MHz PRXa = −10 dBm Input 2 signals (f1, f2)	18.0	24.0	—	dBm	*1

Note) *1 : Checked by design, not production tested.

APPLICATION INFORMATION (continued)

REFERENCE VALUES FOR DESIGN (continued)

Note) $V_{cc} = 2.65 \text{ V to } 2.95 \text{ V}$

$T_a = -20^\circ\text{C to } 70^\circ\text{C}$, $f_{RXb} = 1930\text{MHz, } 1960\text{MHz, } 1990\text{MHz}$, $PRXb = -30 \text{ dBm}$, CW unless otherwise specified.

Performance based on application circuit 2 on page 10.

Parameter	Symbol	Conditions	Reference values			Unit	Note	
			Min	Typ	Max			
LNA AC electrical characteristics (High Gain Mode)								
Power Gain HG	GHTb	—	12.5	15.5	18.5	dB	*1	
Noise Figure HG	NFHTb	—	—	1.3	2.2	dB	*1	
IIP3 +10 MHz offset HG	IIP3H1Tb	f1 = fRXb + 10 MHz f2 = fRXb + 20 MHz Input 2 signals (f1, f2)	5.0	10.0	—	dBm	*1	
IIP3 −10 MHz offset HG	IIP3H2Tb	f1 = fRXb − 10 MHz f2 = fRXb − 20 MHz Input 2 signals (f1, f2)	5.0	10.0	—	dBm	*1	
Input P1dB	IP1dBHTb	—	−17.0	−10.0	—	dBm	*1	
LNA AC electrical characteristics (Low Gain Mode)								
Power Gain LG	GLTb	PRXb = −20 dBm	−10.5	−7.5	−4.5	dB	*1	
Noise Figure LG	NFLTb	—	—	7.5	11.5	dB	*1	
IIP3 +10 MHz offset LG	IIP3LT1b	f1 = fRXb + 10 MHz f2 = fRXb + 20 MHz PRXb = −10 dBm Input 2 signals (f1, f2)	18.0	24.0	—	dBm	*1	

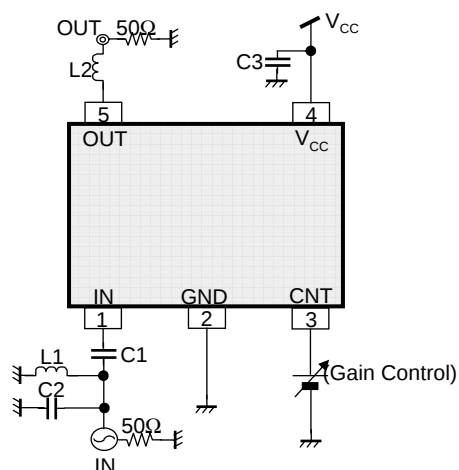
Note) *1 : Checked by design, not production tested.

APPLICATION INFORMATION (Continued)

APPLICATION CIRCUIT

[Application Circuit 1 for 2.1 GHz Band]

TOP VIEW

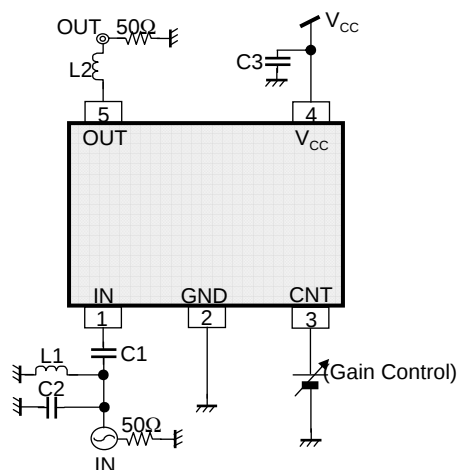


Components	Size	Value	Part Number	Vendor
L1	0603	3.0 nH	LQP03T3N0B04	Murata
L2	0603	5.6 nH	LQP03T5N6H04	Murata
C1	0603	1000 pF	GRM033B11C102KD01	Murata
C2	0603	2.0 pF	GJM0334C1E2R0BB01	Murata
C3	0603	0.1 μF	GRM033B30J104KE18	Murata

Notes) This application circuit is an example. The operation of mass production set is not guaranteed. You should perform enough evaluation and verification on the design of mass production set. You are fully responsible for the incorporation of the above application circuit and information in the design of your equipment.

[Application Circuit 2 for 1.9 GHz Band]

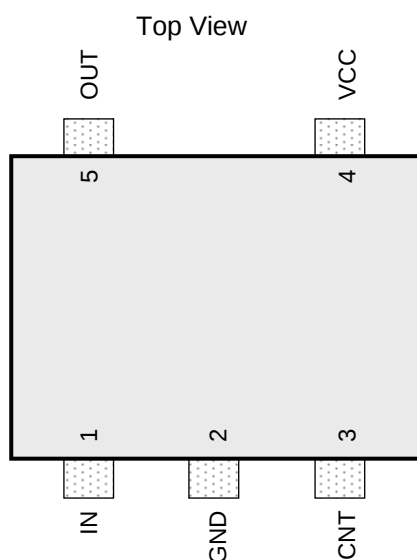
TOP VIEW



Components	Size	Value	Part Number	Vendor
L1	0603	3.6 nH	LQP03T6N0B04	Murata
L2	0603	5.6 nH	LQP03T5N6H04	Murata
C1	0603	1000 pF	GRM033B11C102KD01	Murata
C2	0603	2.0 pF	GJM0334C1E2R0BB01	Murata
C3	0603	0.1 μF	GRM033B30J104KE18	Murata

Notes) This application circuit is an example. The operation of mass production set is not guaranteed. You should perform enough evaluation and verification on the design of mass production set. You are fully responsible for the incorporation of the above application circuit and information in the design of your equipment.

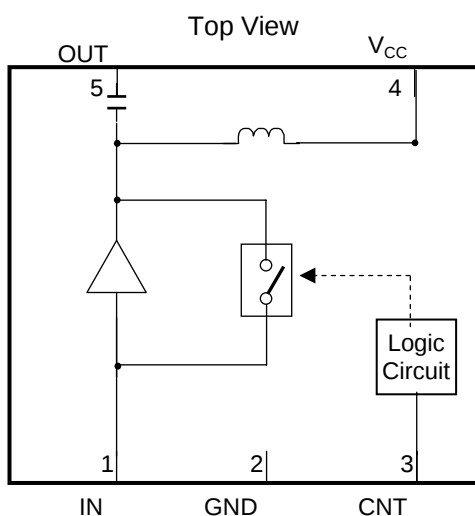
PIN CONFIGURATION



PIN FUNCTIONS

Pin No.	Pin name	Type	Description
1	IN	Input	RF Input
2	GND	Ground	GND
3	CNT	Input	High Gain / Low Gain switch L: Low Gain Mode H: High Gain Mode
4	VCC	Power Supply	V_{CC}
5	OUT	Output	RF Output

FUNCTIONAL BLOCK DIAGRAM

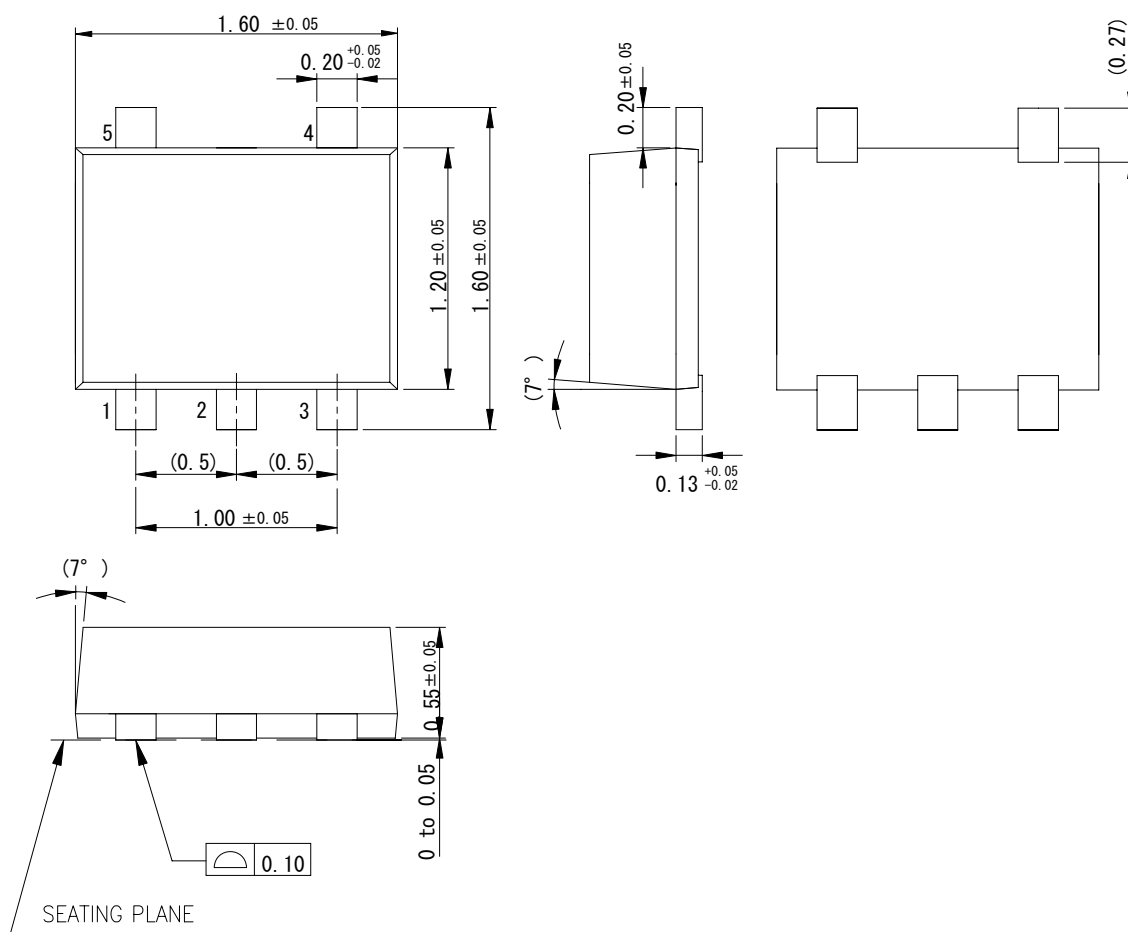


Notes) This circuit and these circuit constants show an example and do not guarantee the design as a mass-production set.
This block diagram is for explaining functions. The part of the block diagram may be omitted, or it may be simplified.

PACKAGE INFORMATION (Reference Data)

Package Code:SSMINI-5DC

Unit:mm



Body Material	: Br / Sb Free Epoxy Resin
Lead Material	: Cu Alloy
Lead Finish Method	: SnBi Plating

IMPORTANT NOTICE

1. The products and product specifications described in this book are subject to change without notice for modification and/or improvement. At the final stage of your design, purchasing, or use of the products, therefore, ask for the most up-to-date Product Standards in advance to make sure that the latest specifications satisfy your requirements.
2. When using the LSI for new models, verify the safety including the long-term reliability for each product.
3. When the application system is designed by using this LSI, be sure to confirm notes in this book.
Be sure to read the notes to descriptions and the usage notes in the book.
4. The technical information described in this book is intended only to show the main characteristics and application circuit examples of the products. No license is granted in and to any intellectual property right or other right owned by Panasonic Corporation or any other company. Therefore, no responsibility is assumed by our company as to the infringement upon any such right owned by any other company which may arise as a result of the use of technical information de-scribed in this book.
5. This book may be not reprinted or reproduced whether wholly or partially, without the prior written permission of our company.
6. This LSI is intended to be used for general electronic equipment [2.1 GHz Band / 1.9 GHz Band Applications].
Consult our sales staff in advance for information on the following applications: Special applications in which exceptional quality and reliability are required, or if the failure or malfunction of this LSI may directly jeopardize life or harm the human body.
Any applications other than the standard applications intended.
 - (1) Space appliance (such as artificial satellite, and rocket)
 - (2) Traffic control equipment (such as for automobile, airplane, train, and ship)
 - (3) Medical equipment for life support
 - (4) Submarine transponder
 - (5) Control equipment for power plant
 - (6) Disaster prevention and security device
 - (7) Weapon
 - (8) Others : Applications of which reliability equivalent to (1) to (7) is required
 It is to be understood that our company shall not be held responsible for any damage incurred as a result of or in connection with your using the LSI described in this book for any special application, unless our company agrees to your using the LSI in this book for any special application.
7. This LSI is neither designed nor intended for use in automotive applications or environments unless the specific product is designated by our company as compliant with the ISO/TS 16949 requirements.
Our company shall not be held responsible for any damage incurred by you or any third party as a result of or in connection with your using the LSI in automotive application, unless our company agrees to your using the LSI in this book for such application.
8. If any of the products or technical information described in this book is to be exported or provided to non-residents, the laws and regulations of the exporting country, especially, those with regard to security export control, must be observed.
9. Please use this product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. Our company shall not be held responsible for any damage incurred as a result of your using the LSI not complying with the applicable laws and regulations.

USAGE NOTES

1. When designing your equipment, comply with the range of absolute maximum rating and the guaranteed operating conditions (operating power supply voltage and operating environment etc.). Especially, please be careful not to exceed the range of absolute maximum rating on the transient state, such as power-on, power-off and mode-switching. Otherwise, we will not be liable for any defect which may arise later in your equipment.

Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.

2. Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.
3. Pay attention to the direction of LSI. When mounting it in the wrong direction onto the PCB (printed-circuit-board), it might smoke or ignite.
4. Pay attention in the PCB (printed-circuit-board) pattern layout in order to prevent damage due to short circuit between pins. In addition, refer to the Pin Description for the pin configuration.
5. Perform a visual inspection on the PCB before applying power, otherwise damage might happen due to problems such as a solder-bridge between the pins of the semiconductor device. Also, perform a full technical verification on the assembly quality, because the same damage possibly can happen due to conductive substances, such as solder ball, that adhere to the LSI during transportation.
6. Take notice in the use of this product that it might break or occasionally smoke when an abnormal state occurs such as output pin-VCC short (Power supply fault), output pin-GND short (Ground fault), or output-to-output-pin short (load short) .

And, safety measures such as an installation of fuses are recommended because the extent of the above-mentioned damage and smoke emission will depend on the current capability of the power supply.

**Request for your special attention and precautions in using the technical information and
semiconductors described in this book**

- (1) If any of the products or technical information described in this book is to be exported or provided to non-residents, the laws and regulations of the exporting country, especially, those with regard to security export control, must be observed.
- (2) The technical information described in this book is intended only to show the main characteristics and application circuit examples of the products. No license is granted in and to any intellectual property right or other right owned by Panasonic Corporation or any other company. Therefore, no responsibility is assumed by our company as to the infringement upon any such right owned by any other company which may arise as a result of the use of technical information described in this book.
- (3) The products described in this book are intended to be used for general applications (such as office equipment, communications equipment, measuring instruments and household appliances), or for specific applications as expressly stated in this book.
Consult our sales staff in advance for information on the following applications:
 - Special applications (such as for airplanes, aerospace, automotive equipment, traffic signaling equipment, combustion equipment, life support systems and safety devices) in which exceptional quality and reliability are required, or if the failure or malfunction of the products may directly jeopardize life or harm the human body.It is to be understood that our company shall not be held responsible for any damage incurred as a result of or in connection with your using the products described in this book for any special application, unless our company agrees to your using the products in this book for any special application.
- (4) The products and product specifications described in this book are subject to change without notice for modification and/or improvement. At the final stage of your design, purchasing, or use of the products, therefore, ask for the most up-to-date Product Standards in advance to make sure that the latest specifications satisfy your requirements.
- (5) When designing your equipment, comply with the range of absolute maximum rating and the guaranteed operating conditions (operating power supply voltage and operating environment etc.). Especially, please be careful not to exceed the range of absolute maximum rating on the transient state, such as power-on, power-off and mode-switching. Otherwise, we will not be liable for any defect which may arise later in your equipment.
Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.
- (6) Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.
- (7) This book may be not reprinted or reproduced whether wholly or partially, without the prior written permission of our company.