



## WISMC02BI Application Note

### Pin Assignment Compatibility

#### Overview

This document outlines the compatibility requirements when designing systems to use the BISM format Wireless LAN Module alongside or BISM II Bluetooth Intelligent Serial Module when designing systems that can optionally use either a BISM or a WISM on the same connector.

#### BISM / WISM Compatibility

The BISM and WISM modules are designed to be electrically compatible at the 40 way Hirose connector. What this means in practice is that common functions (such as the GPIO and UART) will use the same pins in the same way, and where functions are unique to either WISM or BISM (such as the PCM bus on the BISM), then the signal pins are shared in a way that will not cause any conflict.

The pinouts for the two modules are shown in the table below for comparison:

Pin No:	BISM Module		WISM Module	
	Name	Input / Output*	Name	Input / Output
1	Analogue 0	Input	Analogue 0	Input
2	GPIO1	Input / Output	GPIO1	Input / Output
3	Analogue 1	Input	Analogue 1	
4	GPIO2	Input / Output	GPIO2	Input / Output
5	SPI_MISO	Output	WLAN_ACTIVE	Output
6	UART_RI	Output	UART_RI	Output
7	SPI_CSB	Input	N/C	
8	UART_DCD	Output	UART_DCD	Output
9	SPI_CLK	Input	N/C	
10	UART_DSR	Input	UART_DSR	Input
11	GND		GND	
12	UART_DTR	Output	UART_DTR	Output
13	RESET	Input	RESET	
14	GPIO4	Input / Output	GPIO4	Input / Output

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15	GND		GND	
16	GPIO5	Input / Output	GPIO5	Input / Output
17	SPI_MOSI	Input	N/C	
18	GND		GND	
19	UART_CTS	Input	UART_CTS	Input
20	PCM_CLK	Input	IRQ0	Input
21	UART_TX	Output	UART_TX	Output
22	PCM_IN	Input	WAKEUP	Input
23	UART_RTS	Output	UART_RTS	Output
24	PCM_SYNC	Input	BT_PRIORITY	Input
25	UART_RX	Input	UART_RX	Input
26	PCM_OUT	Output	N/C	
27	VCC_3V3	Output	VCC_3V0	Output
28	N/C		VCC_5V	Input
29	VCC_5V	Input	VCC_5V	Input
30	GND		GND	
31	N/C		VCC_5V	Input
32	USB / RESERVED		N/C	
33	GPIO6	Input / Output	GPIO6	Input / Output
34	USB / RESERVED		N/C	
35	GPIO7	Input / Output	GPIO7	Input / Output
36	GND		GND	
37	GPIO8	Input / Output	GPIO8	Input / Output
38	GND		GND	
39	GPIO9	Input / Output	GPIO9	Input / Output
40	N/C		BT_STATE	Input

<sup>I/O</sup> direction shown is default module configuration. Some pins have programmable direction.

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