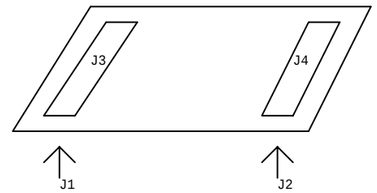
STK600 RC014T-12 for DIP40 socket

J3 and J4 mates up to the socket card, while J1 and J2 goes down to the mother board. J1 is physical placed below J3, and J2 below J4.



VTG — VCC_P1 — C2 100n — GND — Place close to J3, pin3

L2 BLM21AG102SN1D — Place close to J3, pin3 — C9 100n Not Used — GND

L1 BLM21AG102SN1D — Place close to J3, pin26 — C1 100n Not Used — GND

L3 BLM21AG102SN1D — Place close to J4, pin28 — C11 100n Not Used — GND

Not Used — C14 100n — GND — Place close to j4, pin6

Not Used — C15 100n — GND — Place close to J4, pin15

Not Used — C3 100n — GND — Place close to J3, pin23

Not Used — C7 100n — GND — Place close to J3, pin39

Not Used — C8 100n — GND — Place close to J3, pin65

J3

SOCKET PIN 1				SOCKET PIN 40
GND	1	2		GND
VCC_P1	3	4		GND
XTAL1	5	6		AREF0
XTAL2	7	8		PA1
PB3	9	10		PA2
GND	11	12		GND
PB2	13	14		PA3
PA7	15	16		PA4
PA6	17	18		PA5
	19	20		
SOCKET PIN 7	21	22		SOCKET PIN 34
GND	23	24		GND
	25	26		
	27	28		
	29	30		
GND	31	32		GND
	33	34		
	35	36		
	37	38		
	39	40		
GND	41	42		GND
	43	44		
	45	46		
	47	48		
	49	50		
GND	51	52		GND
	53	54		
	55	56		
	57	58		
	59	60		
GND	61	62		GND
	63	64		
	65	66		
	67	68		
	69	70		
GND	71	72		GND
	73	74		
	75	76		
	77	78		
	79	80		

Mating FSI-140

C5 100n — GND — Place close to J3, pin8

Not Used — C6 1u — GND — Place close to J3, pin4

Not Used — C4 100n — GND — Place close to J3, pin24

Not Used — C26 100n — GND — Place close to J3, pin12

Not Used — C12 100n — GND — Place close to J3, pin54

Not Used — C19 100n — GND — Place close to J3, pin38

Not Used — C16 100n — GND — Place close to J3, pin72

Not Used — C18 100n — GND — Place close to J3, pin50

J4

GND	1	2	GND
	3	4	
	5	6	
	7	8	
	9	10	
GND	11	12	GND
	13	14	
	15	16	
	17	18	
	19	20	
GND	21	22	GND
	23	24	
	25	26	
	27	28	
	29	30	
GND	31	32	GND
	33	34	
	35	36	
	37	38	
	39	40	
GND	41	42	GND
	43	44	
	45	46	
	47	48	
	49	50	
GND	51	52	GND
	53	54	
	55	56	
	57	58	
	59	60	
GND	61	62	GND
	63	64	
	65	66	
	67	68	
	69	70	
GND	71	72	GND
GND	75	76	VCC
BOARD_ID0	77	78	BOARD_ID1
BOARD_ID4	79	80	BOARD_ID5

Mating FSI-140

Not Used — C17 100n — GND — Place close to J4, pin26

Not Used — C20 100n — GND — Place close to J4, pin56

Not Used — C22 100n — GND — Place close to J4, pin35

Not Used — C10 100n — GND — Place close to J4, pin55

Not Used — C21 100n — GND — Place close to J4, pin66

J1

GND	1	2	VTG
AREF0	3	4	PA1
PA2	5	6	PA3
PA4	7	8	PA5
PA6	9	10	PA7
GND	11	12	VTG
XTAL1	13	14	XTAL2
PB2	15	16	PB3
	17	18	
	19	20	
GND	21	22	VTG
	23	24	
	25	26	
	27	28	
	29	30	
GND	31	32	VTG
	33	34	
	35	36	
	37	38	
	39	40	
GND	41	42	VTG
	43	44	
	45	46	
	47	48	
	49	50	
GND	51	52	VTG
	53	54	
	55	56	
	57	58	
	59	60	
GND	61	62	VTG
	63	64	
	65	66	PG5
	67	68	
	69	70	
GND	71	72	VTG
	73	74	
	75	76	
	77	78	
	79	80	
GND	81	82	VTG
XTAL1	83	84	AREF0 AREF0
XTAL2	85	86	AREF1
GND	87	88	MOSI PA6
	89	90	MISO PA5
	91	92	SCK PA4
PB3	93	94	TDI
	95	96	TDO
GND	97	98	TMS
VCC	99	100	TCK

Mating FSI-150

J2

GND	1	2	VTG
	3	4	
	5	6	
	7	8	
	9	10	
GND	11	12	VTG
	13	14	
	15	16	
	17	18	
	19	20	
GND	21	22	VTG
	23	24	
	25	26	
	27	28	
	29	30	
GND	31	32	VTG
	33	34	
	35	36	
	37	38	
	39	40	
GND	41	42	VTG
	43	44	
	45	46	
	47	48	
	49	50	
GND	51	52	VTG
	53	54	
	55	56	
	57	58	
	59	60	
GND	61	62	VTG
	63	64	
	65	66	
	67	68	
	69	70	
GND	71	72	VBUS
DP	73	74	UVCon
DN	75	76	VCC
IUID	77	78	
GND	79	80	PDATA1 PA5
PA6 PDATA0	81	82	PDATA3 AREF0
PA4 PDATA2	83	84	PDATA5 PA2
PA1 PDATA4	85	86	PDATA7
PDATA6	87	88	PCTRL1
PCTRL0	89	90	PCTRL3
PCTRL2	91	92	PCTRL5
PCTRL4	93	94	PCTRL7
PCTRL6 A	95	96 B	BOARD_ID1
BOARD_ID0	97	98	BOARD_ID3
BOARD_ID2	99	100	BOARD_ID5
BOARD_ID4			

Mating FSI-150

Board id 0x1C, id2: /(A+B) id3: B

E5 PCB Clip Hole — E7 PCB Clip Hole — E4 PCB Clip Hole — E6 PCB Clip Hole — GND

E1 FIDUCIAL — E2 FIDUCIAL — E3 FIDUCIAL

PCB1 — A0607.3.1212.A

Not Used — NC_U6 — U6 NC7SZ58P6X — C24 100n — GND

Not Used — NC_U5 — U5 NC7SZ58P6X — C13 100n — GND

Not Used — NC_U3 — U3 NC7SZ57P6X — C25 100n — GND

VCC — C23 100n — GND

NOR — U4 NC7SZ57P6X — /(A+B)

<Variant Name>

ATMEL Norway AS Vestre Rosten 79 N-7075 TILLER Norway			
Title	STK600 RouterCard RC014T-12		
Size A3	Document Number <Doc>		Rev A
Date: Thursday, December 21, 2006		Sheet 1 of 1	