

LM2903, LM393/LM393A, LM293A

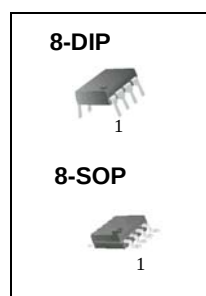
Dual Differential Comparator

Features

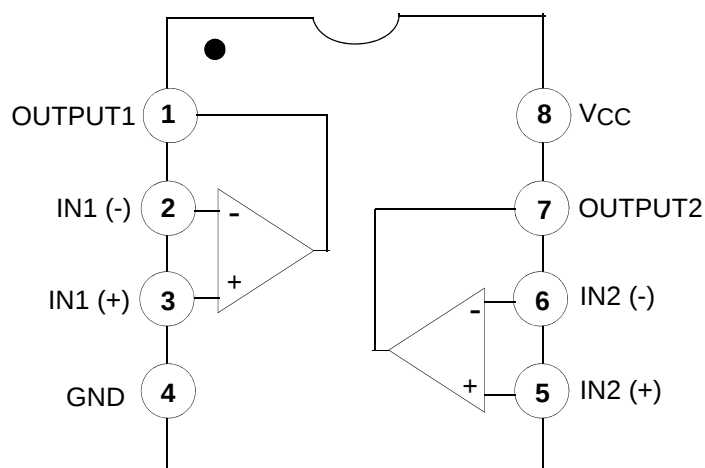
- Single Supply Operation: 2V to 36V
- Dual Supply Operation: $\pm 1\text{V}$ to $\pm 18\text{V}$
- Allow Comparison of Voltages Near Ground Potential
- Low Current Drain $800\mu\text{A}$ Typ.
- Compatible with all Forms of Logic
- Low Input Bias Current 25nA Typ.
- Low Input Offset Current $\pm 5\text{nA}$ Typ.
- Low Offset Voltage $\pm 1\text{mV}$ Typ.

Description

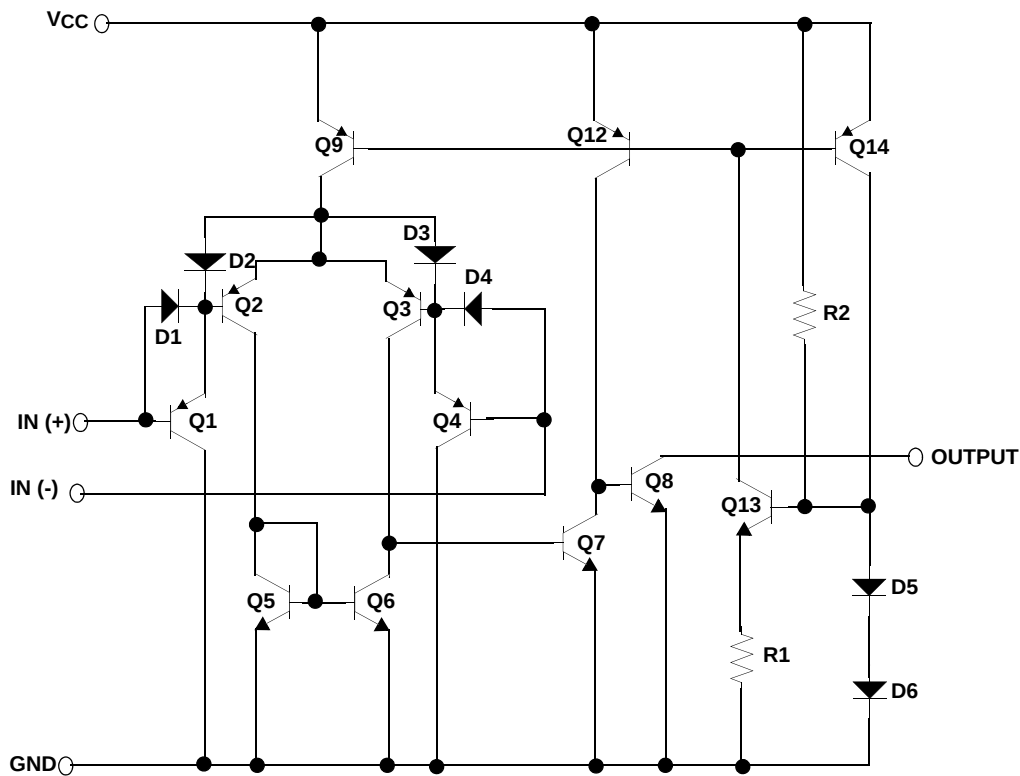
The LM2903, LM393/LM393A, LM293A consist of two independent voltage comparators designed to operate from a single power supply over a wide voltage range.



Internal Block Diagram



Schematic Diagram



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Power Supply Voltage	VCC	±18 or 36	V
Differential Input Voltage	VI(DIFF)	36	V
Input Voltage	VI	-0.3 to +36	V
Output Short Circuit to GND	-	Continuous	-
Power Dissipation, Ta = 25°C 8-DIP 8-SOP	PD	1040 480	mW
Operating Temperature LM393/LM393A LM2903 LM293A	TOPR	0 ~ +70 -40 ~ +105 -25 ~ +85	°C
Storage Temperature	TSTG	-65 ~ +150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-Ambient Max. 8-DIP 8-SOP	Rθja	120 260	°C/W

Electrical Characteristics

($V_{CC} = 5V$, $T_A = 25^\circ C$, unless otherwise specified)

Parameter	Symbol	Conditions	LM293A/LM393A			LM393			Unit
			Min.	Typ.	Max.	Min.	Typ.	Max.	
Input Offset Voltage	V_{IO}	$V_{O(P)} = 1.4V$, $R_S = 0\Omega$	-	± 1	± 2	-	± 1	± 5	mV
		$V_{CM} = 0$ to $1.5V$ Note1	-	-	± 4.0	-	-	± 9.0	
Input Offset Current	I_{IO}		-	± 5	± 50	-	± 5	± 50	nA
		Note1	-	-	± 150	-	-	± 150	
Input Bias Current	I_{BIAS}		-	65	250	-	65	250	nA
		Note1	-	-	400	-	-	400	
Input Common Mode Voltage Range	$V_{I(R)}$		0	-	$V_{CC} - 1.5$	0	-	$V_{CC} - 1.5$	V
		Note1	0	-	$V_{CC} - 2$	0	-	$V_{CC} - 2$	
Supply Current	I_{CC}	$R_L = \infty$, $V_{CC} = 5V$	-	0.6	1	-	0.6	1	mA
		$R_L = \infty$, $V_{CC} = 30V$	-	0.8	2.5	-	0.8	2.5	
Voltage Gain	G_V	$V_{CC} = 15V$, $R_L \geq 15k\Omega$ (for large $V_{O(P-P)swing}$)	50	200	-	50	200	-	V/mV
Large Signal Response Time	T_{LRES}	$V_I = \text{TTL Logic Swing}$ $V_{REF} = 1.4V$, $V_{RL} = 5V$, $R_L = 5.1k\Omega$	-	350	-	-	350	-	nS
Response Time	T_{RES}	$V_{RL} = 5V$, $R_L = 5.1k\Omega$	-	1.4	-	-	1.4	-	μS
Output Sink Current	I_{SINK}	$V_{I(-)} \geq 1V$, $V_{I(+)} = 0V$, $V_{O(P)} \leq 1.5V$	6	18	-	6	18	-	mA
Output Saturation Voltage	V_{SAT}	$V_{I(-)} \geq 1V$, $V_{I(+)} = 0V$	-	160	400	-	160	400	mV
		$I_{SINK} = 4mA$ Note1	-	-	700	-	-	700	
Output Leakage Current	$I_{O(LKG)}$	$V_{I(-)} = 0V$, $V_{I(+)} = 1V$							
		$V_{O(P)} = 5V$	-	0.1	-	-	0.1	-	nA
		$V_{O(P)} = 30V$	-	-	1.0	-	-	1.0	μA

Note1

LM393/LM393A: $0 \leq T_A \leq +70^\circ C$

LM2903: $-40 \leq T_A \leq +105^\circ C$

LM293A: $-25 \leq T_A \leq +85^\circ C$

Electrical Characteristics (Continued)(V_{CC} = 5V, T_A = 25°C, unless otherwise specified)

Parameter	Symbol	Conditions		LM2903			Unit
				Min.	Typ.	Max.	
Input Offset Voltage	V _{IO}	V _{O(P)} = 1.4V, R _S = 0Ω		-	±1	±7	mV
		V _{CM} = 0 to 1.5V	Note1	-	±9	±15	
Input Offset Current	I _{IO}			-	±5	±50	nA
			Note1	-	±50	±200	
Input Bias Current	I _{BIAS}			-	65	250	nA
			Note1	-	-	500	
Input Common Mode Voltage Range	V _{I(R)}			0	-	V _{CC} - 1.5	V
			Note1	0	-	V _{CC} - 2	
Supply Current	I _{CC}	R _L = ∞, V _{CC} = 5V		-	0.6	1	mA
		R _L = ∞, V _{CC} = 30V		-	1	2.5	
Voltage Gain	GV	V _{CC} = 15V, R _L ≥ 15kΩ (for large V _{O(P-P)} swing)		25	100	-	V/mV
Large Signal Response Time	T _{LRES}	V _I = TTL Logic Swing V _{REF} = 1.4V, V _R = 5V, R _L = 5.1kΩ		-	350	-	nS
Response Time	T _{RES}	V _R = 5V, R _L = 5.1kΩ		-	1.5	-	μS
Output Sink Current	I _{SINK}	V _{I(-)} ≥ 1V, V _{I(+)} = 0V, V _{O(P)} ≤ 1.5V		6	16	-	mA
Output Saturation Voltage	V _{SAT}	V _{I(-)} ≥ 1V, V _{I(+)} = 0V		-	160	400	mV
		I _{SINK} = 4mA	Note1	-	-	700	
Output Leakage Current	I _{O(LKG)}	V _{I(-)} = 0V,	V _{O(P)} = 5V	-	0.1	-	nA
		V _{I(+)} = 1V	V _{O(P)} = 30V	-	-	1.0	μA

Note1LM393/LM393A: 0 ≤ T_A ≤ +70°CLM2903: -40 ≤ T_A ≤ +105°CLM293A: -25 ≤ T_A ≤ +85°C

Typical Performance Characteristics

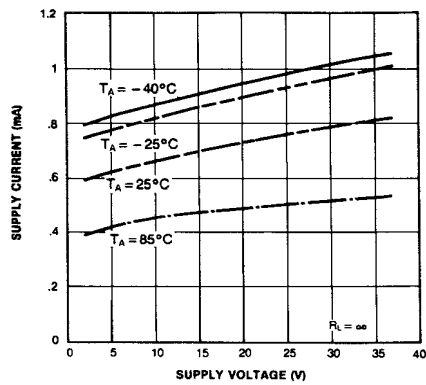


Figure 1. Supply Current vs Supply Voltage

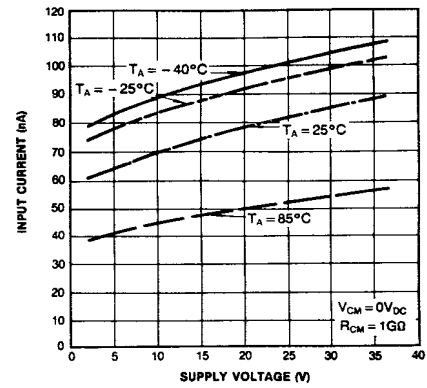


Figure 2. Input Current vs Supply Voltage

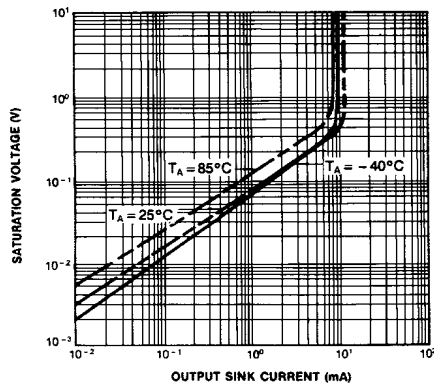


Figure 3. Output Saturation Voltage vs Sink Current

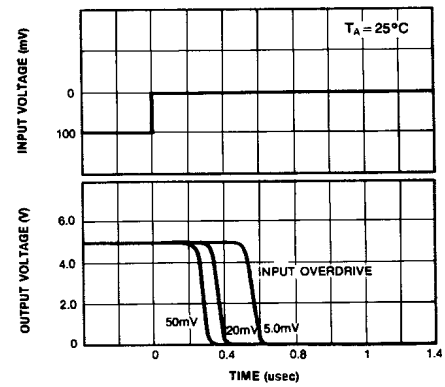


Figure 4. Response Time for Various Input Overdrive-Negative Transition

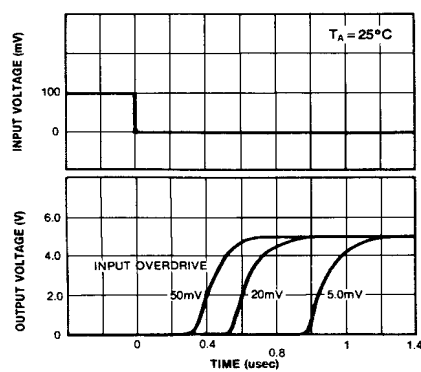


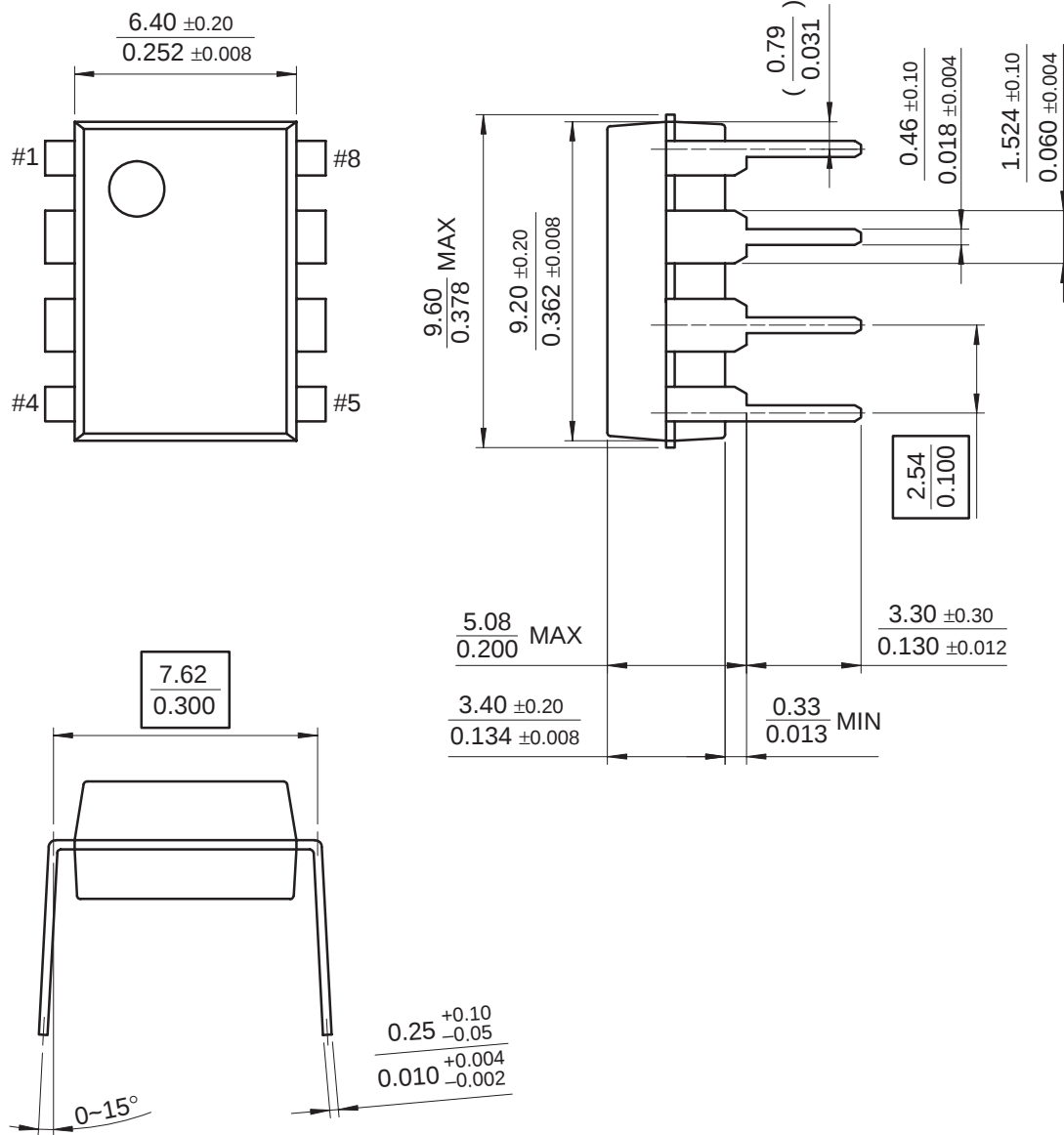
Figure 5. Response Time for Various Input Overdrive-Positive Transition

Mechanical Dimensions

Package

Dimensions in millimeters

8-DIP



Ordering Information

Product Number	Package	Operating Temperature
LM393N	8-DIP	0 ~ +70°C
LM393AN		
LM393M	8-SOP	
LM393AM		
LM2903N	8-DIP	-40 ~ +105°C
LM2903M	8-SOP	
LM293AN	8-DIP	-25 ~ +85°C
LM293AM	8-SOP	

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